800 float	_FREQ	Hz	0
802 float	_N	V	0
804 float	_G	V	0
806 float	_M	V	0
808 float	_ULN[0]	V	0
810 float	_ULN[1]	V	0
812 float	_ULN[2]	V	0
814 float	_ULL[0]	V	0
816 float	_ULL[1]	V	0
818 float	_ULL[2]	V	0
820 float	_COS_PHI[0]		0
822 float	_COS_PHI[1]		0
824 float	_COS_PHI[2]		0
826 float	_COS_PHI[3]		0
828 float	_PF[0]		0
830 float	_PF[1]		0
832 float	_PF[2]		0
834 float	_PF[3]		0
836 float	_THD_ULN[0]	%	0
838 float	_THD_ULN[1]	%	0
840 float	_THD_ULN[2]	%	0
842 float	_THD_ULL[0]	%	0
844 float	_THD_ULL[1]	%	0
846 float	_THD_ULL[2]	%	0
848 float	_ULN_RE[0]	V	0
850 float	_ULN_RE[1]	V	0
852 float	_ULN_RE[2]	V	0
854 float	_ULN_IM[0]	V	0
856 float	_ULN_IM[1]	V	0
858 float	_ULN_IM[2]	V	0
860 float	_ILN[0]	A	0
862 float	_ILN[1]	A	0
864 float	_ILN[2]	A	0
866 float	_ILN[3]	A	0
868 float	_PLN[0]	W	0
870 float	_PLN[1]	W	0
872 float	_PLN[2]	W	0
874 float	_PLN[3]	W	0
876 float	_QLN[0]	var	0
878 float	_QLN[1]	var	0
880 float	_QLN[2]	var	0
882 float	_QLN[3]	var	0
884 float	_SLN[0]	VA	0
886 float	_SLN[1]	VA	0
888 float	_SLN[2]	VA	0
890 float	_SLN[3]	VA	0
892 float	_P0[0]	W	0
894 float	_P0[1]	W	0
896 float	_P0[2]	W	0
898 float	_P0[3]	W	0
900 float	_DLN[0]	var	0
902 float	_DLN[1]	var	0
904 float	_DLN[2]	var	0

906 float	_DLN[3]	var	0
908 float	_THD_ILN[0]	%	0
910 float	_THD_ILN[1]	%	0
912 float	_THD_ILN[2]	%	0
914 float	_TDD_ILN[0]	%	0
916 float	_TDD_ILN[1]	%	0
918 float	_TDD_ILN[2]	%	0
920 float	_IN	A	0
922 float	_IG	A	0
924 float	_IM	A	0
926 float	_IL_RE[0]	A	0
928 float	_IL_RE[1]	A	0
930 float	_IL_RE[2]	A	0
932 float	_IL_IM[0]	A	0
934 float	_IL_IM[1]	A	0
936 float	_IL_IM[2]	A	0
938 float	_PHASE_SEQ		0
1000 float	_FFT_UL1[0]	V	0
1002 float	_FFT_UL1[1]	V	0
1004 float	_FFT_UL1[2]	V	0
1006 float	_FFT_UL1[3]	V	0
1008 float	_FFT_UL1[4]	V	0
1010 float	_FFT_UL1[5]	V	0
1012 float	_FFT_UL1[6]	V	0
1014 float	_FFT_UL1[7]	V	0
1016 float	_FFT_UL1[8]	V	0
1018 float	_FFT_UL1[9]	V	0
1020 float	_FFT_UL1[10]	V	0
1022 float	_FFT_UL1[11]	V	0
1024 float	_FFT_UL1[12]	V	0
1026 float	_FFT_UL1[13]	V	0
1028 float	_FFT_UL1[14]	V	0
1030 float	_FFT_UL1[15]	V	0
1032 float	_FFT_UL1[16]	V	0
1034 float	_FFT_UL1[17]	V	0
1036 float	_FFT_UL1[18]	V	0
1038 float	_FFT_UL1[19]	V	0
1040 float	_FFT_UL1[20]	V	0
1042 float	_FFT_UL1[21]	V	0
1044 float	_FFT_UL1[22]	V	0
1046 float	_FFT_UL1[23]	V	0
1048 float	_FFT_UL1[24]	V	0
1050 float	_FFT_UL1[25]	V	0
1052 float	_FFT_UL1[26]	V	0
1054 float	_FFT_UL1[27]	V	0
1056 float	_FFT_UL1[28]	V	0
1058 float	_FFT_UL1[29]	V	0
1060 float	_FFT_UL1[30]	V	0
1062 float	_FFT_UL1[31]	V	0
1064 float	_FFT_UL1[32]	V	0
1066 float	_FFT_UL1[33]	V	0
1068 float	_FFT_UL1[34]	V	0
1070 float	_FFT_UL1[35]	V	0

1072 float	_FFT_UL1[36]	V	0
1074 float	_FFT_UL1[37]	V	0
1076 float	_FFT_UL1[38]	V	0
1078 float	_FFT_UL1[39]	V	0
1080 float	_FFT_UL2[0]	V	0
1082 float	_FFT_UL2[1]	V	0
1084 float	_FFT_UL2[2]	V	0
1086 float	_FFT_UL2[3]	V	0
1088 float	_FFT_UL2[4]	V	0
1090 float	_FFT_UL2[5]	V	0
1092 float	_FFT_UL2[6]	V	0
1094 float	_FFT_UL2[7]	V	0
1096 float	_FFT_UL2[8]	V	0
1098 float	_FFT_UL2[9]	V	0
1100 float	_FFT_UL2[10]	V	0
1102 float	_FFT_UL2[11]	V	0
1104 float	_FFT_UL2[12]	V	0
1106 float	_FFT_UL2[13]	V	0
1108 float	_FFT_UL2[14]	V	0
1110 float	_FFT_UL2[15]	V	0
1112 float	_FFT_UL2[16]	V	0
1114 float	_FFT_UL2[17]	V	0
1116 float	_FFT_UL2[18]	V	0
1118 float	_FFT_UL2[19]	V	0
1120 float	_FFT_UL2[20]	V	0
1122 float	_FFT_UL2[21]	V	0
1124 float	_FFT_UL2[22]	V	0
1126 float	_FFT_UL2[23]	V	0
1128 float	_FFT_UL2[24]	V	0
1130 float	_FFT_UL2[25]	V	0
1132 float	_FFT_UL2[26]	V	0
1134 float	_FFT_UL2[27]	V	0
1136 float	_FFT_UL2[28]	V	0
1138 float	_FFT_UL2[29]	V	0
1140 float	_FFT_UL2[30]	V	0
1142 float	_FFT_UL2[31]	V	0
1144 float	_FFT_UL2[32]	V	0
1146 float	_FFT_UL2[33]	V	0
1148 float	_FFT_UL2[34]	V	0
1150 float	_FFT_UL2[35]	V	0
1152 float	_FFT_UL2[36]	V	0
1154 float	_FFT_UL2[37]	V	0
1156 float	_FFT_UL2[38]	V	0
1158 float	_FFT_UL2[39]	V	0
1160 float	_FFT_UL3[0]	V	0
1162 float	_FFT_UL3[1]	V	0
1164 float	_FFT_UL3[2]	V	0
1166 float	_FFT_UL3[3]	V	0
1168 float	_FFT_UL3[4]	V	0
1170 float	_FFT_UL3[5]	V	0
1172 float	_FFT_UL3[6]	V	0
1174 float	_FFT_UL3[7]	V	0
1176 float	_FFT_UL3[8]	V	0

1178 float	_FFT_UL3[9]	V	0
1180 float	_FFT_UL3[10]	V	0
1182 float	_FFT_UL3[11]	V	0
1184 float	_FFT_UL3[12]	V	0
1186 float	_FFT_UL3[13]	V	0
1188 float	_FFT_UL3[14]	V	0
1190 float	_FFT_UL3[15]	V	0
1192 float	_FFT_UL3[16]	V	0
1194 float	_FFT_UL3[17]	V	0
1196 float	_FFT_UL3[18]	V	0
1198 float	_FFT_UL3[19]	V	0
1200 float	_FFT_UL3[20]	V	0
1202 float	_FFT_UL3[21]	V	0
1204 float	_FFT_UL3[22]	V	0
1206 float	_FFT_UL3[23]	V	0
1208 float	_FFT_UL3[24]	V	0
1210 float	_FFT_UL3[25]	V	0
1212 float	_FFT_UL3[26]	V	0
1214 float	_FFT_UL3[27]	V	0
1216 float	_FFT_UL3[28]	V	0
1218 float	_FFT_UL3[29]	V	0
1220 float	_FFT_UL3[30]	V	0
1222 float	_FFT_UL3[31]	V	0
1224 float	_FFT_UL3[32]	V	0
1226 float	_FFT_UL3[33]	V	0
1228 float	_FFT_UL3[34]	V	0
1230 float	_FFT_UL3[35]	V	0
1232 float	_FFT_UL3[36]	V	0
1234 float	_FFT_UL3[37]	V	0
1236 float	_FFT_UL3[38]	V	0
1238 float	_FFT_UL3[39]	V	0
1240 float	_FFT_ULL1[0]	V	0
1242 float	_FFT_ULL1[1]	V	0
1244 float	_FFT_ULL1[2]	V	0
1246 float	_FFT_ULL1[3]	V	0
1248 float	_FFT_ULL1[4]	V	0
1250 float	_FFT_ULL1[5]	V	0
1252 float	_FFT_ULL1[6]	V	0
1254 float	_FFT_ULL1[7]	V	0
1256 float	_FFT_ULL1[8]	V	0
1258 float	_FFT_ULL1[9]	V	0
1260 float	_FFT_ULL1[10]	V	0
1262 float	_FFT_ULL1[11]	V	0
1264 float	_FFT_ULL1[12]	V	0
1266 float	_FFT_ULL1[13]	V	0
1268 float	_FFT_ULL1[14]	V	0
1270 float	_FFT_ULL1[15]	V	0
1272 float	_FFT_ULL1[16]	V	0
1274 float	_FFT_ULL1[17]	V	0
1276 float	_FFT_ULL1[18]	V	0
1278 float	_FFT_ULL1[19]	V	0
1280 float	_FFT_ULL1[20]	V	0
1282 float	_FFT_ULL1[21]	V	0

1284 float	_FFT_ULL1[22]	V	0
1286 float	_FFT_ULL1[23]	V	0
1288 float	_FFT_ULL1[24]	V	0
1290 float	_FFT_ULL1[25]	V	0
1292 float	_FFT_ULL1[26]	V	0
1294 float	_FFT_ULL1[27]	V	0
1296 float	_FFT_ULL1[28]	V	0
1298 float	_FFT_ULL1[29]	V	0
1300 float	_FFT_ULL1[30]	V	0
1302 float	_FFT_ULL1[31]	V	0
1304 float	_FFT_ULL1[32]	V	0
1306 float	_FFT_ULL1[33]	V	0
1308 float	_FFT_ULL1[34]	V	0
1310 float	_FFT_ULL1[35]	V	0
1312 float	_FFT_ULL1[36]	V	0
1314 float	_FFT_ULL1[37]	V	0
1316 float	_FFT_ULL1[38]	V	0
1318 float	_FFT_ULL1[39]	V	0
1320 float	_FFT_ULL2[0]	V	0
1322 float	_FFT_ULL2[1]	V	0
1324 float	_FFT_ULL2[2]	V	0
1326 float	_FFT_ULL2[3]	V	0
1328 float	_FFT_ULL2[4]	V	0
1330 float	_FFT_ULL2[5]	V	0
1332 float	_FFT_ULL2[6]	V	0
1334 float	_FFT_ULL2[7]	V	0
1336 float	_FFT_ULL2[8]	V	0
1338 float	_FFT_ULL2[9]	V	0
1340 float	_FFT_ULL2[10]	V	0
1342 float	_FFT_ULL2[11]	V	0
1344 float	_FFT_ULL2[12]	V	0
1346 float	_FFT_ULL2[13]	V	0
1348 float	_FFT_ULL2[14]	V	0
1350 float	_FFT_ULL2[15]	V	0
1352 float	_FFT_ULL2[16]	V	0
1354 float	_FFT_ULL2[17]	V	0
1356 float	_FFT_ULL2[18]	V	0
1358 float	_FFT_ULL2[19]	V	0
1360 float	_FFT_ULL2[20]	V	0
1362 float	_FFT_ULL2[21]	V	0
1364 float	_FFT_ULL2[22]	V	0
1366 float	_FFT_ULL2[23]	V	0
1368 float	_FFT_ULL2[24]	V	0
1370 float	_FFT_ULL2[25]	V	0
1372 float	_FFT_ULL2[26]	V	0
1374 float	_FFT_ULL2[27]	V	0
1376 float	_FFT_ULL2[28]	V	0
1378 float	_FFT_ULL2[29]	V	0
1380 float	_FFT_ULL2[30]	V	0
1382 float	_FFT_ULL2[31]	V	0
1384 float	_FFT_ULL2[32]	V	0
1386 float	_FFT_ULL2[33]	V	0
1388 float	_FFT_ULL2[34]	V	0

1390 float	_FFT_ULL2[35]	V	0
1392 float	_FFT_ULL2[36]	V	0
1394 float	_FFT_ULL2[37]	V	0
1396 float	_FFT_ULL2[38]	V	0
1398 float	_FFT_ULL2[39]	V	0
1400 float	_FFT_ULL3[0]	V	0
1402 float	_FFT_ULL3[1]	V	0
1404 float	_FFT_ULL3[2]	V	0
1406 float	_FFT_ULL3[3]	V	0
1408 float	_FFT_ULL3[4]	V	0
1410 float	_FFT_ULL3[5]	V	0
1412 float	_FFT_ULL3[6]	V	0
1414 float	_FFT_ULL3[7]	V	0
1416 float	_FFT_ULL3[8]	V	0
1418 float	_FFT_ULL3[9]	V	0
1420 float	_FFT_ULL3[10]	V	0
1422 float	_FFT_ULL3[11]	V	0
1424 float	_FFT_ULL3[12]	V	0
1426 float	_FFT_ULL3[13]	V	0
1428 float	_FFT_ULL3[14]	V	0
1430 float	_FFT_ULL3[15]	V	0
1432 float	_FFT_ULL3[16]	V	0
1434 float	_FFT_ULL3[17]	V	0
1436 float	_FFT_ULL3[18]	V	0
1438 float	_FFT_ULL3[19]	V	0
1440 float	_FFT_ULL3[20]	V	0
1442 float	_FFT_ULL3[21]	V	0
1444 float	_FFT_ULL3[22]	V	0
1446 float	_FFT_ULL3[23]	V	0
1448 float	_FFT_ULL3[24]	V	0
1450 float	_FFT_ULL3[25]	V	0
1452 float	_FFT_ULL3[26]	V	0
1454 float	_FFT_ULL3[27]	V	0
1456 float	_FFT_ULL3[28]	V	0
1458 float	_FFT_ULL3[29]	V	0
1460 float	_FFT_ULL3[30]	V	0
1462 float	_FFT_ULL3[31]	V	0
1464 float	_FFT_ULL3[32]	V	0
1466 float	_FFT_ULL3[33]	V	0
1468 float	_FFT_ULL3[34]	V	0
1470 float	_FFT_ULL3[35]	V	0
1472 float	_FFT_ULL3[36]	V	0
1474 float	_FFT_ULL3[37]	V	0
1476 float	_FFT_ULL3[38]	V	0
1478 float	_FFT_ULL3[39]	V	0
1480 float	_FFT_IL1[0]	A	0
1482 float	_FFT_IL1[1]	A	0
1484 float	_FFT_IL1[2]	A	0
1486 float	_FFT_IL1[3]	A	0
1488 float	_FFT_IL1[4]	A	0
1490 float	_FFT_IL1[5]	A	0
1492 float	_FFT_IL1[6]	A	0
1494 float	_FFT_IL1[7]	A	0

1496 float	_FFT_IL1[8]	A	0
1498 float	_FFT_IL1[9]	A	0
1500 float	_FFT_IL1[10]	A	0
1502 float	_FFT_IL1[11]	A	0
1504 float	_FFT_IL1[12]	A	0
1506 float	_FFT_IL1[13]	A	0
1508 float	_FFT_IL1[14]	A	0
1510 float	_FFT_IL1[15]	A	0
1512 float	_FFT_IL1[16]	A	0
1514 float	_FFT_IL1[17]	A	0
1516 float	_FFT_IL1[18]	A	0
1518 float	_FFT_IL1[19]	A	0
1520 float	_FFT_IL1[20]	A	0
1522 float	_FFT_IL1[21]	A	0
1524 float	_FFT_IL1[22]	A	0
1526 float	_FFT_IL1[23]	A	0
1528 float	_FFT_IL1[24]	A	0
1530 float	_FFT_IL1[25]	A	0
1532 float	_FFT_IL1[26]	A	0
1534 float	_FFT_IL1[27]	A	0
1536 float	_FFT_IL1[28]	A	0
1538 float	_FFT_IL1[29]	A	0
1540 float	_FFT_IL1[30]	A	0
1542 float	_FFT_IL1[31]	A	0
1544 float	_FFT_IL1[32]	A	0
1546 float	_FFT_IL1[33]	A	0
1548 float	_FFT_IL1[34]	A	0
1550 float	_FFT_IL1[35]	A	0
1552 float	_FFT_IL1[36]	A	0
1554 float	_FFT_IL1[37]	A	0
1556 float	_FFT_IL1[38]	A	0
1558 float	_FFT_IL1[39]	A	0
1560 float	_FFT_IL2[0]	A	0
1562 float	_FFT_IL2[1]	A	0
1564 float	_FFT_IL2[2]	A	0
1566 float	_FFT_IL2[3]	A	0
1568 float	_FFT_IL2[4]	A	0
1570 float	_FFT_IL2[5]	A	0
1572 float	_FFT_IL2[6]	A	0
1574 float	_FFT_IL2[7]	A	0
1576 float	_FFT_IL2[8]	A	0
1578 float	_FFT_IL2[9]	A	0
1580 float	_FFT_IL2[10]	A	0
1582 float	_FFT_IL2[11]	A	0
1584 float	_FFT_IL2[12]	A	0
1586 float	_FFT_IL2[13]	A	0
1588 float	_FFT_IL2[14]	A	0
1590 float	_FFT_IL2[15]	A	0
1592 float	_FFT_IL2[16]	A	0
1594 float	_FFT_IL2[17]	A	0
1596 float	_FFT_IL2[18]	A	0
1598 float	_FFT_IL2[19]	A	0
1600 float	_FFT_IL2[20]	A	0

1602 float	_FFT_IL2[21]	A	0
1604 float	_FFT_IL2[22]	A	0
1606 float	_FFT_IL2[23]	A	0
1608 float	_FFT_IL2[24]	A	0
1610 float	_FFT_IL2[25]	A	0
1612 float	_FFT_IL2[26]	A	0
1614 float	_FFT_IL2[27]	A	0
1616 float	_FFT_IL2[28]	A	0
1618 float	_FFT_IL2[29]	A	0
1620 float	_FFT_IL2[30]	A	0
1622 float	_FFT_IL2[31]	A	0
1624 float	_FFT_IL2[32]	A	0
1626 float	_FFT_IL2[33]	A	0
1628 float	_FFT_IL2[34]	A	0
1630 float	_FFT_IL2[35]	A	0
1632 float	_FFT_IL2[36]	A	0
1634 float	_FFT_IL2[37]	A	0
1636 float	_FFT_IL2[38]	A	0
1638 float	_FFT_IL2[39]	A	0
1640 float	_FFT_IL3[0]	A	0
1642 float	_FFT_IL3[1]	A	0
1644 float	_FFT_IL3[2]	A	0
1646 float	_FFT_IL3[3]	A	0
1648 float	_FFT_IL3[4]	A	0
1650 float	_FFT_IL3[5]	A	0
1652 float	_FFT_IL3[6]	A	0
1654 float	_FFT_IL3[7]	A	0
1656 float	_FFT_IL3[8]	A	0
1658 float	_FFT_IL3[9]	A	0
1660 float	_FFT_IL3[10]	A	0
1662 float	_FFT_IL3[11]	A	0
1664 float	_FFT_IL3[12]	A	0
1666 float	_FFT_IL3[13]	A	0
1668 float	_FFT_IL3[14]	A	0
1670 float	_FFT_IL3[15]	A	0
1672 float	_FFT_IL3[16]	A	0
1674 float	_FFT_IL3[17]	A	0
1676 float	_FFT_IL3[18]	A	0
1678 float	_FFT_IL3[19]	A	0
1680 float	_FFT_IL3[20]	A	0
1682 float	_FFT_IL3[21]	A	0
1684 float	_FFT_IL3[22]	A	0
1686 float	_FFT_IL3[23]	A	0
1688 float	_FFT_IL3[24]	A	0
1690 float	_FFT_IL3[25]	A	0
1692 float	_FFT_IL3[26]	A	0
1694 float	_FFT_IL3[27]	A	0
1696 float	_FFT_IL3[28]	A	0
1698 float	_FFT_IL3[29]	A	0
1700 float	_FFT_IL3[30]	A	0
1702 float	_FFT_IL3[31]	A	0
1704 float	_FFT_IL3[32]	A	0
1706 float	_FFT_IL3[33]	A	0

1708 float	_FFT_IL3[34]	A	0
1710 float	_FFT_IL3[35]	A	0
1712 float	_FFT_IL3[36]	A	0
1714 float	_FFT_IL3[37]	A	0
1716 float	_FFT_IL3[38]	A	0
1718 float	_FFT_IL3[39]	A	0
1720 float	_FREQ_AVG	Hz	0
1722 float	_N_AVG	V	0
1724 float	_G_AVG	V	0
1726 float	_M_AVG	V	0
1728 float	_ULN_AVG[0]	V	0
1730 float	_ULN_AVG[1]	V	0
1732 float	_ULN_AVG[2]	V	0
1734 float	_ULL_AVG[0]	V	0
1736 float	_ULL_AVG[1]	V	0
1738 float	_ULL_AVG[2]	V	0
1740 float	_FFT_UL1_AVG[0]	V	0
1742 float	_FFT_UL1_AVG[1]	V	0
1744 float	_FFT_UL1_AVG[2]	V	0
1746 float	_FFT_UL1_AVG[3]	V	0
1748 float	_FFT_UL1_AVG[4]	V	0
1750 float	_FFT_UL1_AVG[5]	V	0
1752 float	_FFT_UL1_AVG[6]	V	0
1754 float	_FFT_UL1_AVG[7]	V	0
1756 float	_FFT_UL1_AVG[8]	V	0
1758 float	_FFT_UL1_AVG[9]	V	0
1760 float	_FFT_UL1_AVG[10]	V	0
1762 float	_FFT_UL1_AVG[11]	V	0
1764 float	_FFT_UL1_AVG[12]	V	0
1766 float	_FFT_UL1_AVG[13]	V	0
1768 float	_FFT_UL1_AVG[14]	V	0
1770 float	_FFT_UL1_AVG[15]	V	0
1772 float	_FFT_UL1_AVG[16]	V	0
1774 float	_FFT_UL1_AVG[17]	V	0
1776 float	_FFT_UL1_AVG[18]	V	0
1778 float	_FFT_UL1_AVG[19]	V	0
1780 float	_FFT_UL1_AVG[20]	V	0
1782 float	_FFT_UL1_AVG[21]	V	0
1784 float	_FFT_UL1_AVG[22]	V	0
1786 float	_FFT_UL1_AVG[23]	V	0
1788 float	_FFT_UL1_AVG[24]	V	0
1790 float	_FFT_UL1_AVG[25]	V	0
1792 float	_FFT_UL1_AVG[26]	V	0
1794 float	_FFT_UL1_AVG[27]	V	0
1796 float	_FFT_UL1_AVG[28]	V	0
1798 float	_FFT_UL1_AVG[29]	V	0
1800 float	_FFT_UL1_AVG[30]	V	0
1802 float	_FFT_UL1_AVG[31]	V	0
1804 float	_FFT_UL1_AVG[32]	V	0
1806 float	_FFT_UL1_AVG[33]	V	0
1808 float	_FFT_UL1_AVG[34]	V	0
1810 float	_FFT_UL1_AVG[35]	V	0
1812 float	_FFT_UL1_AVG[36]	V	0

1814 float	_FFT_UL1_AVG[37]	V	0
1816 float	_FFT_UL1_AVG[38]	V	0
1818 float	_FFT_UL1_AVG[39]	V	0
1820 float	_FFT_UL2_AVG[0]	V	0
1822 float	_FFT_UL2_AVG[1]	V	0
1824 float	_FFT_UL2_AVG[2]	V	0
1826 float	_FFT_UL2_AVG[3]	V	0
1828 float	_FFT_UL2_AVG[4]	V	0
1830 float	_FFT_UL2_AVG[5]	V	0
1832 float	_FFT_UL2_AVG[6]	V	0
1834 float	_FFT_UL2_AVG[7]	V	0
1836 float	_FFT_UL2_AVG[8]	V	0
1838 float	_FFT_UL2_AVG[9]	V	0
1840 float	_FFT_UL2_AVG[10]	V	0
1842 float	_FFT_UL2_AVG[11]	V	0
1844 float	_FFT_UL2_AVG[12]	V	0
1846 float	_FFT_UL2_AVG[13]	V	0
1848 float	_FFT_UL2_AVG[14]	V	0
1850 float	_FFT_UL2_AVG[15]	V	0
1852 float	_FFT_UL2_AVG[16]	V	0
1854 float	_FFT_UL2_AVG[17]	V	0
1856 float	_FFT_UL2_AVG[18]	V	0
1858 float	_FFT_UL2_AVG[19]	V	0
1860 float	_FFT_UL2_AVG[20]	V	0
1862 float	_FFT_UL2_AVG[21]	V	0
1864 float	_FFT_UL2_AVG[22]	V	0
1866 float	_FFT_UL2_AVG[23]	V	0
1868 float	_FFT_UL2_AVG[24]	V	0
1870 float	_FFT_UL2_AVG[25]	V	0
1872 float	_FFT_UL2_AVG[26]	V	0
1874 float	_FFT_UL2_AVG[27]	V	0
1876 float	_FFT_UL2_AVG[28]	V	0
1878 float	_FFT_UL2_AVG[29]	V	0
1880 float	_FFT_UL2_AVG[30]	V	0
1882 float	_FFT_UL2_AVG[31]	V	0
1884 float	_FFT_UL2_AVG[32]	V	0
1886 float	_FFT_UL2_AVG[33]	V	0
1888 float	_FFT_UL2_AVG[34]	V	0
1890 float	_FFT_UL2_AVG[35]	V	0
1892 float	_FFT_UL2_AVG[36]	V	0
1894 float	_FFT_UL2_AVG[37]	V	0
1896 float	_FFT_UL2_AVG[38]	V	0
1898 float	_FFT_UL2_AVG[39]	V	0
1900 float	_FFT_UL3_AVG[0]	V	0
1902 float	_FFT_UL3_AVG[1]	V	0
1904 float	_FFT_UL3_AVG[2]	V	0
1906 float	_FFT_UL3_AVG[3]	V	0
1908 float	_FFT_UL3_AVG[4]	V	0
1910 float	_FFT_UL3_AVG[5]	V	0
1912 float	_FFT_UL3_AVG[6]	V	0
1914 float	_FFT_UL3_AVG[7]	V	0
1916 float	_FFT_UL3_AVG[8]	V	0
1918 float	_FFT_UL3_AVG[9]	V	0

1920 float	_FFT_UL3_AVG[10]	V	0
1922 float	_FFT_UL3_AVG[11]	V	0
1924 float	_FFT_UL3_AVG[12]	V	0
1926 float	_FFT_UL3_AVG[13]	V	0
1928 float	_FFT_UL3_AVG[14]	V	0
1930 float	_FFT_UL3_AVG[15]	V	0
1932 float	_FFT_UL3_AVG[16]	V	0
1934 float	_FFT_UL3_AVG[17]	V	0
1936 float	_FFT_UL3_AVG[18]	V	0
1938 float	_FFT_UL3_AVG[19]	V	0
1940 float	_FFT_UL3_AVG[20]	V	0
1942 float	_FFT_UL3_AVG[21]	V	0
1944 float	_FFT_UL3_AVG[22]	V	0
1946 float	_FFT_UL3_AVG[23]	V	0
1948 float	_FFT_UL3_AVG[24]	V	0
1950 float	_FFT_UL3_AVG[25]	V	0
1952 float	_FFT_UL3_AVG[26]	V	0
1954 float	_FFT_UL3_AVG[27]	V	0
1956 float	_FFT_UL3_AVG[28]	V	0
1958 float	_FFT_UL3_AVG[29]	V	0
1960 float	_FFT_UL3_AVG[30]	V	0
1962 float	_FFT_UL3_AVG[31]	V	0
1964 float	_FFT_UL3_AVG[32]	V	0
1966 float	_FFT_UL3_AVG[33]	V	0
1968 float	_FFT_UL3_AVG[34]	V	0
1970 float	_FFT_UL3_AVG[35]	V	0
1972 float	_FFT_UL3_AVG[36]	V	0
1974 float	_FFT_UL3_AVG[37]	V	0
1976 float	_FFT_UL3_AVG[38]	V	0
1978 float	_FFT_UL3_AVG[39]	V	0
1980 float	_FFT_ULL1_AVG[0]	V	0
1982 float	_FFT_ULL1_AVG[1]	V	0
1984 float	_FFT_ULL1_AVG[2]	V	0
1986 float	_FFT_ULL1_AVG[3]	V	0
1988 float	_FFT_ULL1_AVG[4]	V	0
1990 float	_FFT_ULL1_AVG[5]	V	0
1992 float	_FFT_ULL1_AVG[6]	V	0
1994 float	_FFT_ULL1_AVG[7]	V	0
1996 float	_FFT_ULL1_AVG[8]	V	0
1998 float	_FFT_ULL1_AVG[9]	V	0
2000 float	_FFT_ULL1_AVG[10]	V	0
2002 float	_FFT_ULL1_AVG[11]	V	0
2004 float	_FFT_ULL1_AVG[12]	V	0
2006 float	_FFT_ULL1_AVG[13]	V	0
2008 float	_FFT_ULL1_AVG[14]	V	0
2010 float	_FFT_ULL1_AVG[15]	V	0
2012 float	_FFT_ULL1_AVG[16]	V	0
2014 float	_FFT_ULL1_AVG[17]	V	0
2016 float	_FFT_ULL1_AVG[18]	V	0
2018 float	_FFT_ULL1_AVG[19]	V	0
2020 float	_FFT_ULL1_AVG[20]	V	0
2022 float	_FFT_ULL1_AVG[21]	V	0
2024 float	_FFT_ULL1_AVG[22]	V	0

2026 float	_FFT_ULL1_AVG[23]	V	0
2028 float	_FFT_ULL1_AVG[24]	V	0
2030 float	_FFT_ULL1_AVG[25]	V	0
2032 float	_FFT_ULL1_AVG[26]	V	0
2034 float	_FFT_ULL1_AVG[27]	V	0
2036 float	_FFT_ULL1_AVG[28]	V	0
2038 float	_FFT_ULL1_AVG[29]	V	0
2040 float	_FFT_ULL1_AVG[30]	V	0
2042 float	_FFT_ULL1_AVG[31]	V	0
2044 float	_FFT_ULL1_AVG[32]	V	0
2046 float	_FFT_ULL1_AVG[33]	V	0
2048 float	_FFT_ULL1_AVG[34]	V	0
2050 float	_FFT_ULL1_AVG[35]	V	0
2052 float	_FFT_ULL1_AVG[36]	V	0
2054 float	_FFT_ULL1_AVG[37]	V	0
2056 float	_FFT_ULL1_AVG[38]	V	0
2058 float	_FFT_ULL1_AVG[39]	V	0
2060 float	_FFT_ULL2_AVG[0]	V	0
2062 float	_FFT_ULL2_AVG[1]	V	0
2064 float	_FFT_ULL2_AVG[2]	V	0
2066 float	_FFT_ULL2_AVG[3]	V	0
2068 float	_FFT_ULL2_AVG[4]	V	0
2070 float	_FFT_ULL2_AVG[5]	V	0
2072 float	_FFT_ULL2_AVG[6]	V	0
2074 float	_FFT_ULL2_AVG[7]	V	0
2076 float	_FFT_ULL2_AVG[8]	V	0
2078 float	_FFT_ULL2_AVG[9]	V	0
2080 float	_FFT_ULL2_AVG[10]	V	0
2082 float	_FFT_ULL2_AVG[11]	V	0
2084 float	_FFT_ULL2_AVG[12]	V	0
2086 float	_FFT_ULL2_AVG[13]	V	0
2088 float	_FFT_ULL2_AVG[14]	V	0
2090 float	_FFT_ULL2_AVG[15]	V	0
2092 float	_FFT_ULL2_AVG[16]	V	0
2094 float	_FFT_ULL2_AVG[17]	V	0
2096 float	_FFT_ULL2_AVG[18]	V	0
2098 float	_FFT_ULL2_AVG[19]	V	0
2100 float	_FFT_ULL2_AVG[20]	V	0
2102 float	_FFT_ULL2_AVG[21]	V	0
2104 float	_FFT_ULL2_AVG[22]	V	0
2106 float	_FFT_ULL2_AVG[23]	V	0
2108 float	_FFT_ULL2_AVG[24]	V	0
2110 float	_FFT_ULL2_AVG[25]	V	0
2112 float	_FFT_ULL2_AVG[26]	V	0
2114 float	_FFT_ULL2_AVG[27]	V	0
2116 float	_FFT_ULL2_AVG[28]	V	0
2118 float	_FFT_ULL2_AVG[29]	V	0
2120 float	_FFT_ULL2_AVG[30]	V	0
2122 float	_FFT_ULL2_AVG[31]	V	0
2124 float	_FFT_ULL2_AVG[32]	V	0
2126 float	_FFT_ULL2_AVG[33]	V	0
2128 float	_FFT_ULL2_AVG[34]	V	0
2130 float	_FFT_ULL2_AVG[35]	V	0

2132 float	_FFT_ULL2_AVG[36]	V	0
2134 float	_FFT_ULL2_AVG[37]	V	0
2136 float	_FFT_ULL2_AVG[38]	V	0
2138 float	_FFT_ULL2_AVG[39]	V	0
2140 float	_FFT_ULL3_AVG[0]	V	0
2142 float	_FFT_ULL3_AVG[1]	V	0
2144 float	_FFT_ULL3_AVG[2]	V	0
2146 float	_FFT_ULL3_AVG[3]	V	0
2148 float	_FFT_ULL3_AVG[4]	V	0
2150 float	_FFT_ULL3_AVG[5]	V	0
2152 float	_FFT_ULL3_AVG[6]	V	0
2154 float	_FFT_ULL3_AVG[7]	V	0
2156 float	_FFT_ULL3_AVG[8]	V	0
2158 float	_FFT_ULL3_AVG[9]	V	0
2160 float	_FFT_ULL3_AVG[10]	V	0
2162 float	_FFT_ULL3_AVG[11]	V	0
2164 float	_FFT_ULL3_AVG[12]	V	0
2166 float	_FFT_ULL3_AVG[13]	V	0
2168 float	_FFT_ULL3_AVG[14]	V	0
2170 float	_FFT_ULL3_AVG[15]	V	0
2172 float	_FFT_ULL3_AVG[16]	V	0
2174 float	_FFT_ULL3_AVG[17]	V	0
2176 float	_FFT_ULL3_AVG[18]	V	0
2178 float	_FFT_ULL3_AVG[19]	V	0
2180 float	_FFT_ULL3_AVG[20]	V	0
2182 float	_FFT_ULL3_AVG[21]	V	0
2184 float	_FFT_ULL3_AVG[22]	V	0
2186 float	_FFT_ULL3_AVG[23]	V	0
2188 float	_FFT_ULL3_AVG[24]	V	0
2190 float	_FFT_ULL3_AVG[25]	V	0
2192 float	_FFT_ULL3_AVG[26]	V	0
2194 float	_FFT_ULL3_AVG[27]	V	0
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2198 float	_FFT_ULL3_AVG[29]	V	0
2200 float	_FFT_ULL3_AVG[30]	V	0
2202 float	_FFT_ULL3_AVG[31]	V	0
2204 float	_FFT_ULL3_AVG[32]	V	0
2206 float	_FFT_ULL3_AVG[33]	V	0
2208 float	_FFT_ULL3_AVG[34]	V	0
2210 float	_FFT_ULL3_AVG[35]	V	0
2212 float	_FFT_ULL3_AVG[36]	V	0
2214 float	_FFT_ULL3_AVG[37]	V	0
2216 float	_FFT_ULL3_AVG[38]	V	0
2218 float	_FFT_ULL3_AVG[39]	V	0
2220 float	_COS_PHI_AVG[0]		0
2222 float	_COS_PHI_AVG[1]		0
2224 float	_COS_PHI_AVG[2]		0
2226 float	_COS_PHI_AVG[3]		0
2228 float	_PF_AVG[0]		0
2230 float	_PF_AVG[1]		0
2232 float	_PF_AVG[2]		0
2234 float	_PF_AVG[3]		0
2236 float	_THD_ULN_AVG[0]	%	0

2238 float	_THD_ULN_AVG[1]	%	0
2240 float	_THD_ULN_AVG[2]	%	0
2242 float	_THD_ULL_AVG[0]	%	0
2244 float	_THD_ULL_AVG[1]	%	0
2246 float	_THD_ULL_AVG[2]	%	0
2248 float	_ULN_RE_AVG[0]	V	0
2250 float	_ULN_RE_AVG[1]	V	0
2252 float	_ULN_RE_AVG[2]	V	0
2254 float	_ULN_IM_AVG[0]	V	0
2256 float	_ULN_IM_AVG[1]	V	0
2258 float	_ULN_IM_AVG[2]	V	0
2260 float	_FFT_IL1_AVG[0]	A	0
2262 float	_FFT_IL1_AVG[1]	A	0
2264 float	_FFT_IL1_AVG[2]	A	0
2266 float	_FFT_IL1_AVG[3]	A	0
2268 float	_FFT_IL1_AVG[4]	A	0
2270 float	_FFT_IL1_AVG[5]	A	0
2272 float	_FFT_IL1_AVG[6]	A	0
2274 float	_FFT_IL1_AVG[7]	A	0
2276 float	_FFT_IL1_AVG[8]	A	0
2278 float	_FFT_IL1_AVG[9]	A	0
2280 float	_FFT_IL1_AVG[10]	A	0
2282 float	_FFT_IL1_AVG[11]	A	0
2284 float	_FFT_IL1_AVG[12]	A	0
2286 float	_FFT_IL1_AVG[13]	A	0
2288 float	_FFT_IL1_AVG[14]	A	0
2290 float	_FFT_IL1_AVG[15]	A	0
2292 float	_FFT_IL1_AVG[16]	A	0
2294 float	_FFT_IL1_AVG[17]	A	0
2296 float	_FFT_IL1_AVG[18]	A	0
2298 float	_FFT_IL1_AVG[19]	A	0
2300 float	_FFT_IL1_AVG[20]	A	0
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2304 float	_FFT_IL1_AVG[22]	A	0
2306 float	_FFT_IL1_AVG[23]	A	0
2308 float	_FFT_IL1_AVG[24]	A	0
2310 float	_FFT_IL1_AVG[25]	A	0
2312 float	_FFT_IL1_AVG[26]	A	0
2314 float	_FFT_IL1_AVG[27]	A	0
2316 float	_FFT_IL1_AVG[28]	A	0
2318 float	_FFT_IL1_AVG[29]	A	0
2320 float	_FFT_IL1_AVG[30]	A	0
2322 float	_FFT_IL1_AVG[31]	A	0
2324 float	_FFT_IL1_AVG[32]	A	0
2326 float	_FFT_IL1_AVG[33]	A	0
2328 float	_FFT_IL1_AVG[34]	A	0
2330 float	_FFT_IL1_AVG[35]	A	0
2332 float	_FFT_IL1_AVG[36]	A	0
2334 float	_FFT_IL1_AVG[37]	A	0
2336 float	_FFT_IL1_AVG[38]	A	0
2338 float	_FFT_IL1_AVG[39]	A	0
2340 float	_FFT_IL2_AVG[0]	A	0
2342 float	_FFT_IL2_AVG[1]	A	0

2344 float	_FFT_IL2_AVG[2]	A	0
2346 float	_FFT_IL2_AVG[3]	A	0
2348 float	_FFT_IL2_AVG[4]	A	0
2350 float	_FFT_IL2_AVG[5]	A	0
2352 float	_FFT_IL2_AVG[6]	A	0
2354 float	_FFT_IL2_AVG[7]	A	0
2356 float	_FFT_IL2_AVG[8]	A	0
2358 float	_FFT_IL2_AVG[9]	A	0
2360 float	_FFT_IL2_AVG[10]	A	0
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2364 float	_FFT_IL2_AVG[12]	A	0
2366 float	_FFT_IL2_AVG[13]	A	0
2368 float	_FFT_IL2_AVG[14]	A	0
2370 float	_FFT_IL2_AVG[15]	A	0
2372 float	_FFT_IL2_AVG[16]	A	0
2374 float	_FFT_IL2_AVG[17]	A	0
2376 float	_FFT_IL2_AVG[18]	A	0
2378 float	_FFT_IL2_AVG[19]	A	0
2380 float	_FFT_IL2_AVG[20]	A	0
2382 float	_FFT_IL2_AVG[21]	A	0
2384 float	_FFT_IL2_AVG[22]	A	0
2386 float	_FFT_IL2_AVG[23]	A	0
2388 float	_FFT_IL2_AVG[24]	A	0
2390 float	_FFT_IL2_AVG[25]	A	0
2392 float	_FFT_IL2_AVG[26]	A	0
2394 float	_FFT_IL2_AVG[27]	A	0
2396 float	_FFT_IL2_AVG[28]	A	0
2398 float	_FFT_IL2_AVG[29]	A	0
2400 float	_FFT_IL2_AVG[30]	A	0
2402 float	_FFT_IL2_AVG[31]	A	0
2404 float	_FFT_IL2_AVG[32]	A	0
2406 float	_FFT_IL2_AVG[33]	A	0
2408 float	_FFT_IL2_AVG[34]	A	0
2410 float	_FFT_IL2_AVG[35]	A	0
2412 float	_FFT_IL2_AVG[36]	A	0
2414 float	_FFT_IL2_AVG[37]	A	0
2416 float	_FFT_IL2_AVG[38]	A	0
2418 float	_FFT_IL2_AVG[39]	A	0
2420 float	_FFT_IL3_AVG[0]	A	0
2422 float	_FFT_IL3_AVG[1]	A	0
2424 float	_FFT_IL3_AVG[2]	A	0
2426 float	_FFT_IL3_AVG[3]	A	0
2428 float	_FFT_IL3_AVG[4]	A	0
2430 float	_FFT_IL3_AVG[5]	A	0
2432 float	_FFT_IL3_AVG[6]	A	0
2434 float	_FFT_IL3_AVG[7]	A	0
2436 float	_FFT_IL3_AVG[8]	A	0
2438 float	_FFT_IL3_AVG[9]	A	0
2440 float	_FFT_IL3_AVG[10]	A	0
2442 float	_FFT_IL3_AVG[11]	A	0
2444 float	_FFT_IL3_AVG[12]	A	0
2446 float	_FFT_IL3_AVG[13]	A	0
2448 float	_FFT_IL3_AVG[14]	A	0

2450 float	_FFT_IL3_AVG[15]	A	0
2452 float	_FFT_IL3_AVG[16]	A	0
2454 float	_FFT_IL3_AVG[17]	A	0
2456 float	_FFT_IL3_AVG[18]	A	0
2458 float	_FFT_IL3_AVG[19]	A	0
2460 float	_FFT_IL3_AVG[20]	A	0
2462 float	_FFT_IL3_AVG[21]	A	0
2464 float	_FFT_IL3_AVG[22]	A	0
2466 float	_FFT_IL3_AVG[23]	A	0
2468 float	_FFT_IL3_AVG[24]	A	0
2470 float	_FFT_IL3_AVG[25]	A	0
2472 float	_FFT_IL3_AVG[26]	A	0
2474 float	_FFT_IL3_AVG[27]	A	0
2476 float	_FFT_IL3_AVG[28]	A	0
2478 float	_FFT_IL3_AVG[29]	A	0
2480 float	_FFT_IL3_AVG[30]	A	0
2482 float	_FFT_IL3_AVG[31]	A	0
2484 float	_FFT_IL3_AVG[32]	A	0
2486 float	_FFT_IL3_AVG[33]	A	0
2488 float	_FFT_IL3_AVG[34]	A	0
2490 float	_FFT_IL3_AVG[35]	A	0
2492 float	_FFT_IL3_AVG[36]	A	0
2494 float	_FFT_IL3_AVG[37]	A	0
2496 float	_FFT_IL3_AVG[38]	A	0
2498 float	_FFT_IL3_AVG[39]	A	0
2500 float	_ILN_AVG[0]	A	0
2502 float	_ILN_AVG[1]	A	0
2504 float	_ILN_AVG[2]	A	0
2506 float	_ILN_AVG[3]	A	0
2508 float	_PLN_AVG[0]	W	0
2510 float	_PLN_AVG[1]	W	0
2512 float	_PLN_AVG[2]	W	0
2514 float	_PLN_AVG[3]	W	0
2516 float	_QLN_AVG[0]	var	0
2518 float	_QLN_AVG[1]	var	0
2520 float	_QLN_AVG[2]	var	0
2522 float	_QLN_AVG[3]	var	0
2524 float	_SLN_AVG[0]	VA	0
2526 float	_SLN_AVG[1]	VA	0
2528 float	_SLN_AVG[2]	VA	0
2530 float	_SLN_AVG[3]	VA	0
2532 float	_P0_AVG[0]	W	0
2534 float	_P0_AVG[1]	W	0
2536 float	_P0_AVG[2]	W	0
2538 float	_P0_AVG[3]	W	0
2540 float	_DLN_AVG[0]	var	0
2542 float	_DLN_AVG[1]	var	0
2544 float	_DLN_AVG[2]	var	0
2546 float	_DLN_AVG[3]	var	0
2548 float	_THD_ILN_AVG[0]	%	0
2550 float	_THD_ILN_AVG[1]	%	0
2552 float	_THD_ILN_AVG[2]	%	0
2554 float	_TDD_ILN_AVG[0]	%	0

2556 float	_TDD_ILN_AVG[1]	%	0
2558 float	_TDD_ILN_AVG[2]	%	0
2560 float	_IN_AVG	A	0
2562 float	_IG_AVG	A	0
2564 float	_IM_AVG	A	0
2566 float	_IL_RE_AVG[0]	A	0
2568 float	_IL_RE_AVG[1]	A	0
2570 float	_IL_RE_AVG[2]	A	0
2572 float	_IL_IM_AVG[0]	A	0
2574 float	_IL_IM_AVG[1]	A	0
2576 float	_IL_IM_AVG[2]	A	0
2578 float	_FREQ_MAX	Hz	0
2580 float	_N_MAX	V	0
2582 float	_G_MAX	V	0
2584 float	_M_MAX	V	0
2586 float	_ULN_MAX[0]	V	0
2588 float	_ULN_MAX[1]	V	0
2590 float	_ULN_MAX[2]	V	0
2592 float	_ULL_MAX[0]	V	0
2594 float	_ULL_MAX[1]	V	0
2596 float	_ULL_MAX[2]	V	0
2598 float	_FFT_UL1_MAX[0]	V	0
2600 float	_FFT_UL1_MAX[1]	V	0
2602 float	_FFT_UL1_MAX[2]	V	0
2604 float	_FFT_UL1_MAX[3]	V	0
2606 float	_FFT_UL1_MAX[4]	V	0
2608 float	_FFT_UL1_MAX[5]	V	0
2610 float	_FFT_UL1_MAX[6]	V	0
2612 float	_FFT_UL1_MAX[7]	V	0
2614 float	_FFT_UL1_MAX[8]	V	0
2616 float	_FFT_UL1_MAX[9]	V	0
2618 float	_FFT_UL1_MAX[10]	V	0
2620 float	_FFT_UL1_MAX[11]	V	0
2622 float	_FFT_UL1_MAX[12]	V	0
2624 float	_FFT_UL1_MAX[13]	V	0
2626 float	_FFT_UL1_MAX[14]	V	0
2628 float	_FFT_UL1_MAX[15]	V	0
2630 float	_FFT_UL1_MAX[16]	V	0
2632 float	_FFT_UL1_MAX[17]	V	0
2634 float	_FFT_UL1_MAX[18]	V	0
2636 float	_FFT_UL1_MAX[19]	V	0
2638 float	_FFT_UL1_MAX[20]	V	0
2640 float	_FFT_UL1_MAX[21]	V	0
2642 float	_FFT_UL1_MAX[22]	V	0
2644 float	_FFT_UL1_MAX[23]	V	0
2646 float	_FFT_UL1_MAX[24]	V	0
2648 float	_FFT_UL1_MAX[25]	V	0
2650 float	_FFT_UL1_MAX[26]	V	0
2652 float	_FFT_UL1_MAX[27]	V	0
2654 float	_FFT_UL1_MAX[28]	V	0
2656 float	_FFT_UL1_MAX[29]	V	0
2658 float	_FFT_UL1_MAX[30]	V	0
2660 float	_FFT_UL1_MAX[31]	V	0

2662 float	_FFT_UL1_MAX[32]	V	0
2664 float	_FFT_UL1_MAX[33]	V	0
2666 float	_FFT_UL1_MAX[34]	V	0
2668 float	_FFT_UL1_MAX[35]	V	0
2670 float	_FFT_UL1_MAX[36]	V	0
2672 float	_FFT_UL1_MAX[37]	V	0
2674 float	_FFT_UL1_MAX[38]	V	0
2676 float	_FFT_UL1_MAX[39]	V	0
2678 float	_FFT_UL2_MAX[0]	V	0
2680 float	_FFT_UL2_MAX[1]	V	0
2682 float	_FFT_UL2_MAX[2]	V	0
2684 float	_FFT_UL2_MAX[3]	V	0
2686 float	_FFT_UL2_MAX[4]	V	0
2688 float	_FFT_UL2_MAX[5]	V	0
2690 float	_FFT_UL2_MAX[6]	V	0
2692 float	_FFT_UL2_MAX[7]	V	0
2694 float	_FFT_UL2_MAX[8]	V	0
2696 float	_FFT_UL2_MAX[9]	V	0
2698 float	_FFT_UL2_MAX[10]	V	0
2700 float	_FFT_UL2_MAX[11]	V	0
2702 float	_FFT_UL2_MAX[12]	V	0
2704 float	_FFT_UL2_MAX[13]	V	0
2706 float	_FFT_UL2_MAX[14]	V	0
2708 float	_FFT_UL2_MAX[15]	V	0
2710 float	_FFT_UL2_MAX[16]	V	0
2712 float	_FFT_UL2_MAX[17]	V	0
2714 float	_FFT_UL2_MAX[18]	V	0
2716 float	_FFT_UL2_MAX[19]	V	0
2718 float	_FFT_UL2_MAX[20]	V	0
2720 float	_FFT_UL2_MAX[21]	V	0
2722 float	_FFT_UL2_MAX[22]	V	0
2724 float	_FFT_UL2_MAX[23]	V	0
2726 float	_FFT_UL2_MAX[24]	V	0
2728 float	_FFT_UL2_MAX[25]	V	0
2730 float	_FFT_UL2_MAX[26]	V	0
2732 float	_FFT_UL2_MAX[27]	V	0
2734 float	_FFT_UL2_MAX[28]	V	0
2736 float	_FFT_UL2_MAX[29]	V	0
2738 float	_FFT_UL2_MAX[30]	V	0
2740 float	_FFT_UL2_MAX[31]	V	0
2742 float	_FFT_UL2_MAX[32]	V	0
2744 float	_FFT_UL2_MAX[33]	V	0
2746 float	_FFT_UL2_MAX[34]	V	0
2748 float	_FFT_UL2_MAX[35]	V	0
2750 float	_FFT_UL2_MAX[36]	V	0
2752 float	_FFT_UL2_MAX[37]	V	0
2754 float	_FFT_UL2_MAX[38]	V	0
2756 float	_FFT_UL2_MAX[39]	V	0
2758 float	_FFT_UL3_MAX[0]	V	0
2760 float	_FFT_UL3_MAX[1]	V	0
2762 float	_FFT_UL3_MAX[2]	V	0
2764 float	_FFT_UL3_MAX[3]	V	0
2766 float	_FFT_UL3_MAX[4]	V	0

2768 float	_FFT_UL3_MAX[5]	V	0
2770 float	_FFT_UL3_MAX[6]	V	0
2772 float	_FFT_UL3_MAX[7]	V	0
2774 float	_FFT_UL3_MAX[8]	V	0
2776 float	_FFT_UL3_MAX[9]	V	0
2778 float	_FFT_UL3_MAX[10]	V	0
2780 float	_FFT_UL3_MAX[11]	V	0
2782 float	_FFT_UL3_MAX[12]	V	0
2784 float	_FFT_UL3_MAX[13]	V	0
2786 float	_FFT_UL3_MAX[14]	V	0
2788 float	_FFT_UL3_MAX[15]	V	0
2790 float	_FFT_UL3_MAX[16]	V	0
2792 float	_FFT_UL3_MAX[17]	V	0
2794 float	_FFT_UL3_MAX[18]	V	0
2796 float	_FFT_UL3_MAX[19]	V	0
2798 float	_FFT_UL3_MAX[20]	V	0
2800 float	_FFT_UL3_MAX[21]	V	0
2802 float	_FFT_UL3_MAX[22]	V	0
2804 float	_FFT_UL3_MAX[23]	V	0
2806 float	_FFT_UL3_MAX[24]	V	0
2808 float	_FFT_UL3_MAX[25]	V	0
2810 float	_FFT_UL3_MAX[26]	V	0
2812 float	_FFT_UL3_MAX[27]	V	0
2814 float	_FFT_UL3_MAX[28]	V	0
2816 float	_FFT_UL3_MAX[29]	V	0
2818 float	_FFT_UL3_MAX[30]	V	0
2820 float	_FFT_UL3_MAX[31]	V	0
2822 float	_FFT_UL3_MAX[32]	V	0
2824 float	_FFT_UL3_MAX[33]	V	0
2826 float	_FFT_UL3_MAX[34]	V	0
2828 float	_FFT_UL3_MAX[35]	V	0
2830 float	_FFT_UL3_MAX[36]	V	0
2832 float	_FFT_UL3_MAX[37]	V	0
2834 float	_FFT_UL3_MAX[38]	V	0
2836 float	_FFT_UL3_MAX[39]	V	0
2838 float	_FFT_ULL1_MAX[0]	V	0
2840 float	_FFT_ULL1_MAX[1]	V	0
2842 float	_FFT_ULL1_MAX[2]	V	0
2844 float	_FFT_ULL1_MAX[3]	V	0
2846 float	_FFT_ULL1_MAX[4]	V	0
2848 float	_FFT_ULL1_MAX[5]	V	0
2850 float	_FFT_ULL1_MAX[6]	V	0
2852 float	_FFT_ULL1_MAX[7]	V	0
2854 float	_FFT_ULL1_MAX[8]	V	0
2856 float	_FFT_ULL1_MAX[9]	V	0
2858 float	_FFT_ULL1_MAX[10]	V	0
2860 float	_FFT_ULL1_MAX[11]	V	0
2862 float	_FFT_ULL1_MAX[12]	V	0
2864 float	_FFT_ULL1_MAX[13]	V	0
2866 float	_FFT_ULL1_MAX[14]	V	0
2868 float	_FFT_ULL1_MAX[15]	V	0
2870 float	_FFT_ULL1_MAX[16]	V	0
2872 float	_FFT_ULL1_MAX[17]	V	0

2874 float	_FFT_ULL1_MAX[18]	V	0
2876 float	_FFT_ULL1_MAX[19]	V	0
2878 float	_FFT_ULL1_MAX[20]	V	0
2880 float	_FFT_ULL1_MAX[21]	V	0
2882 float	_FFT_ULL1_MAX[22]	V	0
2884 float	_FFT_ULL1_MAX[23]	V	0
2886 float	_FFT_ULL1_MAX[24]	V	0
2888 float	_FFT_ULL1_MAX[25]	V	0
2890 float	_FFT_ULL1_MAX[26]	V	0
2892 float	_FFT_ULL1_MAX[27]	V	0
2894 float	_FFT_ULL1_MAX[28]	V	0
2896 float	_FFT_ULL1_MAX[29]	V	0
2898 float	_FFT_ULL1_MAX[30]	V	0
2900 float	_FFT_ULL1_MAX[31]	V	0
2902 float	_FFT_ULL1_MAX[32]	V	0
2904 float	_FFT_ULL1_MAX[33]	V	0
2906 float	_FFT_ULL1_MAX[34]	V	0
2908 float	_FFT_ULL1_MAX[35]	V	0
2910 float	_FFT_ULL1_MAX[36]	V	0
2912 float	_FFT_ULL1_MAX[37]	V	0
2914 float	_FFT_ULL1_MAX[38]	V	0
2916 float	_FFT_ULL1_MAX[39]	V	0
2918 float	_FFT_ULL2_MAX[0]	V	0
2920 float	_FFT_ULL2_MAX[1]	V	0
2922 float	_FFT_ULL2_MAX[2]	V	0
2924 float	_FFT_ULL2_MAX[3]	V	0
2926 float	_FFT_ULL2_MAX[4]	V	0
2928 float	_FFT_ULL2_MAX[5]	V	0
2930 float	_FFT_ULL2_MAX[6]	V	0
2932 float	_FFT_ULL2_MAX[7]	V	0
2934 float	_FFT_ULL2_MAX[8]	V	0
2936 float	_FFT_ULL2_MAX[9]	V	0
2938 float	_FFT_ULL2_MAX[10]	V	0
2940 float	_FFT_ULL2_MAX[11]	V	0
2942 float	_FFT_ULL2_MAX[12]	V	0
2944 float	_FFT_ULL2_MAX[13]	V	0
2946 float	_FFT_ULL2_MAX[14]	V	0
2948 float	_FFT_ULL2_MAX[15]	V	0
2950 float	_FFT_ULL2_MAX[16]	V	0
2952 float	_FFT_ULL2_MAX[17]	V	0
2954 float	_FFT_ULL2_MAX[18]	V	0
2956 float	_FFT_ULL2_MAX[19]	V	0
2958 float	_FFT_ULL2_MAX[20]	V	0
2960 float	_FFT_ULL2_MAX[21]	V	0
2962 float	_FFT_ULL2_MAX[22]	V	0
2964 float	_FFT_ULL2_MAX[23]	V	0
2966 float	_FFT_ULL2_MAX[24]	V	0
2968 float	_FFT_ULL2_MAX[25]	V	0
2970 float	_FFT_ULL2_MAX[26]	V	0
2972 float	_FFT_ULL2_MAX[27]	V	0
2974 float	_FFT_ULL2_MAX[28]	V	0
2976 float	_FFT_ULL2_MAX[29]	V	0
2978 float	_FFT_ULL2_MAX[30]	V	0

2980 float	_FFT_ULL2_MAX[31]	V	0
2982 float	_FFT_ULL2_MAX[32]	V	0
2984 float	_FFT_ULL2_MAX[33]	V	0
2986 float	_FFT_ULL2_MAX[34]	V	0
2988 float	_FFT_ULL2_MAX[35]	V	0
2990 float	_FFT_ULL2_MAX[36]	V	0
2992 float	_FFT_ULL2_MAX[37]	V	0
2994 float	_FFT_ULL2_MAX[38]	V	0
2996 float	_FFT_ULL2_MAX[39]	V	0
2998 float	_FFT_ULL3_MAX[0]	V	0
3000 float	_FFT_ULL3_MAX[1]	V	0
3002 float	_FFT_ULL3_MAX[2]	V	0
3004 float	_FFT_ULL3_MAX[3]	V	0
3006 float	_FFT_ULL3_MAX[4]	V	0
3008 float	_FFT_ULL3_MAX[5]	V	0
3010 float	_FFT_ULL3_MAX[6]	V	0
3012 float	_FFT_ULL3_MAX[7]	V	0
3014 float	_FFT_ULL3_MAX[8]	V	0
3016 float	_FFT_ULL3_MAX[9]	V	0
3018 float	_FFT_ULL3_MAX[10]	V	0
3020 float	_FFT_ULL3_MAX[11]	V	0
3022 float	_FFT_ULL3_MAX[12]	V	0
3024 float	_FFT_ULL3_MAX[13]	V	0
3026 float	_FFT_ULL3_MAX[14]	V	0
3028 float	_FFT_ULL3_MAX[15]	V	0
3030 float	_FFT_ULL3_MAX[16]	V	0
3032 float	_FFT_ULL3_MAX[17]	V	0
3034 float	_FFT_ULL3_MAX[18]	V	0
3036 float	_FFT_ULL3_MAX[19]	V	0
3038 float	_FFT_ULL3_MAX[20]	V	0
3040 float	_FFT_ULL3_MAX[21]	V	0
3042 float	_FFT_ULL3_MAX[22]	V	0
3044 float	_FFT_ULL3_MAX[23]	V	0
3046 float	_FFT_ULL3_MAX[24]	V	0
3048 float	_FFT_ULL3_MAX[25]	V	0
3050 float	_FFT_ULL3_MAX[26]	V	0
3052 float	_FFT_ULL3_MAX[27]	V	0
3054 float	_FFT_ULL3_MAX[28]	V	0
3056 float	_FFT_ULL3_MAX[29]	V	0
3058 float	_FFT_ULL3_MAX[30]	V	0
3060 float	_FFT_ULL3_MAX[31]	V	0
3062 float	_FFT_ULL3_MAX[32]	V	0
3064 float	_FFT_ULL3_MAX[33]	V	0
3066 float	_FFT_ULL3_MAX[34]	V	0
3068 float	_FFT_ULL3_MAX[35]	V	0
3070 float	_FFT_ULL3_MAX[36]	V	0
3072 float	_FFT_ULL3_MAX[37]	V	0
3074 float	_FFT_ULL3_MAX[38]	V	0
3076 float	_FFT_ULL3_MAX[39]	V	0
3078 float	_COS_PHI_MAX[0]		0
3080 float	_COS_PHI_MAX[1]		0
3082 float	_COS_PHI_MAX[2]		0
3084 float	_COS_PHI_MAX[3]		0

3086 float	_PF_MAX[0]		0
3088 float	_PF_MAX[1]		0
3090 float	_PF_MAX[2]		0
3092 float	_PF_MAX[3]		0
3094 float	_THD_ULN_MAX[0]	%	0
3096 float	_THD_ULN_MAX[1]	%	0
3098 float	_THD_ULN_MAX[2]	%	0
3100 float	_THD_ULL_MAX[0]	%	0
3102 float	_THD_ULL_MAX[1]	%	0
3104 float	_THD_ULL_MAX[2]	%	0
3106 float	_ULN_RE_MAX[0]	V	0
3108 float	_ULN_RE_MAX[1]	V	0
3110 float	_ULN_RE_MAX[2]	V	0
3112 float	_ULN_IM_MAX[0]	V	0
3114 float	_ULN_IM_MAX[1]	V	0
3116 float	_ULN_IM_MAX[2]	V	0
3118 float	_FFT_IL1_MAX[0]	A	0
3120 float	_FFT_IL1_MAX[1]	A	0
3122 float	_FFT_IL1_MAX[2]	A	0
3124 float	_FFT_IL1_MAX[3]	A	0
3126 float	_FFT_IL1_MAX[4]	A	0
3128 float	_FFT_IL1_MAX[5]	A	0
3130 float	_FFT_IL1_MAX[6]	A	0
3132 float	_FFT_IL1_MAX[7]	A	0
3134 float	_FFT_IL1_MAX[8]	A	0
3136 float	_FFT_IL1_MAX[9]	A	0
3138 float	_FFT_IL1_MAX[10]	A	0
3140 float	_FFT_IL1_MAX[11]	A	0
3142 float	_FFT_IL1_MAX[12]	A	0
3144 float	_FFT_IL1_MAX[13]	A	0
3146 float	_FFT_IL1_MAX[14]	A	0
3148 float	_FFT_IL1_MAX[15]	A	0
3150 float	_FFT_IL1_MAX[16]	A	0
3152 float	_FFT_IL1_MAX[17]	A	0
3154 float	_FFT_IL1_MAX[18]	A	0
3156 float	_FFT_IL1_MAX[19]	A	0
3158 float	_FFT_IL1_MAX[20]	A	0
3160 float	_FFT_IL1_MAX[21]	A	0
3162 float	_FFT_IL1_MAX[22]	A	0
3164 float	_FFT_IL1_MAX[23]	A	0
3166 float	_FFT_IL1_MAX[24]	A	0
3168 float	_FFT_IL1_MAX[25]	A	0
3170 float	_FFT_IL1_MAX[26]	A	0
3172 float	_FFT_IL1_MAX[27]	A	0
3174 float	_FFT_IL1_MAX[28]	A	0
3176 float	_FFT_IL1_MAX[29]	A	0
3178 float	_FFT_IL1_MAX[30]	A	0
3180 float	_FFT_IL1_MAX[31]	A	0
3182 float	_FFT_IL1_MAX[32]	A	0
3184 float	_FFT_IL1_MAX[33]	A	0
3186 float	_FFT_IL1_MAX[34]	A	0
3188 float	_FFT_IL1_MAX[35]	A	0
3190 float	_FFT_IL1_MAX[36]	A	0

3192 float	_FFT_IL1_MAX[37]	A	0
3194 float	_FFT_IL1_MAX[38]	A	0
3196 float	_FFT_IL1_MAX[39]	A	0
3198 float	_FFT_IL2_MAX[0]	A	0
3200 float	_FFT_IL2_MAX[1]	A	0
3202 float	_FFT_IL2_MAX[2]	A	0
3204 float	_FFT_IL2_MAX[3]	A	0
3206 float	_FFT_IL2_MAX[4]	A	0
3208 float	_FFT_IL2_MAX[5]	A	0
3210 float	_FFT_IL2_MAX[6]	A	0
3212 float	_FFT_IL2_MAX[7]	A	0
3214 float	_FFT_IL2_MAX[8]	A	0
3216 float	_FFT_IL2_MAX[9]	A	0
3218 float	_FFT_IL2_MAX[10]	A	0
3220 float	_FFT_IL2_MAX[11]	A	0
3222 float	_FFT_IL2_MAX[12]	A	0
3224 float	_FFT_IL2_MAX[13]	A	0
3226 float	_FFT_IL2_MAX[14]	A	0
3228 float	_FFT_IL2_MAX[15]	A	0
3230 float	_FFT_IL2_MAX[16]	A	0
3232 float	_FFT_IL2_MAX[17]	A	0
3234 float	_FFT_IL2_MAX[18]	A	0
3236 float	_FFT_IL2_MAX[19]	A	0
3238 float	_FFT_IL2_MAX[20]	A	0
3240 float	_FFT_IL2_MAX[21]	A	0
3242 float	_FFT_IL2_MAX[22]	A	0
3244 float	_FFT_IL2_MAX[23]	A	0
3246 float	_FFT_IL2_MAX[24]	A	0
3248 float	_FFT_IL2_MAX[25]	A	0
3250 float	_FFT_IL2_MAX[26]	A	0
3252 float	_FFT_IL2_MAX[27]	A	0
3254 float	_FFT_IL2_MAX[28]	A	0
3256 float	_FFT_IL2_MAX[29]	A	0
3258 float	_FFT_IL2_MAX[30]	A	0
3260 float	_FFT_IL2_MAX[31]	A	0
3262 float	_FFT_IL2_MAX[32]	A	0
3264 float	_FFT_IL2_MAX[33]	A	0
3266 float	_FFT_IL2_MAX[34]	A	0
3268 float	_FFT_IL2_MAX[35]	A	0
3270 float	_FFT_IL2_MAX[36]	A	0
3272 float	_FFT_IL2_MAX[37]	A	0
3274 float	_FFT_IL2_MAX[38]	A	0
3276 float	_FFT_IL2_MAX[39]	A	0
3278 float	_FFT_IL3_MAX[0]	A	0
3280 float	_FFT_IL3_MAX[1]	A	0
3282 float	_FFT_IL3_MAX[2]	A	0
3284 float	_FFT_IL3_MAX[3]	A	0
3286 float	_FFT_IL3_MAX[4]	A	0
3288 float	_FFT_IL3_MAX[5]	A	0
3290 float	_FFT_IL3_MAX[6]	A	0
3292 float	_FFT_IL3_MAX[7]	A	0
3294 float	_FFT_IL3_MAX[8]	A	0
3296 float	_FFT_IL3_MAX[9]	A	0

3298 float	_FFT_IL3_MAX[10]	A	0
3300 float	_FFT_IL3_MAX[11]	A	0
3302 float	_FFT_IL3_MAX[12]	A	0
3304 float	_FFT_IL3_MAX[13]	A	0
3306 float	_FFT_IL3_MAX[14]	A	0
3308 float	_FFT_IL3_MAX[15]	A	0
3310 float	_FFT_IL3_MAX[16]	A	0
3312 float	_FFT_IL3_MAX[17]	A	0
3314 float	_FFT_IL3_MAX[18]	A	0
3316 float	_FFT_IL3_MAX[19]	A	0
3318 float	_FFT_IL3_MAX[20]	A	0
3320 float	_FFT_IL3_MAX[21]	A	0
3322 float	_FFT_IL3_MAX[22]	A	0
3324 float	_FFT_IL3_MAX[23]	A	0
3326 float	_FFT_IL3_MAX[24]	A	0
3328 float	_FFT_IL3_MAX[25]	A	0
3330 float	_FFT_IL3_MAX[26]	A	0
3332 float	_FFT_IL3_MAX[27]	A	0
3334 float	_FFT_IL3_MAX[28]	A	0
3336 float	_FFT_IL3_MAX[29]	A	0
3338 float	_FFT_IL3_MAX[30]	A	0
3340 float	_FFT_IL3_MAX[31]	A	0
3342 float	_FFT_IL3_MAX[32]	A	0
3344 float	_FFT_IL3_MAX[33]	A	0
3346 float	_FFT_IL3_MAX[34]	A	0
3348 float	_FFT_IL3_MAX[35]	A	0
3350 float	_FFT_IL3_MAX[36]	A	0
3352 float	_FFT_IL3_MAX[37]	A	0
3354 float	_FFT_IL3_MAX[38]	A	0
3356 float	_FFT_IL3_MAX[39]	A	0
3358 float	_ILN_MAX[0]	A	0
3360 float	_ILN_MAX[1]	A	0
3362 float	_ILN_MAX[2]	A	0
3364 float	_ILN_MAX[3]	A	0
3366 float	_PLN_MAX[0]	W	0
3368 float	_PLN_MAX[1]	W	0
3370 float	_PLN_MAX[2]	W	0
3372 float	_PLN_MAX[3]	W	0
3374 float	_QLN_MAX[0]	var	0
3376 float	_QLN_MAX[1]	var	0
3378 float	_QLN_MAX[2]	var	0
3380 float	_QLN_MAX[3]	var	0
3382 float	_SLN_MAX[0]	VA	0
3384 float	_SLN_MAX[1]	VA	0
3386 float	_SLN_MAX[2]	VA	0
3388 float	_SLN_MAX[3]	VA	0
3390 float	_P0_MAX[0]	W	0
3392 float	_P0_MAX[1]	W	0
3394 float	_P0_MAX[2]	W	0
3396 float	_P0_MAX[3]	W	0
3398 float	_DLN_MAX[0]	var	0
3400 float	_DLN_MAX[1]	var	0
3402 float	_DLN_MAX[2]	var	0

3404 float	_DLN_MAX[3]	var	0
3406 float	_THD_ILN_MAX[0]	%	0
3408 float	_THD_ILN_MAX[1]	%	0
3410 float	_THD_ILN_MAX[2]	%	0
3412 float	_TDD_ILN_MAX[0]	%	0
3414 float	_TDD_ILN_MAX[1]	%	0
3416 float	_TDD_ILN_MAX[2]	%	0
3418 float	_IN_MAX	A	0
3420 float	_IG_MAX	A	0
3422 float	_IM_MAX	A	0
3424 float	_IL_RE_MAX[0]	A	0
3426 float	_IL_RE_MAX[1]	A	0
3428 float	_IL_RE_MAX[2]	A	0
3430 float	_IL_IM_MAX[0]	A	0
3432 float	_IL_IM_MAX[1]	A	0
3434 float	_IL_IM_MAX[2]	A	0
3436 float	_FREQ_MIN	Hz	0
3438 float	_N_MIN	V	0
3440 float	_G_MIN	V	0
3442 float	_M_MIN	V	0
3444 float	_ULN_MIN[0]	V	0
3446 float	_ULN_MIN[1]	V	0
3448 float	_ULN_MIN[2]	V	0
3450 float	_ULL_MIN[0]	V	0
3452 float	_ULL_MIN[1]	V	0
3454 float	_ULL_MIN[2]	V	0
3456 float	_COS_PHI_MIN[0]		0
3458 float	_COS_PHI_MIN[1]		0
3460 float	_COS_PHI_MIN[2]		0
3462 float	_COS_PHI_MIN[3]		0
3464 float	_PF_MIN[0]		0
3466 float	_PF_MIN[1]		0
3468 float	_PF_MIN[2]		0
3470 float	_PF_MIN[3]		0
3472 float	_THD_ULN_MIN[0]	%	0
3474 float	_THD_ULN_MIN[1]	%	0
3476 float	_THD_ULN_MIN[2]	%	0
3478 float	_THD_ULL_MIN[0]	%	0
3480 float	_THD_ULL_MIN[1]	%	0
3482 float	_THD_ULL_MIN[2]	%	0
3484 float	_ULN_RE_MIN[0]	V	0
3486 float	_ULN_RE_MIN[1]	V	0
3488 float	_ULN_RE_MIN[2]	V	0
3490 float	_ULN_IM_MIN[0]	V	0
3492 float	_ULN_IM_MIN[1]	V	0
3494 float	_ULN_IM_MIN[2]	V	0
3496 float	_ILN_AVG_MAX[0]	A	0
3498 float	_ILN_AVG_MAX[1]	A	0
3500 float	_ILN_AVG_MAX[2]	A	0
3502 float	_ILN_AVG_MAX[3]	A	0
3504 float	_PLN_AVG_MAX[0]	W	0
3506 float	_PLN_AVG_MAX[1]	W	0
3508 float	_PLN_AVG_MAX[2]	W	0

3510 float	_PLN_AVG_MAX[3]	W	0
3512 dfloat	_EEPROM_TIMER	s	0
3526 short	_FREQ_I	0.01 Hz	0
3527 short	_N_I	100mV	0
3528 short	_G_I	100mV	0
3529 short	_M_I	100mV	0
3530 short	_ULN_I[0]	100mV	0
3531 short	_ULN_I[1]	100mV	0
3532 short	_ULN_I[2]	100mV	0
3533 short	_ULL_I[0]	100mV	0
3534 short	_ULL_I[1]	100mV	0
3535 short	_ULL_I[2]	100mV	0
3536 short	_FFT_UL1_I[0]	100mV	0
3537 short	_FFT_UL1_I[1]	100mV	0
3538 short	_FFT_UL1_I[2]	100mV	0
3539 short	_FFT_UL1_I[3]	100mV	0
3540 short	_FFT_UL1_I[4]	100mV	0
3541 short	_FFT_UL1_I[5]	100mV	0
3542 short	_FFT_UL1_I[6]	100mV	0
3543 short	_FFT_UL1_I[7]	100mV	0
3544 short	_FFT_UL1_I[8]	100mV	0
3545 short	_FFT_UL1_I[9]	100mV	0
3546 short	_FFT_UL1_I[10]	100mV	0
3547 short	_FFT_UL1_I[11]	100mV	0
3548 short	_FFT_UL1_I[12]	100mV	0
3549 short	_FFT_UL1_I[13]	100mV	0
3550 short	_FFT_UL1_I[14]	100mV	0
3551 short	_FFT_UL1_I[15]	100mV	0
3552 short	_FFT_UL1_I[16]	100mV	0
3553 short	_FFT_UL1_I[17]	100mV	0
3554 short	_FFT_UL1_I[18]	100mV	0
3555 short	_FFT_UL1_I[19]	100mV	0
3556 short	_FFT_UL1_I[20]	100mV	0
3557 short	_FFT_UL1_I[21]	100mV	0
3558 short	_FFT_UL1_I[22]	100mV	0
3559 short	_FFT_UL1_I[23]	100mV	0
3560 short	_FFT_UL1_I[24]	100mV	0
3561 short	_FFT_UL1_I[25]	100mV	0
3562 short	_FFT_UL1_I[26]	100mV	0
3563 short	_FFT_UL1_I[27]	100mV	0
3564 short	_FFT_UL1_I[28]	100mV	0
3565 short	_FFT_UL1_I[29]	100mV	0
3566 short	_FFT_UL1_I[30]	100mV	0
3567 short	_FFT_UL1_I[31]	100mV	0
3568 short	_FFT_UL1_I[32]	100mV	0
3569 short	_FFT_UL1_I[33]	100mV	0
3570 short	_FFT_UL1_I[34]	100mV	0
3571 short	_FFT_UL1_I[35]	100mV	0
3572 short	_FFT_UL1_I[36]	100mV	0
3573 short	_FFT_UL1_I[37]	100mV	0
3574 short	_FFT_UL1_I[38]	100mV	0
3575 short	_FFT_UL1_I[39]	100mV	0
3576 short	_FFT_UL2_I[0]	100mV	0

3577 short	_FFT_UL2_I[1]	100mV	0
3578 short	_FFT_UL2_I[2]	100mV	0
3579 short	_FFT_UL2_I[3]	100mV	0
3580 short	_FFT_UL2_I[4]	100mV	0
3581 short	_FFT_UL2_I[5]	100mV	0
3582 short	_FFT_UL2_I[6]	100mV	0
3583 short	_FFT_UL2_I[7]	100mV	0
3584 short	_FFT_UL2_I[8]	100mV	0
3585 short	_FFT_UL2_I[9]	100mV	0
3586 short	_FFT_UL2_I[10]	100mV	0
3587 short	_FFT_UL2_I[11]	100mV	0
3588 short	_FFT_UL2_I[12]	100mV	0
3589 short	_FFT_UL2_I[13]	100mV	0
3590 short	_FFT_UL2_I[14]	100mV	0
3591 short	_FFT_UL2_I[15]	100mV	0
3592 short	_FFT_UL2_I[16]	100mV	0
3593 short	_FFT_UL2_I[17]	100mV	0
3594 short	_FFT_UL2_I[18]	100mV	0
3595 short	_FFT_UL2_I[19]	100mV	0
3596 short	_FFT_UL2_I[20]	100mV	0
3597 short	_FFT_UL2_I[21]	100mV	0
3598 short	_FFT_UL2_I[22]	100mV	0
3599 short	_FFT_UL2_I[23]	100mV	0
3600 short	_FFT_UL2_I[24]	100mV	0
3601 short	_FFT_UL2_I[25]	100mV	0
3602 short	_FFT_UL2_I[26]	100mV	0
3603 short	_FFT_UL2_I[27]	100mV	0
3604 short	_FFT_UL2_I[28]	100mV	0
3605 short	_FFT_UL2_I[29]	100mV	0
3606 short	_FFT_UL2_I[30]	100mV	0
3607 short	_FFT_UL2_I[31]	100mV	0
3608 short	_FFT_UL2_I[32]	100mV	0
3609 short	_FFT_UL2_I[33]	100mV	0
3610 short	_FFT_UL2_I[34]	100mV	0
3611 short	_FFT_UL2_I[35]	100mV	0
3612 short	_FFT_UL2_I[36]	100mV	0
3613 short	_FFT_UL2_I[37]	100mV	0
3614 short	_FFT_UL2_I[38]	100mV	0
3615 short	_FFT_UL2_I[39]	100mV	0
3616 short	_FFT_UL3_I[0]	100mV	0
3617 short	_FFT_UL3_I[1]	100mV	0
3618 short	_FFT_UL3_I[2]	100mV	0
3619 short	_FFT_UL3_I[3]	100mV	0
3620 short	_FFT_UL3_I[4]	100mV	0
3621 short	_FFT_UL3_I[5]	100mV	0
3622 short	_FFT_UL3_I[6]	100mV	0
3623 short	_FFT_UL3_I[7]	100mV	0
3624 short	_FFT_UL3_I[8]	100mV	0
3625 short	_FFT_UL3_I[9]	100mV	0
3626 short	_FFT_UL3_I[10]	100mV	0
3627 short	_FFT_UL3_I[11]	100mV	0
3628 short	_FFT_UL3_I[12]	100mV	0
3629 short	_FFT_UL3_I[13]	100mV	0

3630 short	_FFT_UL3_I[14]	100mV	0
3631 short	_FFT_UL3_I[15]	100mV	0
3632 short	_FFT_UL3_I[16]	100mV	0
3633 short	_FFT_UL3_I[17]	100mV	0
3634 short	_FFT_UL3_I[18]	100mV	0
3635 short	_FFT_UL3_I[19]	100mV	0
3636 short	_FFT_UL3_I[20]	100mV	0
3637 short	_FFT_UL3_I[21]	100mV	0
3638 short	_FFT_UL3_I[22]	100mV	0
3639 short	_FFT_UL3_I[23]	100mV	0
3640 short	_FFT_UL3_I[24]	100mV	0
3641 short	_FFT_UL3_I[25]	100mV	0
3642 short	_FFT_UL3_I[26]	100mV	0
3643 short	_FFT_UL3_I[27]	100mV	0
3644 short	_FFT_UL3_I[28]	100mV	0
3645 short	_FFT_UL3_I[29]	100mV	0
3646 short	_FFT_UL3_I[30]	100mV	0
3647 short	_FFT_UL3_I[31]	100mV	0
3648 short	_FFT_UL3_I[32]	100mV	0
3649 short	_FFT_UL3_I[33]	100mV	0
3650 short	_FFT_UL3_I[34]	100mV	0
3651 short	_FFT_UL3_I[35]	100mV	0
3652 short	_FFT_UL3_I[36]	100mV	0
3653 short	_FFT_UL3_I[37]	100mV	0
3654 short	_FFT_UL3_I[38]	100mV	0
3655 short	_FFT_UL3_I[39]	100mV	0
3656 short	_FFT_ULL1_I[0]	100mV	0
3657 short	_FFT_ULL1_I[1]	100mV	0
3658 short	_FFT_ULL1_I[2]	100mV	0
3659 short	_FFT_ULL1_I[3]	100mV	0
3660 short	_FFT_ULL1_I[4]	100mV	0
3661 short	_FFT_ULL1_I[5]	100mV	0
3662 short	_FFT_ULL1_I[6]	100mV	0
3663 short	_FFT_ULL1_I[7]	100mV	0
3664 short	_FFT_ULL1_I[8]	100mV	0
3665 short	_FFT_ULL1_I[9]	100mV	0
3666 short	_FFT_ULL1_I[10]	100mV	0
3667 short	_FFT_ULL1_I[11]	100mV	0
3668 short	_FFT_ULL1_I[12]	100mV	0
3669 short	_FFT_ULL1_I[13]	100mV	0
3670 short	_FFT_ULL1_I[14]	100mV	0
3671 short	_FFT_ULL1_I[15]	100mV	0
3672 short	_FFT_ULL1_I[16]	100mV	0
3673 short	_FFT_ULL1_I[17]	100mV	0
3674 short	_FFT_ULL1_I[18]	100mV	0
3675 short	_FFT_ULL1_I[19]	100mV	0
3676 short	_FFT_ULL1_I[20]	100mV	0
3677 short	_FFT_ULL1_I[21]	100mV	0
3678 short	_FFT_ULL1_I[22]	100mV	0
3679 short	_FFT_ULL1_I[23]	100mV	0
3680 short	_FFT_ULL1_I[24]	100mV	0
3681 short	_FFT_ULL1_I[25]	100mV	0
3682 short	_FFT_ULL1_I[26]	100mV	0

3683 short	_FFT_ULL1_I[27]	100mV	0
3684 short	_FFT_ULL1_I[28]	100mV	0
3685 short	_FFT_ULL1_I[29]	100mV	0
3686 short	_FFT_ULL1_I[30]	100mV	0
3687 short	_FFT_ULL1_I[31]	100mV	0
3688 short	_FFT_ULL1_I[32]	100mV	0
3689 short	_FFT_ULL1_I[33]	100mV	0
3690 short	_FFT_ULL1_I[34]	100mV	0
3691 short	_FFT_ULL1_I[35]	100mV	0
3692 short	_FFT_ULL1_I[36]	100mV	0
3693 short	_FFT_ULL1_I[37]	100mV	0
3694 short	_FFT_ULL1_I[38]	100mV	0
3695 short	_FFT_ULL1_I[39]	100mV	0
3696 short	_FFT_ULL2_I[0]	100mV	0
3697 short	_FFT_ULL2_I[1]	100mV	0
3698 short	_FFT_ULL2_I[2]	100mV	0
3699 short	_FFT_ULL2_I[3]	100mV	0
3700 short	_FFT_ULL2_I[4]	100mV	0
3701 short	_FFT_ULL2_I[5]	100mV	0
3702 short	_FFT_ULL2_I[6]	100mV	0
3703 short	_FFT_ULL2_I[7]	100mV	0
3704 short	_FFT_ULL2_I[8]	100mV	0
3705 short	_FFT_ULL2_I[9]	100mV	0
3706 short	_FFT_ULL2_I[10]	100mV	0
3707 short	_FFT_ULL2_I[11]	100mV	0
3708 short	_FFT_ULL2_I[12]	100mV	0
3709 short	_FFT_ULL2_I[13]	100mV	0
3710 short	_FFT_ULL2_I[14]	100mV	0
3711 short	_FFT_ULL2_I[15]	100mV	0
3712 short	_FFT_ULL2_I[16]	100mV	0
3713 short	_FFT_ULL2_I[17]	100mV	0
3714 short	_FFT_ULL2_I[18]	100mV	0
3715 short	_FFT_ULL2_I[19]	100mV	0
3716 short	_FFT_ULL2_I[20]	100mV	0
3717 short	_FFT_ULL2_I[21]	100mV	0
3718 short	_FFT_ULL2_I[22]	100mV	0
3719 short	_FFT_ULL2_I[23]	100mV	0
3720 short	_FFT_ULL2_I[24]	100mV	0
3721 short	_FFT_ULL2_I[25]	100mV	0
3722 short	_FFT_ULL2_I[26]	100mV	0
3723 short	_FFT_ULL2_I[27]	100mV	0
3724 short	_FFT_ULL2_I[28]	100mV	0
3725 short	_FFT_ULL2_I[29]	100mV	0
3726 short	_FFT_ULL2_I[30]	100mV	0
3727 short	_FFT_ULL2_I[31]	100mV	0
3728 short	_FFT_ULL2_I[32]	100mV	0
3729 short	_FFT_ULL2_I[33]	100mV	0
3730 short	_FFT_ULL2_I[34]	100mV	0
3731 short	_FFT_ULL2_I[35]	100mV	0
3732 short	_FFT_ULL2_I[36]	100mV	0
3733 short	_FFT_ULL2_I[37]	100mV	0
3734 short	_FFT_ULL2_I[38]	100mV	0
3735 short	_FFT_ULL2_I[39]	100mV	0

3736 short	_FFT_ULL3_I[0]	100mV	0
3737 short	_FFT_ULL3_I[1]	100mV	0
3738 short	_FFT_ULL3_I[2]	100mV	0
3739 short	_FFT_ULL3_I[3]	100mV	0
3740 short	_FFT_ULL3_I[4]	100mV	0
3741 short	_FFT_ULL3_I[5]	100mV	0
3742 short	_FFT_ULL3_I[6]	100mV	0
3743 short	_FFT_ULL3_I[7]	100mV	0
3744 short	_FFT_ULL3_I[8]	100mV	0
3745 short	_FFT_ULL3_I[9]	100mV	0
3746 short	_FFT_ULL3_I[10]	100mV	0
3747 short	_FFT_ULL3_I[11]	100mV	0
3748 short	_FFT_ULL3_I[12]	100mV	0
3749 short	_FFT_ULL3_I[13]	100mV	0
3750 short	_FFT_ULL3_I[14]	100mV	0
3751 short	_FFT_ULL3_I[15]	100mV	0
3752 short	_FFT_ULL3_I[16]	100mV	0
3753 short	_FFT_ULL3_I[17]	100mV	0
3754 short	_FFT_ULL3_I[18]	100mV	0
3755 short	_FFT_ULL3_I[19]	100mV	0
3756 short	_FFT_ULL3_I[20]	100mV	0
3757 short	_FFT_ULL3_I[21]	100mV	0
3758 short	_FFT_ULL3_I[22]	100mV	0
3759 short	_FFT_ULL3_I[23]	100mV	0
3760 short	_FFT_ULL3_I[24]	100mV	0
3761 short	_FFT_ULL3_I[25]	100mV	0
3762 short	_FFT_ULL3_I[26]	100mV	0
3763 short	_FFT_ULL3_I[27]	100mV	0
3764 short	_FFT_ULL3_I[28]	100mV	0
3765 short	_FFT_ULL3_I[29]	100mV	0
3766 short	_FFT_ULL3_I[30]	100mV	0
3767 short	_FFT_ULL3_I[31]	100mV	0
3768 short	_FFT_ULL3_I[32]	100mV	0
3769 short	_FFT_ULL3_I[33]	100mV	0
3770 short	_FFT_ULL3_I[34]	100mV	0
3771 short	_FFT_ULL3_I[35]	100mV	0
3772 short	_FFT_ULL3_I[36]	100mV	0
3773 short	_FFT_ULL3_I[37]	100mV	0
3774 short	_FFT_ULL3_I[38]	100mV	0
3775 short	_FFT_ULL3_I[39]	100mV	0
3776 short	_COS_PHI_I[0]		0
3777 short	_COS_PHI_I[1]		0
3778 short	_COS_PHI_I[2]		0
3779 short	_COS_PHI_I[3]		0
3780 short	_PF_I[0]		0
3781 short	_PF_I[1]		0
3782 short	_PF_I[2]		0
3783 short	_PF_I[3]		0
3784 short	_THD_ULN_I[0]	%	0
3785 short	_THD_ULN_I[1]	%	0
3786 short	_THD_ULN_I[2]	%	0
3787 short	_THD_ULL_I[0]	%	0
3788 short	_THD_ULL_I[1]	%	0

3789 short	_THD_ULL_I[2]	%	0
3790 short	_ULN_RE_I[0]	100mV	0
3791 short	_ULN_RE_I[1]	100mV	0
3792 short	_ULN_RE_I[2]	100mV	0
3793 short	_ULN_IM_I[0]	100mV	0
3794 short	_ULN_IM_I[1]	100mV	0
3795 short	_ULN_IM_I[2]	100mV	0
3796 short	_FFT_IL1_I[0]	mA	0
3797 short	_FFT_IL1_I[1]	mA	0
3798 short	_FFT_IL1_I[2]	mA	0
3799 short	_FFT_IL1_I[3]	mA	0
3800 short	_FFT_IL1_I[4]	mA	0
3801 short	_FFT_IL1_I[5]	mA	0
3802 short	_FFT_IL1_I[6]	mA	0
3803 short	_FFT_IL1_I[7]	mA	0
3804 short	_FFT_IL1_I[8]	mA	0
3805 short	_FFT_IL1_I[9]	mA	0
3806 short	_FFT_IL1_I[10]	mA	0
3807 short	_FFT_IL1_I[11]	mA	0
3808 short	_FFT_IL1_I[12]	mA	0
3809 short	_FFT_IL1_I[13]	mA	0
3810 short	_FFT_IL1_I[14]	mA	0
3811 short	_FFT_IL1_I[15]	mA	0
3812 short	_FFT_IL1_I[16]	mA	0
3813 short	_FFT_IL1_I[17]	mA	0
3814 short	_FFT_IL1_I[18]	mA	0
3815 short	_FFT_IL1_I[19]	mA	0
3816 short	_FFT_IL1_I[20]	mA	0
3817 short	_FFT_IL1_I[21]	mA	0
3818 short	_FFT_IL1_I[22]	mA	0
3819 short	_FFT_IL1_I[23]	mA	0
3820 short	_FFT_IL1_I[24]	mA	0
3821 short	_FFT_IL1_I[25]	mA	0
3822 short	_FFT_IL1_I[26]	mA	0
3823 short	_FFT_IL1_I[27]	mA	0
3824 short	_FFT_IL1_I[28]	mA	0
3825 short	_FFT_IL1_I[29]	mA	0
3826 short	_FFT_IL1_I[30]	mA	0
3827 short	_FFT_IL1_I[31]	mA	0
3828 short	_FFT_IL1_I[32]	mA	0
3829 short	_FFT_IL1_I[33]	mA	0
3830 short	_FFT_IL1_I[34]	mA	0
3831 short	_FFT_IL1_I[35]	mA	0
3832 short	_FFT_IL1_I[36]	mA	0
3833 short	_FFT_IL1_I[37]	mA	0
3834 short	_FFT_IL1_I[38]	mA	0
3835 short	_FFT_IL1_I[39]	mA	0
3836 short	_FFT_IL2_I[0]	mA	0
3837 short	_FFT_IL2_I[1]	mA	0
3838 short	_FFT_IL2_I[2]	mA	0
3839 short	_FFT_IL2_I[3]	mA	0
3840 short	_FFT_IL2_I[4]	mA	0
3841 short	_FFT_IL2_I[5]	mA	0

3842 short	_FFT_IL2_I[6]	mA	0
3843 short	_FFT_IL2_I[7]	mA	0
3844 short	_FFT_IL2_I[8]	mA	0
3845 short	_FFT_IL2_I[9]	mA	0
3846 short	_FFT_IL2_I[10]	mA	0
3847 short	_FFT_IL2_I[11]	mA	0
3848 short	_FFT_IL2_I[12]	mA	0
3849 short	_FFT_IL2_I[13]	mA	0
3850 short	_FFT_IL2_I[14]	mA	0
3851 short	_FFT_IL2_I[15]	mA	0
3852 short	_FFT_IL2_I[16]	mA	0
3853 short	_FFT_IL2_I[17]	mA	0
3854 short	_FFT_IL2_I[18]	mA	0
3855 short	_FFT_IL2_I[19]	mA	0
3856 short	_FFT_IL2_I[20]	mA	0
3857 short	_FFT_IL2_I[21]	mA	0
3858 short	_FFT_IL2_I[22]	mA	0
3859 short	_FFT_IL2_I[23]	mA	0
3860 short	_FFT_IL2_I[24]	mA	0
3861 short	_FFT_IL2_I[25]	mA	0
3862 short	_FFT_IL2_I[26]	mA	0
3863 short	_FFT_IL2_I[27]	mA	0
3864 short	_FFT_IL2_I[28]	mA	0
3865 short	_FFT_IL2_I[29]	mA	0
3866 short	_FFT_IL2_I[30]	mA	0
3867 short	_FFT_IL2_I[31]	mA	0
3868 short	_FFT_IL2_I[32]	mA	0
3869 short	_FFT_IL2_I[33]	mA	0
3870 short	_FFT_IL2_I[34]	mA	0
3871 short	_FFT_IL2_I[35]	mA	0
3872 short	_FFT_IL2_I[36]	mA	0
3873 short	_FFT_IL2_I[37]	mA	0
3874 short	_FFT_IL2_I[38]	mA	0
3875 short	_FFT_IL2_I[39]	mA	0
3876 short	_FFT_IL3_I[0]	mA	0
3877 short	_FFT_IL3_I[1]	mA	0
3878 short	_FFT_IL3_I[2]	mA	0
3879 short	_FFT_IL3_I[3]	mA	0
3880 short	_FFT_IL3_I[4]	mA	0
3881 short	_FFT_IL3_I[5]	mA	0
3882 short	_FFT_IL3_I[6]	mA	0
3883 short	_FFT_IL3_I[7]	mA	0
3884 short	_FFT_IL3_I[8]	mA	0
3885 short	_FFT_IL3_I[9]	mA	0
3886 short	_FFT_IL3_I[10]	mA	0
3887 short	_FFT_IL3_I[11]	mA	0
3888 short	_FFT_IL3_I[12]	mA	0
3889 short	_FFT_IL3_I[13]	mA	0
3890 short	_FFT_IL3_I[14]	mA	0
3891 short	_FFT_IL3_I[15]	mA	0
3892 short	_FFT_IL3_I[16]	mA	0
3893 short	_FFT_IL3_I[17]	mA	0
3894 short	_FFT_IL3_I[18]	mA	0

3895 short	_FFT_IL3_I[19]	mA	0
3896 short	_FFT_IL3_I[20]	mA	0
3897 short	_FFT_IL3_I[21]	mA	0
3898 short	_FFT_IL3_I[22]	mA	0
3899 short	_FFT_IL3_I[23]	mA	0
3900 short	_FFT_IL3_I[24]	mA	0
3901 short	_FFT_IL3_I[25]	mA	0
3902 short	_FFT_IL3_I[26]	mA	0
3903 short	_FFT_IL3_I[27]	mA	0
3904 short	_FFT_IL3_I[28]	mA	0
3905 short	_FFT_IL3_I[29]	mA	0
3906 short	_FFT_IL3_I[30]	mA	0
3907 short	_FFT_IL3_I[31]	mA	0
3908 short	_FFT_IL3_I[32]	mA	0
3909 short	_FFT_IL3_I[33]	mA	0
3910 short	_FFT_IL3_I[34]	mA	0
3911 short	_FFT_IL3_I[35]	mA	0
3912 short	_FFT_IL3_I[36]	mA	0
3913 short	_FFT_IL3_I[37]	mA	0
3914 short	_FFT_IL3_I[38]	mA	0
3915 short	_FFT_IL3_I[39]	mA	0
3916 short	_ILN_I[0]	mA	0
3917 short	_ILN_I[1]	mA	0
3918 short	_ILN_I[2]	mA	0
3919 short	_ILN_I[3]	mA	0
3920 short	_PLN_I[0]	100mW	0
3921 short	_PLN_I[1]	100mW	0
3922 short	_PLN_I[2]	100mW	0
3923 short	_PLN_I[3]	100mW	0
3924 short	_QLN_I[0]	100mvar	0
3925 short	_QLN_I[1]	100mvar	0
3926 short	_QLN_I[2]	100mvar	0
3927 short	_QLN_I[3]	100mvar	0
3928 short	_SLN_I[0]	100mVA	0
3929 short	_SLN_I[1]	100mVA	0
3930 short	_SLN_I[2]	100mVA	0
3931 short	_SLN_I[3]	100mVA	0
3932 short	_P0_I[0]	100mW	0
3933 short	_P0_I[1]	100mW	0
3934 short	_P0_I[2]	100mW	0
3935 short	_P0_I[3]	100mW	0
3936 short	_DLN_I[0]	100mvar	0
3937 short	_DLN_I[1]	100mvar	0
3938 short	_DLN_I[2]	100mvar	0
3939 short	_DLN_I[3]	100mvar	0
3940 short	_THD_ILN_I[0]	%	0
3941 short	_THD_ILN_I[1]	%	0
3942 short	_THD_ILN_I[2]	%	0
3943 short	_TDD_ILN_I[0]	%	0
3944 short	_TDD_ILN_I[1]	%	0
3945 short	_TDD_ILN_I[2]	%	0
3946 short	_IN_I	mA	0
3947 short	_IG_I	mA	0

3948 short	_IM_I	mA	0
3949 short	_IL_RE_I[0]	mA	0
3950 short	_IL_RE_I[1]	mA	0
3951 short	_IL_RE_I[2]	mA	0
3952 short	_IL_IM_I[0]	mA	0
3953 short	_IL_IM_I[1]	mA	0
3954 short	_IL_IM_I[2]	mA	0
3955 short	_PHASE_SEQ_I		0
3956 short	_FREQ_AVG_I	Hz	0
3957 short	_N_AVG_I	100mV	0
3958 short	_G_AVG_I	100mV	0
3959 short	_M_AVG_I	100mV	0
3960 short	_ULN_AVG_I[0]	100mV	0
3961 short	_ULN_AVG_I[1]	100mV	0
3962 short	_ULN_AVG_I[2]	100mV	0
3963 short	_ULL_AVG_I[0]	100mV	0
3964 short	_ULL_AVG_I[1]	100mV	0
3965 short	_ULL_AVG_I[2]	100mV	0
3966 short	_FFT_UL1_AVG_I[0]	100mV	0
3967 short	_FFT_UL1_AVG_I[1]	100mV	0
3968 short	_FFT_UL1_AVG_I[2]	100mV	0
3969 short	_FFT_UL1_AVG_I[3]	100mV	0
3970 short	_FFT_UL1_AVG_I[4]	100mV	0
3971 short	_FFT_UL1_AVG_I[5]	100mV	0
3972 short	_FFT_UL1_AVG_I[6]	100mV	0
3973 short	_FFT_UL1_AVG_I[7]	100mV	0
3974 short	_FFT_UL1_AVG_I[8]	100mV	0
3975 short	_FFT_UL1_AVG_I[9]	100mV	0
3976 short	_FFT_UL1_AVG_I[10]	100mV	0
3977 short	_FFT_UL1_AVG_I[11]	100mV	0
3978 short	_FFT_UL1_AVG_I[12]	100mV	0
3979 short	_FFT_UL1_AVG_I[13]	100mV	0
3980 short	_FFT_UL1_AVG_I[14]	100mV	0
3981 short	_FFT_UL1_AVG_I[15]	100mV	0
3982 short	_FFT_UL1_AVG_I[16]	100mV	0
3983 short	_FFT_UL1_AVG_I[17]	100mV	0
3984 short	_FFT_UL1_AVG_I[18]	100mV	0
3985 short	_FFT_UL1_AVG_I[19]	100mV	0
3986 short	_FFT_UL1_AVG_I[20]	100mV	0
3987 short	_FFT_UL1_AVG_I[21]	100mV	0
3988 short	_FFT_UL1_AVG_I[22]	100mV	0
3989 short	_FFT_UL1_AVG_I[23]	100mV	0
3990 short	_FFT_UL1_AVG_I[24]	100mV	0
3991 short	_FFT_UL1_AVG_I[25]	100mV	0
3992 short	_FFT_UL1_AVG_I[26]	100mV	0
3993 short	_FFT_UL1_AVG_I[27]	100mV	0
3994 short	_FFT_UL1_AVG_I[28]	100mV	0
3995 short	_FFT_UL1_AVG_I[29]	100mV	0
3996 short	_FFT_UL1_AVG_I[30]	100mV	0
3997 short	_FFT_UL1_AVG_I[31]	100mV	0
3998 short	_FFT_UL1_AVG_I[32]	100mV	0
3999 short	_FFT_UL1_AVG_I[33]	100mV	0
4000 short	_FFT_UL1_AVG_I[34]	100mV	0

4001 short	_FFT_UL1_AVG_I[35]	100mV	0
4002 short	_FFT_UL1_AVG_I[36]	100mV	0
4003 short	_FFT_UL1_AVG_I[37]	100mV	0
4004 short	_FFT_UL1_AVG_I[38]	100mV	0
4005 short	_FFT_UL1_AVG_I[39]	100mV	0
4006 short	_FFT_UL2_AVG_I[0]	100mV	0
4007 short	_FFT_UL2_AVG_I[1]	100mV	0
4008 short	_FFT_UL2_AVG_I[2]	100mV	0
4009 short	_FFT_UL2_AVG_I[3]	100mV	0
4010 short	_FFT_UL2_AVG_I[4]	100mV	0
4011 short	_FFT_UL2_AVG_I[5]	100mV	0
4012 short	_FFT_UL2_AVG_I[6]	100mV	0
4013 short	_FFT_UL2_AVG_I[7]	100mV	0
4014 short	_FFT_UL2_AVG_I[8]	100mV	0
4015 short	_FFT_UL2_AVG_I[9]	100mV	0
4016 short	_FFT_UL2_AVG_I[10]	100mV	0
4017 short	_FFT_UL2_AVG_I[11]	100mV	0
4018 short	_FFT_UL2_AVG_I[12]	100mV	0
4019 short	_FFT_UL2_AVG_I[13]	100mV	0
4020 short	_FFT_UL2_AVG_I[14]	100mV	0
4021 short	_FFT_UL2_AVG_I[15]	100mV	0
4022 short	_FFT_UL2_AVG_I[16]	100mV	0
4023 short	_FFT_UL2_AVG_I[17]	100mV	0
4024 short	_FFT_UL2_AVG_I[18]	100mV	0
4025 short	_FFT_UL2_AVG_I[19]	100mV	0
4026 short	_FFT_UL2_AVG_I[20]	100mV	0
4027 short	_FFT_UL2_AVG_I[21]	100mV	0
4028 short	_FFT_UL2_AVG_I[22]	100mV	0
4029 short	_FFT_UL2_AVG_I[23]	100mV	0
4030 short	_FFT_UL2_AVG_I[24]	100mV	0
4031 short	_FFT_UL2_AVG_I[25]	100mV	0
4032 short	_FFT_UL2_AVG_I[26]	100mV	0
4033 short	_FFT_UL2_AVG_I[27]	100mV	0
4034 short	_FFT_UL2_AVG_I[28]	100mV	0
4035 short	_FFT_UL2_AVG_I[29]	100mV	0
4036 short	_FFT_UL2_AVG_I[30]	100mV	0
4037 short	_FFT_UL2_AVG_I[31]	100mV	0
4038 short	_FFT_UL2_AVG_I[32]	100mV	0
4039 short	_FFT_UL2_AVG_I[33]	100mV	0
4040 short	_FFT_UL2_AVG_I[34]	100mV	0
4041 short	_FFT_UL2_AVG_I[35]	100mV	0
4042 short	_FFT_UL2_AVG_I[36]	100mV	0
4043 short	_FFT_UL2_AVG_I[37]	100mV	0
4044 short	_FFT_UL2_AVG_I[38]	100mV	0
4045 short	_FFT_UL2_AVG_I[39]	100mV	0
4046 short	_FFT_UL3_AVG_I[0]	100mV	0
4047 short	_FFT_UL3_AVG_I[1]	100mV	0
4048 short	_FFT_UL3_AVG_I[2]	100mV	0
4049 short	_FFT_UL3_AVG_I[3]	100mV	0
4050 short	_FFT_UL3_AVG_I[4]	100mV	0
4051 short	_FFT_UL3_AVG_I[5]	100mV	0
4052 short	_FFT_UL3_AVG_I[6]	100mV	0
4053 short	_FFT_UL3_AVG_I[7]	100mV	0

4054 short	_FFT_UL3_AVG_I[8]	100mV	0
4055 short	_FFT_UL3_AVG_I[9]	100mV	0
4056 short	_FFT_UL3_AVG_I[10]	100mV	0
4057 short	_FFT_UL3_AVG_I[11]	100mV	0
4058 short	_FFT_UL3_AVG_I[12]	100mV	0
4059 short	_FFT_UL3_AVG_I[13]	100mV	0
4060 short	_FFT_UL3_AVG_I[14]	100mV	0
4061 short	_FFT_UL3_AVG_I[15]	100mV	0
4062 short	_FFT_UL3_AVG_I[16]	100mV	0
4063 short	_FFT_UL3_AVG_I[17]	100mV	0
4064 short	_FFT_UL3_AVG_I[18]	100mV	0
4065 short	_FFT_UL3_AVG_I[19]	100mV	0
4066 short	_FFT_UL3_AVG_I[20]	100mV	0
4067 short	_FFT_UL3_AVG_I[21]	100mV	0
4068 short	_FFT_UL3_AVG_I[22]	100mV	0
4069 short	_FFT_UL3_AVG_I[23]	100mV	0
4070 short	_FFT_UL3_AVG_I[24]	100mV	0
4071 short	_FFT_UL3_AVG_I[25]	100mV	0
4072 short	_FFT_UL3_AVG_I[26]	100mV	0
4073 short	_FFT_UL3_AVG_I[27]	100mV	0
4074 short	_FFT_UL3_AVG_I[28]	100mV	0
4075 short	_FFT_UL3_AVG_I[29]	100mV	0
4076 short	_FFT_UL3_AVG_I[30]	100mV	0
4077 short	_FFT_UL3_AVG_I[31]	100mV	0
4078 short	_FFT_UL3_AVG_I[32]	100mV	0
4079 short	_FFT_UL3_AVG_I[33]	100mV	0
4080 short	_FFT_UL3_AVG_I[34]	100mV	0
4081 short	_FFT_UL3_AVG_I[35]	100mV	0
4082 short	_FFT_UL3_AVG_I[36]	100mV	0
4083 short	_FFT_UL3_AVG_I[37]	100mV	0
4084 short	_FFT_UL3_AVG_I[38]	100mV	0
4085 short	_FFT_UL3_AVG_I[39]	100mV	0
4086 short	_FFT_ULL1_AVG_I[0]	100mV	0
4087 short	_FFT_ULL1_AVG_I[1]	100mV	0
4088 short	_FFT_ULL1_AVG_I[2]	100mV	0
4089 short	_FFT_ULL1_AVG_I[3]	100mV	0
4090 short	_FFT_ULL1_AVG_I[4]	100mV	0
4091 short	_FFT_ULL1_AVG_I[5]	100mV	0
4092 short	_FFT_ULL1_AVG_I[6]	100mV	0
4093 short	_FFT_ULL1_AVG_I[7]	100mV	0
4094 short	_FFT_ULL1_AVG_I[8]	100mV	0
4095 short	_FFT_ULL1_AVG_I[9]	100mV	0
4096 short	_FFT_ULL1_AVG_I[10]	100mV	0
4097 short	_FFT_ULL1_AVG_I[11]	100mV	0
4098 short	_FFT_ULL1_AVG_I[12]	100mV	0
4099 short	_FFT_ULL1_AVG_I[13]	100mV	0
4100 short	_FFT_ULL1_AVG_I[14]	100mV	0
4101 short	_FFT_ULL1_AVG_I[15]	100mV	0
4102 short	_FFT_ULL1_AVG_I[16]	100mV	0
4103 short	_FFT_ULL1_AVG_I[17]	100mV	0
4104 short	_FFT_ULL1_AVG_I[18]	100mV	0
4105 short	_FFT_ULL1_AVG_I[19]	100mV	0
4106 short	_FFT_ULL1_AVG_I[20]	100mV	0

4107 short	_FFT_ULL1_AVG_I[21]	100mV	0
4108 short	_FFT_ULL1_AVG_I[22]	100mV	0
4109 short	_FFT_ULL1_AVG_I[23]	100mV	0
4110 short	_FFT_ULL1_AVG_I[24]	100mV	0
4111 short	_FFT_ULL1_AVG_I[25]	100mV	0
4112 short	_FFT_ULL1_AVG_I[26]	100mV	0
4113 short	_FFT_ULL1_AVG_I[27]	100mV	0
4114 short	_FFT_ULL1_AVG_I[28]	100mV	0
4115 short	_FFT_ULL1_AVG_I[29]	100mV	0
4116 short	_FFT_ULL1_AVG_I[30]	100mV	0
4117 short	_FFT_ULL1_AVG_I[31]	100mV	0
4118 short	_FFT_ULL1_AVG_I[32]	100mV	0
4119 short	_FFT_ULL1_AVG_I[33]	100mV	0
4120 short	_FFT_ULL1_AVG_I[34]	100mV	0
4121 short	_FFT_ULL1_AVG_I[35]	100mV	0
4122 short	_FFT_ULL1_AVG_I[36]	100mV	0
4123 short	_FFT_ULL1_AVG_I[37]	100mV	0
4124 short	_FFT_ULL1_AVG_I[38]	100mV	0
4125 short	_FFT_ULL1_AVG_I[39]	100mV	0
4126 short	_FFT_ULL2_AVG_I[0]	100mV	0
4127 short	_FFT_ULL2_AVG_I[1]	100mV	0
4128 short	_FFT_ULL2_AVG_I[2]	100mV	0
4129 short	_FFT_ULL2_AVG_I[3]	100mV	0
4130 short	_FFT_ULL2_AVG_I[4]	100mV	0
4131 short	_FFT_ULL2_AVG_I[5]	100mV	0
4132 short	_FFT_ULL2_AVG_I[6]	100mV	0
4133 short	_FFT_ULL2_AVG_I[7]	100mV	0
4134 short	_FFT_ULL2_AVG_I[8]	100mV	0
4135 short	_FFT_ULL2_AVG_I[9]	100mV	0
4136 short	_FFT_ULL2_AVG_I[10]	100mV	0
4137 short	_FFT_ULL2_AVG_I[11]	100mV	0
4138 short	_FFT_ULL2_AVG_I[12]	100mV	0
4139 short	_FFT_ULL2_AVG_I[13]	100mV	0
4140 short	_FFT_ULL2_AVG_I[14]	100mV	0
4141 short	_FFT_ULL2_AVG_I[15]	100mV	0
4142 short	_FFT_ULL2_AVG_I[16]	100mV	0
4143 short	_FFT_ULL2_AVG_I[17]	100mV	0
4144 short	_FFT_ULL2_AVG_I[18]	100mV	0
4145 short	_FFT_ULL2_AVG_I[19]	100mV	0
4146 short	_FFT_ULL2_AVG_I[20]	100mV	0
4147 short	_FFT_ULL2_AVG_I[21]	100mV	0
4148 short	_FFT_ULL2_AVG_I[22]	100mV	0
4149 short	_FFT_ULL2_AVG_I[23]	100mV	0
4150 short	_FFT_ULL2_AVG_I[24]	100mV	0
4151 short	_FFT_ULL2_AVG_I[25]	100mV	0
4152 short	_FFT_ULL2_AVG_I[26]	100mV	0
4153 short	_FFT_ULL2_AVG_I[27]	100mV	0
4154 short	_FFT_ULL2_AVG_I[28]	100mV	0
4155 short	_FFT_ULL2_AVG_I[29]	100mV	0
4156 short	_FFT_ULL2_AVG_I[30]	100mV	0
4157 short	_FFT_ULL2_AVG_I[31]	100mV	0
4158 short	_FFT_ULL2_AVG_I[32]	100mV	0
4159 short	_FFT_ULL2_AVG_I[33]	100mV	0

4160 short	_FFT_ULL2_AVG_I[34]	100mV	0
4161 short	_FFT_ULL2_AVG_I[35]	100mV	0
4162 short	_FFT_ULL2_AVG_I[36]	100mV	0
4163 short	_FFT_ULL2_AVG_I[37]	100mV	0
4164 short	_FFT_ULL2_AVG_I[38]	100mV	0
4165 short	_FFT_ULL2_AVG_I[39]	100mV	0
4166 short	_FFT_ULL3_AVG_I[0]	100mV	0
4167 short	_FFT_ULL3_AVG_I[1]	100mV	0
4168 short	_FFT_ULL3_AVG_I[2]	100mV	0
4169 short	_FFT_ULL3_AVG_I[3]	100mV	0
4170 short	_FFT_ULL3_AVG_I[4]	100mV	0
4171 short	_FFT_ULL3_AVG_I[5]	100mV	0
4172 short	_FFT_ULL3_AVG_I[6]	100mV	0
4173 short	_FFT_ULL3_AVG_I[7]	100mV	0
4174 short	_FFT_ULL3_AVG_I[8]	100mV	0
4175 short	_FFT_ULL3_AVG_I[9]	100mV	0
4176 short	_FFT_ULL3_AVG_I[10]	100mV	0
4177 short	_FFT_ULL3_AVG_I[11]	100mV	0
4178 short	_FFT_ULL3_AVG_I[12]	100mV	0
4179 short	_FFT_ULL3_AVG_I[13]	100mV	0
4180 short	_FFT_ULL3_AVG_I[14]	100mV	0
4181 short	_FFT_ULL3_AVG_I[15]	100mV	0
4182 short	_FFT_ULL3_AVG_I[16]	100mV	0
4183 short	_FFT_ULL3_AVG_I[17]	100mV	0
4184 short	_FFT_ULL3_AVG_I[18]	100mV	0
4185 short	_FFT_ULL3_AVG_I[19]	100mV	0
4186 short	_FFT_ULL3_AVG_I[20]	100mV	0
4187 short	_FFT_ULL3_AVG_I[21]	100mV	0
4188 short	_FFT_ULL3_AVG_I[22]	100mV	0
4189 short	_FFT_ULL3_AVG_I[23]	100mV	0
4190 short	_FFT_ULL3_AVG_I[24]	100mV	0
4191 short	_FFT_ULL3_AVG_I[25]	100mV	0
4192 short	_FFT_ULL3_AVG_I[26]	100mV	0
4193 short	_FFT_ULL3_AVG_I[27]	100mV	0
4194 short	_FFT_ULL3_AVG_I[28]	100mV	0
4195 short	_FFT_ULL3_AVG_I[29]	100mV	0
4196 short	_FFT_ULL3_AVG_I[30]	100mV	0
4197 short	_FFT_ULL3_AVG_I[31]	100mV	0
4198 short	_FFT_ULL3_AVG_I[32]	100mV	0
4199 short	_FFT_ULL3_AVG_I[33]	100mV	0
4200 short	_FFT_ULL3_AVG_I[34]	100mV	0
4201 short	_FFT_ULL3_AVG_I[35]	100mV	0
4202 short	_FFT_ULL3_AVG_I[36]	100mV	0
4203 short	_FFT_ULL3_AVG_I[37]	100mV	0
4204 short	_FFT_ULL3_AVG_I[38]	100mV	0
4205 short	_FFT_ULL3_AVG_I[39]	100mV	0
4206 short	_COS_PHI_AVG_I[0]		0
4207 short	_COS_PHI_AVG_I[1]		0
4208 short	_COS_PHI_AVG_I[2]		0
4209 short	_COS_PHI_AVG_I[3]		0
4210 short	_PF_AVG_I[0]		0
4211 short	_PF_AVG_I[1]		0
4212 short	_PF_AVG_I[2]		0

4213 short	_PF_AVG_I[3]		0
4214 short	_THD_ULN_AVG_I[0]	%	0
4215 short	_THD_ULN_AVG_I[1]	%	0
4216 short	_THD_ULN_AVG_I[2]	%	0
4217 short	_THD_ULL_AVG_I[0]	%	0
4218 short	_THD_ULL_AVG_I[1]	%	0
4219 short	_THD_ULL_AVG_I[2]	%	0
4220 short	_ULN_RE_AVG_I[0]	100mV	0
4221 short	_ULN_RE_AVG_I[1]	100mV	0
4222 short	_ULN_RE_AVG_I[2]	100mV	0
4223 short	_ULN_IM_AVG_I[0]	100mV	0
4224 short	_ULN_IM_AVG_I[1]	100mV	0
4225 short	_ULN_IM_AVG_I[2]	100mV	0
4226 short	_FFT_IL1_AVG_I[0]	mA	0
4227 short	_FFT_IL1_AVG_I[1]	mA	0
4228 short	_FFT_IL1_AVG_I[2]	mA	0
4229 short	_FFT_IL1_AVG_I[3]	mA	0
4230 short	_FFT_IL1_AVG_I[4]	mA	0
4231 short	_FFT_IL1_AVG_I[5]	mA	0
4232 short	_FFT_IL1_AVG_I[6]	mA	0
4233 short	_FFT_IL1_AVG_I[7]	mA	0
4234 short	_FFT_IL1_AVG_I[8]	mA	0
4235 short	_FFT_IL1_AVG_I[9]	mA	0
4236 short	_FFT_IL1_AVG_I[10]	mA	0
4237 short	_FFT_IL1_AVG_I[11]	mA	0
4238 short	_FFT_IL1_AVG_I[12]	mA	0
4239 short	_FFT_IL1_AVG_I[13]	mA	0
4240 short	_FFT_IL1_AVG_I[14]	mA	0
4241 short	_FFT_IL1_AVG_I[15]	mA	0
4242 short	_FFT_IL1_AVG_I[16]	mA	0
4243 short	_FFT_IL1_AVG_I[17]	mA	0
4244 short	_FFT_IL1_AVG_I[18]	mA	0
4245 short	_FFT_IL1_AVG_I[19]	mA	0
4246 short	_FFT_IL1_AVG_I[20]	mA	0
4247 short	_FFT_IL1_AVG_I[21]	mA	0
4248 short	_FFT_IL1_AVG_I[22]	mA	0
4249 short	_FFT_IL1_AVG_I[23]	mA	0
4250 short	_FFT_IL1_AVG_I[24]	mA	0
4251 short	_FFT_IL1_AVG_I[25]	mA	0
4252 short	_FFT_IL1_AVG_I[26]	mA	0
4253 short	_FFT_IL1_AVG_I[27]	mA	0
4254 short	_FFT_IL1_AVG_I[28]	mA	0
4255 short	_FFT_IL1_AVG_I[29]	mA	0
4256 short	_FFT_IL1_AVG_I[30]	mA	0
4257 short	_FFT_IL1_AVG_I[31]	mA	0
4258 short	_FFT_IL1_AVG_I[32]	mA	0
4259 short	_FFT_IL1_AVG_I[33]	mA	0
4260 short	_FFT_IL1_AVG_I[34]	mA	0
4261 short	_FFT_IL1_AVG_I[35]	mA	0
4262 short	_FFT_IL1_AVG_I[36]	mA	0
4263 short	_FFT_IL1_AVG_I[37]	mA	0
4264 short	_FFT_IL1_AVG_I[38]	mA	0
4265 short	_FFT_IL1_AVG_I[39]	mA	0

4266 short	_FFT_IL2_AVG_I[0]	mA	0
4267 short	_FFT_IL2_AVG_I[1]	mA	0
4268 short	_FFT_IL2_AVG_I[2]	mA	0
4269 short	_FFT_IL2_AVG_I[3]	mA	0
4270 short	_FFT_IL2_AVG_I[4]	mA	0
4271 short	_FFT_IL2_AVG_I[5]	mA	0
4272 short	_FFT_IL2_AVG_I[6]	mA	0
4273 short	_FFT_IL2_AVG_I[7]	mA	0
4274 short	_FFT_IL2_AVG_I[8]	mA	0
4275 short	_FFT_IL2_AVG_I[9]	mA	0
4276 short	_FFT_IL2_AVG_I[10]	mA	0
4277 short	_FFT_IL2_AVG_I[11]	mA	0
4278 short	_FFT_IL2_AVG_I[12]	mA	0
4279 short	_FFT_IL2_AVG_I[13]	mA	0
4280 short	_FFT_IL2_AVG_I[14]	mA	0
4281 short	_FFT_IL2_AVG_I[15]	mA	0
4282 short	_FFT_IL2_AVG_I[16]	mA	0
4283 short	_FFT_IL2_AVG_I[17]	mA	0
4284 short	_FFT_IL2_AVG_I[18]	mA	0
4285 short	_FFT_IL2_AVG_I[19]	mA	0
4286 short	_FFT_IL2_AVG_I[20]	mA	0
4287 short	_FFT_IL2_AVG_I[21]	mA	0
4288 short	_FFT_IL2_AVG_I[22]	mA	0
4289 short	_FFT_IL2_AVG_I[23]	mA	0
4290 short	_FFT_IL2_AVG_I[24]	mA	0
4291 short	_FFT_IL2_AVG_I[25]	mA	0
4292 short	_FFT_IL2_AVG_I[26]	mA	0
4293 short	_FFT_IL2_AVG_I[27]	mA	0
4294 short	_FFT_IL2_AVG_I[28]	mA	0
4295 short	_FFT_IL2_AVG_I[29]	mA	0
4296 short	_FFT_IL2_AVG_I[30]	mA	0
4297 short	_FFT_IL2_AVG_I[31]	mA	0
4298 short	_FFT_IL2_AVG_I[32]	mA	0
4299 short	_FFT_IL2_AVG_I[33]	mA	0
4300 short	_FFT_IL2_AVG_I[34]	mA	0
4301 short	_FFT_IL2_AVG_I[35]	mA	0
4302 short	_FFT_IL2_AVG_I[36]	mA	0
4303 short	_FFT_IL2_AVG_I[37]	mA	0
4304 short	_FFT_IL2_AVG_I[38]	mA	0
4305 short	_FFT_IL2_AVG_I[39]	mA	0
4306 short	_FFT_IL3_AVG_I[0]	mA	0
4307 short	_FFT_IL3_AVG_I[1]	mA	0
4308 short	_FFT_IL3_AVG_I[2]	mA	0
4309 short	_FFT_IL3_AVG_I[3]	mA	0
4310 short	_FFT_IL3_AVG_I[4]	mA	0
4311 short	_FFT_IL3_AVG_I[5]	mA	0
4312 short	_FFT_IL3_AVG_I[6]	mA	0
4313 short	_FFT_IL3_AVG_I[7]	mA	0
4314 short	_FFT_IL3_AVG_I[8]	mA	0
4315 short	_FFT_IL3_AVG_I[9]	mA	0
4316 short	_FFT_IL3_AVG_I[10]	mA	0
4317 short	_FFT_IL3_AVG_I[11]	mA	0
4318 short	_FFT_IL3_AVG_I[12]	mA	0

4319 short	_FFT_IL3_AVG_I[13]	mA	0
4320 short	_FFT_IL3_AVG_I[14]	mA	0
4321 short	_FFT_IL3_AVG_I[15]	mA	0
4322 short	_FFT_IL3_AVG_I[16]	mA	0
4323 short	_FFT_IL3_AVG_I[17]	mA	0
4324 short	_FFT_IL3_AVG_I[18]	mA	0
4325 short	_FFT_IL3_AVG_I[19]	mA	0
4326 short	_FFT_IL3_AVG_I[20]	mA	0
4327 short	_FFT_IL3_AVG_I[21]	mA	0
4328 short	_FFT_IL3_AVG_I[22]	mA	0
4329 short	_FFT_IL3_AVG_I[23]	mA	0
4330 short	_FFT_IL3_AVG_I[24]	mA	0
4331 short	_FFT_IL3_AVG_I[25]	mA	0
4332 short	_FFT_IL3_AVG_I[26]	mA	0
4333 short	_FFT_IL3_AVG_I[27]	mA	0
4334 short	_FFT_IL3_AVG_I[28]	mA	0
4335 short	_FFT_IL3_AVG_I[29]	mA	0
4336 short	_FFT_IL3_AVG_I[30]	mA	0
4337 short	_FFT_IL3_AVG_I[31]	mA	0
4338 short	_FFT_IL3_AVG_I[32]	mA	0
4339 short	_FFT_IL3_AVG_I[33]	mA	0
4340 short	_FFT_IL3_AVG_I[34]	mA	0
4341 short	_FFT_IL3_AVG_I[35]	mA	0
4342 short	_FFT_IL3_AVG_I[36]	mA	0
4343 short	_FFT_IL3_AVG_I[37]	mA	0
4344 short	_FFT_IL3_AVG_I[38]	mA	0
4345 short	_FFT_IL3_AVG_I[39]	mA	0
4346 short	_ILN_AVG_I[0]	mA	0
4347 short	_ILN_AVG_I[1]	mA	0
4348 short	_ILN_AVG_I[2]	mA	0
4349 short	_ILN_AVG_I[3]	mA	0
4350 short	_PLN_AVG_I[0]	100mW	0
4351 short	_PLN_AVG_I[1]	100mW	0
4352 short	_PLN_AVG_I[2]	100mW	0
4353 short	_PLN_AVG_I[3]	100mW	0
4354 short	_QLN_AVG_I[0]	100mvar	0
4355 short	_QLN_AVG_I[1]	100mvar	0
4356 short	_QLN_AVG_I[2]	100mvar	0
4357 short	_QLN_AVG_I[3]	100mvar	0
4358 short	_SLN_AVG_I[0]	100mVA	0
4359 short	_SLN_AVG_I[1]	100mVA	0
4360 short	_SLN_AVG_I[2]	100mVA	0
4361 short	_SLN_AVG_I[3]	100mVA	0
4362 short	_P0_AVG_I[0]	100mW	0
4363 short	_P0_AVG_I[1]	100mW	0
4364 short	_P0_AVG_I[2]	100mW	0
4365 short	_P0_AVG_I[3]	100mW	0
4366 short	_DLN_AVG_I[0]	100mvar	0
4367 short	_DLN_AVG_I[1]	100mvar	0
4368 short	_DLN_AVG_I[2]	100mvar	0
4369 short	_DLN_AVG_I[3]	100mvar	0
4370 short	_THD_ILN_AVG_I[0]	%	0
4371 short	_THD_ILN_AVG_I[1]	%	0

4372 short	_THD_ILN_AVG_I[2]	%	0
4373 short	_TDD_ILN_AVG_I[0]	%	0
4374 short	_TDD_ILN_AVG_I[1]	%	0
4375 short	_TDD_ILN_AVG_I[2]	%	0
4376 short	_IN_AVG_I	mA	0
4377 short	_IG_AVG_I	mA	0
4378 short	_IM_AVG_I	mA	0
4379 short	_IL_RE_AVG_I[0]	mA	0
4380 short	_IL_RE_AVG_I[1]	mA	0
4381 short	_IL_RE_AVG_I[2]	mA	0
4382 short	_IL_IM_AVG_I[0]	mA	0
4383 short	_IL_IM_AVG_I[1]	mA	0
4384 short	_IL_IM_AVG_I[2]	mA	0
4385 short	_FREQ_MAX_I	Hz	0
4386 short	_N_MAX_I	100mV	0
4387 short	_G_MAX_I	100mV	0
4388 short	_M_MAX_I	100mV	0
4389 short	_ULN_MAX_I[0]	100mV	0
4390 short	_ULN_MAX_I[1]	100mV	0
4391 short	_ULN_MAX_I[2]	100mV	0
4392 short	_ULL_MAX_I[0]	100mV	0
4393 short	_ULL_MAX_I[1]	100mV	0
4394 short	_ULL_MAX_I[2]	100mV	0
4395 short	_FFT_UL1_MAX_I[0]	100mV	0
4396 short	_FFT_UL1_MAX_I[1]	100mV	0
4397 short	_FFT_UL1_MAX_I[2]	100mV	0
4398 short	_FFT_UL1_MAX_I[3]	100mV	0
4399 short	_FFT_UL1_MAX_I[4]	100mV	0
4400 short	_FFT_UL1_MAX_I[5]	100mV	0
4401 short	_FFT_UL1_MAX_I[6]	100mV	0
4402 short	_FFT_UL1_MAX_I[7]	100mV	0
4403 short	_FFT_UL1_MAX_I[8]	100mV	0
4404 short	_FFT_UL1_MAX_I[9]	100mV	0
4405 short	_FFT_UL1_MAX_I[10]	100mV	0
4406 short	_FFT_UL1_MAX_I[11]	100mV	0
4407 short	_FFT_UL1_MAX_I[12]	100mV	0
4408 short	_FFT_UL1_MAX_I[13]	100mV	0
4409 short	_FFT_UL1_MAX_I[14]	100mV	0
4410 short	_FFT_UL1_MAX_I[15]	100mV	0
4411 short	_FFT_UL1_MAX_I[16]	100mV	0
4412 short	_FFT_UL1_MAX_I[17]	100mV	0
4413 short	_FFT_UL1_MAX_I[18]	100mV	0
4414 short	_FFT_UL1_MAX_I[19]	100mV	0
4415 short	_FFT_UL1_MAX_I[20]	100mV	0
4416 short	_FFT_UL1_MAX_I[21]	100mV	0
4417 short	_FFT_UL1_MAX_I[22]	100mV	0
4418 short	_FFT_UL1_MAX_I[23]	100mV	0
4419 short	_FFT_UL1_MAX_I[24]	100mV	0
4420 short	_FFT_UL1_MAX_I[25]	100mV	0
4421 short	_FFT_UL1_MAX_I[26]	100mV	0
4422 short	_FFT_UL1_MAX_I[27]	100mV	0
4423 short	_FFT_UL1_MAX_I[28]	100mV	0
4424 short	_FFT_UL1_MAX_I[29]	100mV	0

4425 short	_FFT_UL1_MAX_I[30]	100mV	0
4426 short	_FFT_UL1_MAX_I[31]	100mV	0
4427 short	_FFT_UL1_MAX_I[32]	100mV	0
4428 short	_FFT_UL1_MAX_I[33]	100mV	0
4429 short	_FFT_UL1_MAX_I[34]	100mV	0
4430 short	_FFT_UL1_MAX_I[35]	100mV	0
4431 short	_FFT_UL1_MAX_I[36]	100mV	0
4432 short	_FFT_UL1_MAX_I[37]	100mV	0
4433 short	_FFT_UL1_MAX_I[38]	100mV	0
4434 short	_FFT_UL1_MAX_I[39]	100mV	0
4435 short	_FFT_UL2_MAX_I[0]	100mV	0
4436 short	_FFT_UL2_MAX_I[1]	100mV	0
4437 short	_FFT_UL2_MAX_I[2]	100mV	0
4438 short	_FFT_UL2_MAX_I[3]	100mV	0
4439 short	_FFT_UL2_MAX_I[4]	100mV	0
4440 short	_FFT_UL2_MAX_I[5]	100mV	0
4441 short	_FFT_UL2_MAX_I[6]	100mV	0
4442 short	_FFT_UL2_MAX_I[7]	100mV	0
4443 short	_FFT_UL2_MAX_I[8]	100mV	0
4444 short	_FFT_UL2_MAX_I[9]	100mV	0
4445 short	_FFT_UL2_MAX_I[10]	100mV	0
4446 short	_FFT_UL2_MAX_I[11]	100mV	0
4447 short	_FFT_UL2_MAX_I[12]	100mV	0
4448 short	_FFT_UL2_MAX_I[13]	100mV	0
4449 short	_FFT_UL2_MAX_I[14]	100mV	0
4450 short	_FFT_UL2_MAX_I[15]	100mV	0
4451 short	_FFT_UL2_MAX_I[16]	100mV	0
4452 short	_FFT_UL2_MAX_I[17]	100mV	0
4453 short	_FFT_UL2_MAX_I[18]	100mV	0
4454 short	_FFT_UL2_MAX_I[19]	100mV	0
4455 short	_FFT_UL2_MAX_I[20]	100mV	0
4456 short	_FFT_UL2_MAX_I[21]	100mV	0
4457 short	_FFT_UL2_MAX_I[22]	100mV	0
4458 short	_FFT_UL2_MAX_I[23]	100mV	0
4459 short	_FFT_UL2_MAX_I[24]	100mV	0
4460 short	_FFT_UL2_MAX_I[25]	100mV	0
4461 short	_FFT_UL2_MAX_I[26]	100mV	0
4462 short	_FFT_UL2_MAX_I[27]	100mV	0
4463 short	_FFT_UL2_MAX_I[28]	100mV	0
4464 short	_FFT_UL2_MAX_I[29]	100mV	0
4465 short	_FFT_UL2_MAX_I[30]	100mV	0
4466 short	_FFT_UL2_MAX_I[31]	100mV	0
4467 short	_FFT_UL2_MAX_I[32]	100mV	0
4468 short	_FFT_UL2_MAX_I[33]	100mV	0
4469 short	_FFT_UL2_MAX_I[34]	100mV	0
4470 short	_FFT_UL2_MAX_I[35]	100mV	0
4471 short	_FFT_UL2_MAX_I[36]	100mV	0
4472 short	_FFT_UL2_MAX_I[37]	100mV	0
4473 short	_FFT_UL2_MAX_I[38]	100mV	0
4474 short	_FFT_UL2_MAX_I[39]	100mV	0
4475 short	_FFT_UL3_MAX_I[0]	100mV	0
4476 short	_FFT_UL3_MAX_I[1]	100mV	0
4477 short	_FFT_UL3_MAX_I[2]	100mV	0

4478 short	_FFT_UL3_MAX_I[3]	100mV	0
4479 short	_FFT_UL3_MAX_I[4]	100mV	0
4480 short	_FFT_UL3_MAX_I[5]	100mV	0
4481 short	_FFT_UL3_MAX_I[6]	100mV	0
4482 short	_FFT_UL3_MAX_I[7]	100mV	0
4483 short	_FFT_UL3_MAX_I[8]	100mV	0
4484 short	_FFT_UL3_MAX_I[9]	100mV	0
4485 short	_FFT_UL3_MAX_I[10]	100mV	0
4486 short	_FFT_UL3_MAX_I[11]	100mV	0
4487 short	_FFT_UL3_MAX_I[12]	100mV	0
4488 short	_FFT_UL3_MAX_I[13]	100mV	0
4489 short	_FFT_UL3_MAX_I[14]	100mV	0
4490 short	_FFT_UL3_MAX_I[15]	100mV	0
4491 short	_FFT_UL3_MAX_I[16]	100mV	0
4492 short	_FFT_UL3_MAX_I[17]	100mV	0
4493 short	_FFT_UL3_MAX_I[18]	100mV	0
4494 short	_FFT_UL3_MAX_I[19]	100mV	0
4495 short	_FFT_UL3_MAX_I[20]	100mV	0
4496 short	_FFT_UL3_MAX_I[21]	100mV	0
4497 short	_FFT_UL3_MAX_I[22]	100mV	0
4498 short	_FFT_UL3_MAX_I[23]	100mV	0
4499 short	_FFT_UL3_MAX_I[24]	100mV	0
4500 short	_FFT_UL3_MAX_I[25]	100mV	0
4501 short	_FFT_UL3_MAX_I[26]	100mV	0
4502 short	_FFT_UL3_MAX_I[27]	100mV	0
4503 short	_FFT_UL3_MAX_I[28]	100mV	0
4504 short	_FFT_UL3_MAX_I[29]	100mV	0
4505 short	_FFT_UL3_MAX_I[30]	100mV	0
4506 short	_FFT_UL3_MAX_I[31]	100mV	0
4507 short	_FFT_UL3_MAX_I[32]	100mV	0
4508 short	_FFT_UL3_MAX_I[33]	100mV	0
4509 short	_FFT_UL3_MAX_I[34]	100mV	0
4510 short	_FFT_UL3_MAX_I[35]	100mV	0
4511 short	_FFT_UL3_MAX_I[36]	100mV	0
4512 short	_FFT_UL3_MAX_I[37]	100mV	0
4513 short	_FFT_UL3_MAX_I[38]	100mV	0
4514 short	_FFT_UL3_MAX_I[39]	100mV	0
4515 short	_FFT_ULL1_MAX_I[0]	100mV	0
4516 short	_FFT_ULL1_MAX_I[1]	100mV	0
4517 short	_FFT_ULL1_MAX_I[2]	100mV	0
4518 short	_FFT_ULL1_MAX_I[3]	100mV	0
4519 short	_FFT_ULL1_MAX_I[4]	100mV	0
4520 short	_FFT_ULL1_MAX_I[5]	100mV	0
4521 short	_FFT_ULL1_MAX_I[6]	100mV	0
4522 short	_FFT_ULL1_MAX_I[7]	100mV	0
4523 short	_FFT_ULL1_MAX_I[8]	100mV	0
4524 short	_FFT_ULL1_MAX_I[9]	100mV	0
4525 short	_FFT_ULL1_MAX_I[10]	100mV	0
4526 short	_FFT_ULL1_MAX_I[11]	100mV	0
4527 short	_FFT_ULL1_MAX_I[12]	100mV	0
4528 short	_FFT_ULL1_MAX_I[13]	100mV	0
4529 short	_FFT_ULL1_MAX_I[14]	100mV	0
4530 short	_FFT_ULL1_MAX_I[15]	100mV	0

4531 short	_FFT_ULL1_MAX_I[16]	100mV	0
4532 short	_FFT_ULL1_MAX_I[17]	100mV	0
4533 short	_FFT_ULL1_MAX_I[18]	100mV	0
4534 short	_FFT_ULL1_MAX_I[19]	100mV	0
4535 short	_FFT_ULL1_MAX_I[20]	100mV	0
4536 short	_FFT_ULL1_MAX_I[21]	100mV	0
4537 short	_FFT_ULL1_MAX_I[22]	100mV	0
4538 short	_FFT_ULL1_MAX_I[23]	100mV	0
4539 short	_FFT_ULL1_MAX_I[24]	100mV	0
4540 short	_FFT_ULL1_MAX_I[25]	100mV	0
4541 short	_FFT_ULL1_MAX_I[26]	100mV	0
4542 short	_FFT_ULL1_MAX_I[27]	100mV	0
4543 short	_FFT_ULL1_MAX_I[28]	100mV	0
4544 short	_FFT_ULL1_MAX_I[29]	100mV	0
4545 short	_FFT_ULL1_MAX_I[30]	100mV	0
4546 short	_FFT_ULL1_MAX_I[31]	100mV	0
4547 short	_FFT_ULL1_MAX_I[32]	100mV	0
4548 short	_FFT_ULL1_MAX_I[33]	100mV	0
4549 short	_FFT_ULL1_MAX_I[34]	100mV	0
4550 short	_FFT_ULL1_MAX_I[35]	100mV	0
4551 short	_FFT_ULL1_MAX_I[36]	100mV	0
4552 short	_FFT_ULL1_MAX_I[37]	100mV	0
4553 short	_FFT_ULL1_MAX_I[38]	100mV	0
4554 short	_FFT_ULL1_MAX_I[39]	100mV	0
4555 short	_FFT_ULL2_MAX_I[0]	100mV	0
4556 short	_FFT_ULL2_MAX_I[1]	100mV	0
4557 short	_FFT_ULL2_MAX_I[2]	100mV	0
4558 short	_FFT_ULL2_MAX_I[3]	100mV	0
4559 short	_FFT_ULL2_MAX_I[4]	100mV	0
4560 short	_FFT_ULL2_MAX_I[5]	100mV	0
4561 short	_FFT_ULL2_MAX_I[6]	100mV	0
4562 short	_FFT_ULL2_MAX_I[7]	100mV	0
4563 short	_FFT_ULL2_MAX_I[8]	100mV	0
4564 short	_FFT_ULL2_MAX_I[9]	100mV	0
4565 short	_FFT_ULL2_MAX_I[10]	100mV	0
4566 short	_FFT_ULL2_MAX_I[11]	100mV	0
4567 short	_FFT_ULL2_MAX_I[12]	100mV	0
4568 short	_FFT_ULL2_MAX_I[13]	100mV	0
4569 short	_FFT_ULL2_MAX_I[14]	100mV	0
4570 short	_FFT_ULL2_MAX_I[15]	100mV	0
4571 short	_FFT_ULL2_MAX_I[16]	100mV	0
4572 short	_FFT_ULL2_MAX_I[17]	100mV	0
4573 short	_FFT_ULL2_MAX_I[18]	100mV	0
4574 short	_FFT_ULL2_MAX_I[19]	100mV	0
4575 short	_FFT_ULL2_MAX_I[20]	100mV	0
4576 short	_FFT_ULL2_MAX_I[21]	100mV	0
4577 short	_FFT_ULL2_MAX_I[22]	100mV	0
4578 short	_FFT_ULL2_MAX_I[23]	100mV	0
4579 short	_FFT_ULL2_MAX_I[24]	100mV	0
4580 short	_FFT_ULL2_MAX_I[25]	100mV	0
4581 short	_FFT_ULL2_MAX_I[26]	100mV	0
4582 short	_FFT_ULL2_MAX_I[27]	100mV	0
4583 short	_FFT_ULL2_MAX_I[28]	100mV	0

4584 short	_FFT_ULL2_MAX_I[29]	100mV	0
4585 short	_FFT_ULL2_MAX_I[30]	100mV	0
4586 short	_FFT_ULL2_MAX_I[31]	100mV	0
4587 short	_FFT_ULL2_MAX_I[32]	100mV	0
4588 short	_FFT_ULL2_MAX_I[33]	100mV	0
4589 short	_FFT_ULL2_MAX_I[34]	100mV	0
4590 short	_FFT_ULL2_MAX_I[35]	100mV	0
4591 short	_FFT_ULL2_MAX_I[36]	100mV	0
4592 short	_FFT_ULL2_MAX_I[37]	100mV	0
4593 short	_FFT_ULL2_MAX_I[38]	100mV	0
4594 short	_FFT_ULL2_MAX_I[39]	100mV	0
4595 short	_FFT_ULL3_MAX_I[0]	100mV	0
4596 short	_FFT_ULL3_MAX_I[1]	100mV	0
4597 short	_FFT_ULL3_MAX_I[2]	100mV	0
4598 short	_FFT_ULL3_MAX_I[3]	100mV	0
4599 short	_FFT_ULL3_MAX_I[4]	100mV	0
4600 short	_FFT_ULL3_MAX_I[5]	100mV	0
4601 short	_FFT_ULL3_MAX_I[6]	100mV	0
4602 short	_FFT_ULL3_MAX_I[7]	100mV	0
4603 short	_FFT_ULL3_MAX_I[8]	100mV	0
4604 short	_FFT_ULL3_MAX_I[9]	100mV	0
4605 short	_FFT_ULL3_MAX_I[10]	100mV	0
4606 short	_FFT_ULL3_MAX_I[11]	100mV	0
4607 short	_FFT_ULL3_MAX_I[12]	100mV	0
4608 short	_FFT_ULL3_MAX_I[13]	100mV	0
4609 short	_FFT_ULL3_MAX_I[14]	100mV	0
4610 short	_FFT_ULL3_MAX_I[15]	100mV	0
4611 short	_FFT_ULL3_MAX_I[16]	100mV	0
4612 short	_FFT_ULL3_MAX_I[17]	100mV	0
4613 short	_FFT_ULL3_MAX_I[18]	100mV	0
4614 short	_FFT_ULL3_MAX_I[19]	100mV	0
4615 short	_FFT_ULL3_MAX_I[20]	100mV	0
4616 short	_FFT_ULL3_MAX_I[21]	100mV	0
4617 short	_FFT_ULL3_MAX_I[22]	100mV	0
4618 short	_FFT_ULL3_MAX_I[23]	100mV	0
4619 short	_FFT_ULL3_MAX_I[24]	100mV	0
4620 short	_FFT_ULL3_MAX_I[25]	100mV	0
4621 short	_FFT_ULL3_MAX_I[26]	100mV	0
4622 short	_FFT_ULL3_MAX_I[27]	100mV	0
4623 short	_FFT_ULL3_MAX_I[28]	100mV	0
4624 short	_FFT_ULL3_MAX_I[29]	100mV	0
4625 short	_FFT_ULL3_MAX_I[30]	100mV	0
4626 short	_FFT_ULL3_MAX_I[31]	100mV	0
4627 short	_FFT_ULL3_MAX_I[32]	100mV	0
4628 short	_FFT_ULL3_MAX_I[33]	100mV	0
4629 short	_FFT_ULL3_MAX_I[34]	100mV	0
4630 short	_FFT_ULL3_MAX_I[35]	100mV	0
4631 short	_FFT_ULL3_MAX_I[36]	100mV	0
4632 short	_FFT_ULL3_MAX_I[37]	100mV	0
4633 short	_FFT_ULL3_MAX_I[38]	100mV	0
4634 short	_FFT_ULL3_MAX_I[39]	100mV	0
4635 short	_COS_PHI_MAX_I[0]		0
4636 short	_COS_PHI_MAX_I[1]		0

4637 short	_COS_PHI_MAX_I[2]		0
4638 short	_COS_PHI_MAX_I[3]		0
4639 short	_PCALC_FMAX_MEASUREMENTS_I[0]		0
4640 short	_PCALC_FMAX_MEASUREMENTS_I[1]		0
4641 short	_PCALC_FMAX_MEASUREMENTS_I[2]		0
4642 short	_PCALC_FMAX_MEASUREMENTS_I[3]		0
4643 short	_THD_ULN_MAX_I[0]	%	0
4644 short	_THD_ULN_MAX_I[1]	%	0
4645 short	_THD_ULN_MAX_I[2]	%	0
4646 short	_THD_ULL_MAX_I[0]	%	0
4647 short	_THD_ULL_MAX_I[1]	%	0
4648 short	_THD_ULL_MAX_I[2]	%	0
4649 short	_ULN_RE_MAX_I[0]	100mV	0
4650 short	_ULN_RE_MAX_I[1]	100mV	0
4651 short	_ULN_RE_MAX_I[2]	100mV	0
4652 short	_ULN_IM_MAX_I[0]	100mV	0
4653 short	_ULN_IM_MAX_I[1]	100mV	0
4654 short	_ULN_IM_MAX_I[2]	100mV	0
4655 short	_FFT_IL1_MAX_I[0]	mA	0
4656 short	_FFT_IL1_MAX_I[1]	mA	0
4657 short	_FFT_IL1_MAX_I[2]	mA	0
4658 short	_FFT_IL1_MAX_I[3]	mA	0
4659 short	_FFT_IL1_MAX_I[4]	mA	0
4660 short	_FFT_IL1_MAX_I[5]	mA	0
4661 short	_FFT_IL1_MAX_I[6]	mA	0
4662 short	_FFT_IL1_MAX_I[7]	mA	0
4663 short	_FFT_IL1_MAX_I[8]	mA	0
4664 short	_FFT_IL1_MAX_I[9]	mA	0
4665 short	_FFT_IL1_MAX_I[10]	mA	0
4666 short	_FFT_IL1_MAX_I[11]	mA	0
4667 short	_FFT_IL1_MAX_I[12]	mA	0
4668 short	_FFT_IL1_MAX_I[13]	mA	0
4669 short	_FFT_IL1_MAX_I[14]	mA	0
4670 short	_FFT_IL1_MAX_I[15]	mA	0
4671 short	_FFT_IL1_MAX_I[16]	mA	0
4672 short	_FFT_IL1_MAX_I[17]	mA	0
4673 short	_FFT_IL1_MAX_I[18]	mA	0
4674 short	_FFT_IL1_MAX_I[19]	mA	0
4675 short	_FFT_IL1_MAX_I[20]	mA	0
4676 short	_FFT_IL1_MAX_I[21]	mA	0
4677 short	_FFT_IL1_MAX_I[22]	mA	0
4678 short	_FFT_IL1_MAX_I[23]	mA	0
4679 short	_FFT_IL1_MAX_I[24]	mA	0
4680 short	_FFT_IL1_MAX_I[25]	mA	0
4681 short	_FFT_IL1_MAX_I[26]	mA	0
4682 short	_FFT_IL1_MAX_I[27]	mA	0
4683 short	_FFT_IL1_MAX_I[28]	mA	0
4684 short	_FFT_IL1_MAX_I[29]	mA	0
4685 short	_FFT_IL1_MAX_I[30]	mA	0
4686 short	_FFT_IL1_MAX_I[31]	mA	0
4687 short	_FFT_IL1_MAX_I[32]	mA	0
4688 short	_FFT_IL1_MAX_I[33]	mA	0
4689 short	_FFT_IL1_MAX_I[34]	mA	0

4690 short	_FFT_IL1_MAX_I[35]	mA	0
4691 short	_FFT_IL1_MAX_I[36]	mA	0
4692 short	_FFT_IL1_MAX_I[37]	mA	0
4693 short	_FFT_IL1_MAX_I[38]	mA	0
4694 short	_FFT_IL1_MAX_I[39]	mA	0
4695 short	_FFT_IL2_MAX_I[0]	mA	0
4696 short	_FFT_IL2_MAX_I[1]	mA	0
4697 short	_FFT_IL2_MAX_I[2]	mA	0
4698 short	_FFT_IL2_MAX_I[3]	mA	0
4699 short	_FFT_IL2_MAX_I[4]	mA	0
4700 short	_FFT_IL2_MAX_I[5]	mA	0
4701 short	_FFT_IL2_MAX_I[6]	mA	0
4702 short	_FFT_IL2_MAX_I[7]	mA	0
4703 short	_FFT_IL2_MAX_I[8]	mA	0
4704 short	_FFT_IL2_MAX_I[9]	mA	0
4705 short	_FFT_IL2_MAX_I[10]	mA	0
4706 short	_FFT_IL2_MAX_I[11]	mA	0
4707 short	_FFT_IL2_MAX_I[12]	mA	0
4708 short	_FFT_IL2_MAX_I[13]	mA	0
4709 short	_FFT_IL2_MAX_I[14]	mA	0
4710 short	_FFT_IL2_MAX_I[15]	mA	0
4711 short	_FFT_IL2_MAX_I[16]	mA	0
4712 short	_FFT_IL2_MAX_I[17]	mA	0
4713 short	_FFT_IL2_MAX_I[18]	mA	0
4714 short	_FFT_IL2_MAX_I[19]	mA	0
4715 short	_FFT_IL2_MAX_I[20]	mA	0
4716 short	_FFT_IL2_MAX_I[21]	mA	0
4717 short	_FFT_IL2_MAX_I[22]	mA	0
4718 short	_FFT_IL2_MAX_I[23]	mA	0
4719 short	_FFT_IL2_MAX_I[24]	mA	0
4720 short	_FFT_IL2_MAX_I[25]	mA	0
4721 short	_FFT_IL2_MAX_I[26]	mA	0
4722 short	_FFT_IL2_MAX_I[27]	mA	0
4723 short	_FFT_IL2_MAX_I[28]	mA	0
4724 short	_FFT_IL2_MAX_I[29]	mA	0
4725 short	_FFT_IL2_MAX_I[30]	mA	0
4726 short	_FFT_IL2_MAX_I[31]	mA	0
4727 short	_FFT_IL2_MAX_I[32]	mA	0
4728 short	_FFT_IL2_MAX_I[33]	mA	0
4729 short	_FFT_IL2_MAX_I[34]	mA	0
4730 short	_FFT_IL2_MAX_I[35]	mA	0
4731 short	_FFT_IL2_MAX_I[36]	mA	0
4732 short	_FFT_IL2_MAX_I[37]	mA	0
4733 short	_FFT_IL2_MAX_I[38]	mA	0
4734 short	_FFT_IL2_MAX_I[39]	mA	0
4735 short	_FFT_IL3_MAX_I[0]	mA	0
4736 short	_FFT_IL3_MAX_I[1]	mA	0
4737 short	_FFT_IL3_MAX_I[2]	mA	0
4738 short	_FFT_IL3_MAX_I[3]	mA	0
4739 short	_FFT_IL3_MAX_I[4]	mA	0
4740 short	_FFT_IL3_MAX_I[5]	mA	0
4741 short	_FFT_IL3_MAX_I[6]	mA	0
4742 short	_FFT_IL3_MAX_I[7]	mA	0

4743 short	_FFT_IL3_MAX_I[8]	mA	0
4744 short	_FFT_IL3_MAX_I[9]	mA	0
4745 short	_FFT_IL3_MAX_I[10]	mA	0
4746 short	_FFT_IL3_MAX_I[11]	mA	0
4747 short	_FFT_IL3_MAX_I[12]	mA	0
4748 short	_FFT_IL3_MAX_I[13]	mA	0
4749 short	_FFT_IL3_MAX_I[14]	mA	0
4750 short	_FFT_IL3_MAX_I[15]	mA	0
4751 short	_FFT_IL3_MAX_I[16]	mA	0
4752 short	_FFT_IL3_MAX_I[17]	mA	0
4753 short	_FFT_IL3_MAX_I[18]	mA	0
4754 short	_FFT_IL3_MAX_I[19]	mA	0
4755 short	_FFT_IL3_MAX_I[20]	mA	0
4756 short	_FFT_IL3_MAX_I[21]	mA	0
4757 short	_FFT_IL3_MAX_I[22]	mA	0
4758 short	_FFT_IL3_MAX_I[23]	mA	0
4759 short	_FFT_IL3_MAX_I[24]	mA	0
4760 short	_FFT_IL3_MAX_I[25]	mA	0
4761 short	_FFT_IL3_MAX_I[26]	mA	0
4762 short	_FFT_IL3_MAX_I[27]	mA	0
4763 short	_FFT_IL3_MAX_I[28]	mA	0
4764 short	_FFT_IL3_MAX_I[29]	mA	0
4765 short	_FFT_IL3_MAX_I[30]	mA	0
4766 short	_FFT_IL3_MAX_I[31]	mA	0
4767 short	_FFT_IL3_MAX_I[32]	mA	0
4768 short	_FFT_IL3_MAX_I[33]	mA	0
4769 short	_FFT_IL3_MAX_I[34]	mA	0
4770 short	_FFT_IL3_MAX_I[35]	mA	0
4771 short	_FFT_IL3_MAX_I[36]	mA	0
4772 short	_FFT_IL3_MAX_I[37]	mA	0
4773 short	_FFT_IL3_MAX_I[38]	mA	0
4774 short	_FFT_IL3_MAX_I[39]	mA	0
4775 short	_ILN_MAX_I[0]	mA	0
4776 short	_ILN_MAX_I[1]	mA	0
4777 short	_ILN_MAX_I[2]	mA	0
4778 short	_ILN_MAX_I[3]	mA	0
4779 short	_PLN_MAX_I[0]	100mW	0
4780 short	_PLN_MAX_I[1]	100mW	0
4781 short	_PLN_MAX_I[2]	100mW	0
4782 short	_PLN_MAX_I[3]	100mW	0
4783 short	_QLN_MAX_I[0]	100mvar	0
4784 short	_QLN_MAX_I[1]	100mvar	0
4785 short	_QLN_MAX_I[2]	100mvar	0
4786 short	_QLN_MAX_I[3]	100mvar	0
4787 short	_SLN_MAX_I[0]	100mVA	0
4788 short	_SLN_MAX_I[1]	100mVA	0
4789 short	_SLN_MAX_I[2]	100mVA	0
4790 short	_SLN_MAX_I[3]	100mVA	0
4791 short	_P0_MAX_I[0]	100mW	0
4792 short	_P0_MAX_I[1]	100mW	0
4793 short	_P0_MAX_I[2]	100mW	0
4794 short	_P0_MAX_I[3]	100mW	0
4795 short	_DLN_MAX_I[0]	100mvar	0

4796 short	_DLN_MAX_I[1]	100mvar	0
4797 short	_DLN_MAX_I[2]	100mvar	0
4798 short	_DLN_MAX_I[3]	100mvar	0
4799 short	_THD_ILN_MAX_I[0]	%	0
4800 short	_THD_ILN_MAX_I[1]	%	0
4801 short	_THD_ILN_MAX_I[2]	%	0
4802 short	_TDD_ILN_MAX_I[0]	%	0
4803 short	_TDD_ILN_MAX_I[1]	%	0
4804 short	_TDD_ILN_MAX_I[2]	%	0
4805 short	_IN_MAX_I	mA	0
4806 short	_IG_MAX_I	mA	0
4807 short	_IM_MAX_I	mA	0
4808 short	_IL_RE_MAX_I[0]	mA	0
4809 short	_IL_RE_MAX_I[1]	mA	0
4810 short	_IL_RE_MAX_I[2]	mA	0
4811 short	_IL_IM_MAX_I[0]	mA	0
4812 short	_IL_IM_MAX_I[1]	mA	0
4813 short	_IL_IM_MAX_I[2]	mA	0
4814 short	_FREQ_MIN_I	Hz	0
4815 short	_N_MIN_I	100mV	0
4816 short	_G_MIN_I	100mV	0
4817 short	_M_MIN_I	100mV	0
4818 short	_ULN_MIN_I[0]	100mV	0
4819 short	_ULN_MIN_I[1]	100mV	0
4820 short	_ULN_MIN_I[2]	100mV	0
4821 short	_ULL_MIN_I[0]	100mV	0
4822 short	_ULL_MIN_I[1]	100mV	0
4823 short	_ULL_MIN_I[2]	100mV	0
4824 short	_COS_PHI_MIN_I[0]		0
4825 short	_COS_PHI_MIN_I[1]		0
4826 short	_COS_PHI_MIN_I[2]		0
4827 short	_COS_PHI_MIN_I[3]		0
4828 short	_PCALC_FMIN_MEASUREMENTS_I[0]		0
4829 short	_PCALC_FMIN_MEASUREMENTS_I[1]		0
4830 short	_PCALC_FMIN_MEASUREMENTS_I[2]		0
4831 short	_PCALC_FMIN_MEASUREMENTS_I[3]		0
4832 short	_THD_ULN_MIN_I[0]	%	0
4833 short	_THD_ULN_MIN_I[1]	%	0
4834 short	_THD_ULN_MIN_I[2]	%	0
4835 short	_THD_ULL_MIN_I[0]	%	0
4836 short	_THD_ULL_MIN_I[1]	%	0
4837 short	_THD_ULL_MIN_I[2]	%	0
4838 short	_ULN_RE_MIN_I[0]	100mV	0
4839 short	_ULN_RE_MIN_I[1]	100mV	0
4840 short	_ULN_RE_MIN_I[2]	100mV	0
4841 short	_ULN_IM_MIN_I[0]	100mV	0
4842 short	_ULN_IM_MIN_I[1]	100mV	0
4843 short	_ULN_IM_MIN_I[2]	100mV	0
4844 short	_ILN_AVG_MAX_I[0]	mA	0
4845 short	_ILN_AVG_MAX_I[1]	mA	0
4846 short	_ILN_AVG_MAX_I[2]	mA	0
4847 short	_ILN_AVG_MAX_I[3]	mA	0
4848 short	_PLN_AVG_MAX_I[0]	100mW	0

4849 short	_PLN_AVG_MAX_I[1]	100mW	0
4850 short	_PLN_AVG_MAX_I[2]	100mW	0
4851 short	_PLN_AVG_MAX_I[3]	100mW	0
5000 dfloat	_WH0[0]	Wh	0
5002 dfloat	_WH0[1]	Wh	0
5004 dfloat	_WH0[2]	Wh	0
5006 dfloat	_WH0[3]	Wh	0
5008 dfloat	_WH0[4]	Wh	0
5010 dfloat	_WH0[5]	Wh	0
5012 dfloat	_WH0[6]	Wh	0
5014 dfloat	_WH0[7]	Wh	0
5016 dfloat	_WH_V0[0]	Wh	0
5018 dfloat	_WH_V0[1]	Wh	0
5020 dfloat	_WH_V0[2]	Wh	0
5022 dfloat	_WH_V0[3]	Wh	0
5024 dfloat	_WH_V0[4]	Wh	0
5026 dfloat	_WH_V0[5]	Wh	0
5028 dfloat	_WH_V0[6]	Wh	0
5030 dfloat	_WH_V0[7]	Wh	0
5032 dfloat	_WH_Z0[0]	Wh	0
5034 dfloat	_WH_Z0[1]	Wh	0
5036 dfloat	_WH_Z0[2]	Wh	0
5038 dfloat	_WH_Z0[3]	Wh	0
5040 dfloat	_WH_Z0[4]	Wh	0
5042 dfloat	_WH_Z0[5]	Wh	0
5044 dfloat	_WH_Z0[6]	Wh	0
5046 dfloat	_WH_Z0[7]	Wh	0
5048 dfloat	_QH0[0]	varh	0
5050 dfloat	_QH0[1]	varh	0
5052 dfloat	_QH0[2]	varh	0
5054 dfloat	_QH0[3]	varh	0
5056 dfloat	_QH0[4]	varh	0
5058 dfloat	_QH0[5]	varh	0
5060 dfloat	_QH0[6]	varh	0
5062 dfloat	_QH0[7]	varh	0
5064 dfloat	_IQH0[0]	varh	0
5066 dfloat	_IQH0[1]	varh	0
5068 dfloat	_IQH0[2]	varh	0
5070 dfloat	_IQH0[3]	varh	0
5072 dfloat	_IQH0[4]	varh	0
5074 dfloat	_IQH0[5]	varh	0
5076 dfloat	_IQH0[6]	varh	0
5078 dfloat	_IQH0[7]	varh	0
5080 dfloat	_CQH0[0]	varh	0
5082 dfloat	_CQH0[1]	varh	0
5084 dfloat	_CQH0[2]	varh	0
5086 dfloat	_CQH0[3]	varh	0
5088 dfloat	_CQH0[4]	varh	0
5090 dfloat	_CQH0[5]	varh	0
5092 dfloat	_CQH0[6]	varh	0
5094 dfloat	_CQH0[7]	varh	0
5096 dfloat	_WH_S0[0]	VAh	0
5098 dfloat	_WH_S0[1]	VAh	0

5100 dfloat	_WH_S0[2]	VAh	0
5102 dfloat	_WH_S0[3]	VAh	0
5104 dfloat	_WH_S0[4]	VAh	0
5106 dfloat	_WH_S0[5]	VAh	0
5108 dfloat	_WH_S0[6]	VAh	0
5110 dfloat	_WH_S0[7]	VAh	0
5112 dfloat	_WH1[0]	Wh	0
5114 dfloat	_WH1[1]	Wh	0
5116 dfloat	_WH1[2]	Wh	0
5118 dfloat	_WH1[3]	Wh	0
5120 dfloat	_WH1[4]	Wh	0
5122 dfloat	_WH1[5]	Wh	0
5124 dfloat	_WH1[6]	Wh	0
5126 dfloat	_WH1[7]	Wh	0
5128 dfloat	_WH_V1[0]	Wh	0
5130 dfloat	_WH_V1[1]	Wh	0
5132 dfloat	_WH_V1[2]	Wh	0
5134 dfloat	_WH_V1[3]	Wh	0
5136 dfloat	_WH_V1[4]	Wh	0
5138 dfloat	_WH_V1[5]	Wh	0
5140 dfloat	_WH_V1[6]	Wh	0
5142 dfloat	_WH_V1[7]	Wh	0
5144 dfloat	_WH_Z1[0]	Wh	0
5146 dfloat	_WH_Z1[1]	Wh	0
5148 dfloat	_WH_Z1[2]	Wh	0
5150 dfloat	_WH_Z1[3]	Wh	0
5152 dfloat	_WH_Z1[4]	Wh	0
5154 dfloat	_WH_Z1[5]	Wh	0
5156 dfloat	_WH_Z1[6]	Wh	0
5158 dfloat	_WH_Z1[7]	Wh	0
5160 dfloat	_QH1[0]	varh	0
5162 dfloat	_QH1[1]	varh	0
5164 dfloat	_QH1[2]	varh	0
5166 dfloat	_QH1[3]	varh	0
5168 dfloat	_QH1[4]	varh	0
5170 dfloat	_QH1[5]	varh	0
5172 dfloat	_QH1[6]	varh	0
5174 dfloat	_QH1[7]	varh	0
5176 dfloat	_IQH1[0]	varh	0
5178 dfloat	_IQH1[1]	varh	0
5180 dfloat	_IQH1[2]	varh	0
5182 dfloat	_IQH1[3]	varh	0
5184 dfloat	_IQH1[4]	varh	0
5186 dfloat	_IQH1[5]	varh	0
5188 dfloat	_IQH1[6]	varh	0
5190 dfloat	_IQH1[7]	varh	0
5192 dfloat	_CQH1[0]	varh	0
5194 dfloat	_CQH1[1]	varh	0
5196 dfloat	_CQH1[2]	varh	0
5198 dfloat	_CQH1[3]	varh	0
5200 dfloat	_CQH1[4]	varh	0
5202 dfloat	_CQH1[5]	varh	0
5204 dfloat	_CQH1[6]	varh	0

5206 dfloat	_CQH1[7]	varh	0
5208 dfloat	_WH_S1[0]	VAh	0
5210 dfloat	_WH_S1[1]	VAh	0
5212 dfloat	_WH_S1[2]	VAh	0
5214 dfloat	_WH_S1[3]	VAh	0
5216 dfloat	_WH_S1[4]	VAh	0
5218 dfloat	_WH_S1[5]	VAh	0
5220 dfloat	_WH_S1[6]	VAh	0
5222 dfloat	_WH_S1[7]	VAh	0
5224 dfloat	_WH2[0]	Wh	0
5226 dfloat	_WH2[1]	Wh	0
5228 dfloat	_WH2[2]	Wh	0
5230 dfloat	_WH2[3]	Wh	0
5232 dfloat	_WH2[4]	Wh	0
5234 dfloat	_WH2[5]	Wh	0
5236 dfloat	_WH2[6]	Wh	0
5238 dfloat	_WH2[7]	Wh	0
5240 dfloat	_WH_V2[0]	Wh	0
5242 dfloat	_WH_V2[1]	Wh	0
5244 dfloat	_WH_V2[2]	Wh	0
5246 dfloat	_WH_V2[3]	Wh	0
5248 dfloat	_WH_V2[4]	Wh	0
5250 dfloat	_WH_V2[5]	Wh	0
5252 dfloat	_WH_V2[6]	Wh	0
5254 dfloat	_WH_V2[7]	Wh	0
5256 dfloat	_WH_Z2[0]	Wh	0
5258 dfloat	_WH_Z2[1]	Wh	0
5260 dfloat	_WH_Z2[2]	Wh	0
5262 dfloat	_WH_Z2[3]	Wh	0
5264 dfloat	_WH_Z2[4]	Wh	0
5266 dfloat	_WH_Z2[5]	Wh	0
5268 dfloat	_WH_Z2[6]	Wh	0
5270 dfloat	_WH_Z2[7]	Wh	0
5272 dfloat	_QH2[0]	varh	0
5274 dfloat	_QH2[1]	varh	0
5276 dfloat	_QH2[2]	varh	0
5278 dfloat	_QH2[3]	varh	0
5280 dfloat	_QH2[4]	varh	0
5282 dfloat	_QH2[5]	varh	0
5284 dfloat	_QH2[6]	varh	0
5286 dfloat	_QH2[7]	varh	0
5288 dfloat	_IQH2[0]	varh	0
5290 dfloat	_IQH2[1]	varh	0
5292 dfloat	_IQH2[2]	varh	0
5294 dfloat	_IQH2[3]	varh	0
5296 dfloat	_IQH2[4]	varh	0
5298 dfloat	_IQH2[5]	varh	0
5300 dfloat	_IQH2[6]	varh	0
5302 dfloat	_IQH2[7]	varh	0
5304 dfloat	_CQH2[0]	varh	0
5306 dfloat	_CQH2[1]	varh	0
5308 dfloat	_CQH2[2]	varh	0
5310 dfloat	_CQH2[3]	varh	0

5312 dfloat	_CQH2[4]	varh	0
5314 dfloat	_CQH2[5]	varh	0
5316 dfloat	_CQH2[6]	varh	0
5318 dfloat	_CQH2[7]	varh	0
5320 dfloat	_WH_S2[0]	VAh	0
5322 dfloat	_WH_S2[1]	VAh	0
5324 dfloat	_WH_S2[2]	VAh	0
5326 dfloat	_WH_S2[3]	VAh	0
5328 dfloat	_WH_S2[4]	VAh	0
5330 dfloat	_WH_S2[5]	VAh	0
5332 dfloat	_WH_S2[6]	VAh	0
5334 dfloat	_WH_S2[7]	VAh	0
5336 dfloat	_WHSUM[0]	Wh	0
5338 dfloat	_WHSUM[1]	Wh	0
5340 dfloat	_WHSUM[2]	Wh	0
5342 dfloat	_WHSUM[3]	Wh	0
5344 dfloat	_WHSUM[4]	Wh	0
5346 dfloat	_WHSUM[5]	Wh	0
5348 dfloat	_WHSUM[6]	Wh	0
5350 dfloat	_WHSUM[7]	Wh	0
5352 dfloat	_WH_VSUM[0]	Wh	0
5354 dfloat	_WH_VSUM[1]	Wh	0
5356 dfloat	_WH_VSUM[2]	Wh	0
5358 dfloat	_WH_VSUM[3]	Wh	0
5360 dfloat	_WH_VSUM[4]	Wh	0
5362 dfloat	_WH_VSUM[5]	Wh	0
5364 dfloat	_WH_VSUM[6]	Wh	0
5366 dfloat	_WH_VSUM[7]	Wh	0
5368 dfloat	_WH_ZSUM[0]	Wh	0
5370 dfloat	_WH_ZSUM[1]	Wh	0
5372 dfloat	_WH_ZSUM[2]	Wh	0
5374 dfloat	_WH_ZSUM[3]	Wh	0
5376 dfloat	_WH_ZSUM[4]	Wh	0
5378 dfloat	_WH_ZSUM[5]	Wh	0
5380 dfloat	_WH_ZSUM[6]	Wh	0
5382 dfloat	_WH_ZSUM[7]	Wh	0
5384 dfloat	_QHSUM[0]	varh	0
5386 dfloat	_QHSUM[1]	varh	0
5388 dfloat	_QHSUM[2]	varh	0
5390 dfloat	_QHSUM[3]	varh	0
5392 dfloat	_QHSUM[4]	varh	0
5394 dfloat	_QHSUM[5]	varh	0
5396 dfloat	_QHSUM[6]	varh	0
5398 dfloat	_QHSUM[7]	varh	0
5400 dfloat	_IQHSUM[0]	varh	0
5402 dfloat	_IQHSUM[1]	varh	0
5404 dfloat	_IQHSUM[2]	varh	0
5406 dfloat	_IQHSUM[3]	varh	0
5408 dfloat	_IQHSUM[4]	varh	0
5410 dfloat	_IQHSUM[5]	varh	0
5412 dfloat	_IQHSUM[6]	varh	0
5414 dfloat	_IQHSUM[7]	varh	0
5416 dfloat	_CQH3[0]	varh	0

5418 dfloat	_CQH3[1]	varh	0
5420 dfloat	_CQH3[2]	varh	0
5422 dfloat	_CQH3[3]	varh	0
5424 dfloat	_CQH3[4]	varh	0
5426 dfloat	_CQH3[5]	varh	0
5428 dfloat	_CQH3[6]	varh	0
5430 dfloat	_CQH3[7]	varh	0
5432 dfloat	_WH_SSUM[0]	VAh	0
5434 dfloat	_WH_SSUM[1]	VAh	0
5436 dfloat	_WH_SSUM[2]	VAh	0
5438 dfloat	_WH_SSUM[3]	VAh	0
5440 dfloat	_WH_SSUM[4]	VAh	0
5442 dfloat	_WH_SSUM[5]	VAh	0
5444 dfloat	_WH_SSUM[6]	VAh	0
5446 dfloat	_WH_SSUM[7]	VAh	0
5448 int	_WH0_I[0]	Wh	0
5450 int	_WH0_I[1]	Wh	0
5452 int	_WH0_I[2]	Wh	0
5454 int	_WH0_I[3]	Wh	0
5456 int	_WH0_I[4]	Wh	0
5458 int	_WH0_I[5]	Wh	0
5460 int	_WH0_I[6]	Wh	0
5462 int	_WH0_I[7]	Wh	0
5464 int	_WH_V0_I[0]	Wh	0
5466 int	_WH_V0_I[1]	Wh	0
5468 int	_WH_V0_I[2]	Wh	0
5470 int	_WH_V0_I[3]	Wh	0
5472 int	_WH_V0_I[4]	Wh	0
5474 int	_WH_V0_I[5]	Wh	0
5476 int	_WH_V0_I[6]	Wh	0
5478 int	_WH_V0_I[7]	Wh	0
5480 int	_WH_Z0_I[0]	Wh	0
5482 int	_WH_Z0_I[1]	Wh	0
5484 int	_WH_Z0_I[2]	Wh	0
5486 int	_WH_Z0_I[3]	Wh	0
5488 int	_WH_Z0_I[4]	Wh	0
5490 int	_WH_Z0_I[5]	Wh	0
5492 int	_WH_Z0_I[6]	Wh	0
5494 int	_WH_Z0_I[7]	Wh	0
5496 int	_QH0_I[0]	varh	0
5498 int	_QH0_I[1]	varh	0
5500 int	_QH0_I[2]	varh	0
5502 int	_QH0_I[3]	varh	0
5504 int	_QH0_I[4]	varh	0
5506 int	_QH0_I[5]	varh	0
5508 int	_QH0_I[6]	varh	0
5510 int	_QH0_I[7]	varh	0
5512 int	_IQH0_I[0]	varh	0
5514 int	_IQH0_I[1]	varh	0
5516 int	_IQH0_I[2]	varh	0
5518 int	_IQH0_I[3]	varh	0
5520 int	_IQH0_I[4]	varh	0
5522 int	_IQH0_I[5]	varh	0

5524 int	_IQH0_I[6]	varh	0
5526 int	_IQH0_I[7]	varh	0
5528 int	_CQH0_I[0]	varh	0
5530 int	_CQH0_I[1]	varh	0
5532 int	_CQH0_I[2]	varh	0
5534 int	_CQH0_I[3]	varh	0
5536 int	_CQH0_I[4]	varh	0
5538 int	_CQH0_I[5]	varh	0
5540 int	_CQH0_I[6]	varh	0
5542 int	_CQH0_I[7]	varh	0
5544 int	_WH_S0_I[0]	VAh	0
5546 int	_WH_S0_I[1]	VAh	0
5548 int	_WH_S0_I[2]	VAh	0
5550 int	_WH_S0_I[3]	VAh	0
5552 int	_WH_S0_I[4]	VAh	0
5554 int	_WH_S0_I[5]	VAh	0
5556 int	_WH_S0_I[6]	VAh	0
5558 int	_WH_S0_I[7]	VAh	0
5560 int	_WH1_I[0]	Wh	0
5562 int	_WH1_I[1]	Wh	0
5564 int	_WH1_I[2]	Wh	0
5566 int	_WH1_I[3]	Wh	0
5568 int	_WH1_I[4]	Wh	0
5570 int	_WH1_I[5]	Wh	0
5572 int	_WH1_I[6]	Wh	0
5574 int	_WH1_I[7]	Wh	0
5576 int	_WH_V1_I[0]	Wh	0
5578 int	_WH_V1_I[1]	Wh	0
5580 int	_WH_V1_I[2]	Wh	0
5582 int	_WH_V1_I[3]	Wh	0
5584 int	_WH_V1_I[4]	Wh	0
5586 int	_WH_V1_I[5]	Wh	0
5588 int	_WH_V1_I[6]	Wh	0
5590 int	_WH_V1_I[7]	Wh	0
5592 int	_WH_Z1_I[0]	Wh	0
5594 int	_WH_Z1_I[1]	Wh	0
5596 int	_WH_Z1_I[2]	Wh	0
5598 int	_WH_Z1_I[3]	Wh	0
5600 int	_WH_Z1_I[4]	Wh	0
5602 int	_WH_Z1_I[5]	Wh	0
5604 int	_WH_Z1_I[6]	Wh	0
5606 int	_WH_Z1_I[7]	Wh	0
5608 int	_QH1_I[0]	varh	0
5610 int	_QH1_I[1]	varh	0
5612 int	_QH1_I[2]	varh	0
5614 int	_QH1_I[3]	varh	0
5616 int	_QH1_I[4]	varh	0
5618 int	_QH1_I[5]	varh	0
5620 int	_QH1_I[6]	varh	0
5622 int	_QH1_I[7]	varh	0
5624 int	_IQH1_I[0]	varh	0
5626 int	_IQH1_I[1]	varh	0
5628 int	_IQH1_I[2]	varh	0

5630 int	_IQH1_I[3]	varh	0
5632 int	_IQH1_I[4]	varh	0
5634 int	_IQH1_I[5]	varh	0
5636 int	_IQH1_I[6]	varh	0
5638 int	_IQH1_I[7]	varh	0
5640 int	_CQH1_I[0]	varh	0
5642 int	_CQH1_I[1]	varh	0
5644 int	_CQH1_I[2]	varh	0
5646 int	_CQH1_I[3]	varh	0
5648 int	_CQH1_I[4]	varh	0
5650 int	_CQH1_I[5]	varh	0
5652 int	_CQH1_I[6]	varh	0
5654 int	_CQH1_I[7]	varh	0
5656 int	_WH_S1_I[0]	VAh	0
5658 int	_WH_S1_I[1]	VAh	0
5660 int	_WH_S1_I[2]	VAh	0
5662 int	_WH_S1_I[3]	VAh	0
5664 int	_WH_S1_I[4]	VAh	0
5666 int	_WH_S1_I[5]	VAh	0
5668 int	_WH_S1_I[6]	VAh	0
5670 int	_WH_S1_I[7]	VAh	0
5672 int	_WH2_I[0]	Wh	0
5674 int	_WH2_I[1]	Wh	0
5676 int	_WH2_I[2]	Wh	0
5678 int	_WH2_I[3]	Wh	0
5680 int	_WH2_I[4]	Wh	0
5682 int	_WH2_I[5]	Wh	0
5684 int	_WH2_I[6]	Wh	0
5686 int	_WH2_I[7]	Wh	0
5688 int	_WH_V2_I[0]	Wh	0
5690 int	_WH_V2_I[1]	Wh	0
5692 int	_WH_V2_I[2]	Wh	0
5694 int	_WH_V2_I[3]	Wh	0
5696 int	_WH_V2_I[4]	Wh	0
5698 int	_WH_V2_I[5]	Wh	0
5700 int	_WH_V2_I[6]	Wh	0
5702 int	_WH_V2_I[7]	Wh	0
5704 int	_WH_Z2_I[0]	Wh	0
5706 int	_WH_Z2_I[1]	Wh	0
5708 int	_WH_Z2_I[2]	Wh	0
5710 int	_WH_Z2_I[3]	Wh	0
5712 int	_WH_Z2_I[4]	Wh	0
5714 int	_WH_Z2_I[5]	Wh	0
5716 int	_WH_Z2_I[6]	Wh	0
5718 int	_WH_Z2_I[7]	Wh	0
5720 int	_QH2_I[0]	varh	0
5722 int	_QH2_I[1]	varh	0
5724 int	_QH2_I[2]	varh	0
5726 int	_QH2_I[3]	varh	0
5728 int	_QH2_I[4]	varh	0
5730 int	_QH2_I[5]	varh	0
5732 int	_QH2_I[6]	varh	0
5734 int	_QH2_I[7]	varh	0

5736 int	_IQH2_I[0]	varh	0
5738 int	_IQH2_I[1]	varh	0
5740 int	_IQH2_I[2]	varh	0
5742 int	_IQH2_I[3]	varh	0
5744 int	_IQH2_I[4]	varh	0
5746 int	_IQH2_I[5]	varh	0
5748 int	_IQH2_I[6]	varh	0
5750 int	_IQH2_I[7]	varh	0
5752 int	_CQH2_I[0]	varh	0
5754 int	_CQH2_I[1]	varh	0
5756 int	_CQH2_I[2]	varh	0
5758 int	_CQH2_I[3]	varh	0
5760 int	_CQH2_I[4]	varh	0
5762 int	_CQH2_I[5]	varh	0
5764 int	_CQH2_I[6]	varh	0
5766 int	_CQH2_I[7]	varh	0
5768 int	_WH_S2_I[0]	VAh	0
5770 int	_WH_S2_I[1]	VAh	0
5772 int	_WH_S2_I[2]	VAh	0
5774 int	_WH_S2_I[3]	VAh	0
5776 int	_WH_S2_I[4]	VAh	0
5778 int	_WH_S2_I[5]	VAh	0
5780 int	_WH_S2_I[6]	VAh	0
5782 int	_WH_S2_I[7]	VAh	0
5784 int	_WHSUM_I[0]	Wh	0
5786 int	_WHSUM_I[1]	Wh	0
5788 int	_WHSUM_I[2]	Wh	0
5790 int	_WHSUM_I[3]	Wh	0
5792 int	_WHSUM_I[4]	Wh	0
5794 int	_WHSUM_I[5]	Wh	0
5796 int	_WHSUM_I[6]	Wh	0
5798 int	_WHSUM_I[7]	Wh	0
5800 int	_WH_VSUM_I[0]	Wh	0
5802 int	_WH_VSUM_I[1]	Wh	0
5804 int	_WH_VSUM_I[2]	Wh	0
5806 int	_WH_VSUM_I[3]	Wh	0
5808 int	_WH_VSUM_I[4]	Wh	0
5810 int	_WH_VSUM_I[5]	Wh	0
5812 int	_WH_VSUM_I[6]	Wh	0
5814 int	_WH_VSUM_I[7]	Wh	0
5816 int	_WH_ZSUM_I[0]	Wh	0
5818 int	_WH_ZSUM_I[1]	Wh	0
5820 int	_WH_ZSUM_I[2]	Wh	0
5822 int	_WH_ZSUM_I[3]	Wh	0
5824 int	_WH_ZSUM_I[4]	Wh	0
5826 int	_WH_ZSUM_I[5]	Wh	0
5828 int	_WH_ZSUM_I[6]	Wh	0
5830 int	_WH_ZSUM_I[7]	Wh	0
5832 int	_QHSUM_I[0]	varh	0
5834 int	_QHSUM_I[1]	varh	0
5836 int	_QHSUM_I[2]	varh	0
5838 int	_QHSUM_I[3]	varh	0
5840 int	_QHSUM_I[4]	varh	0

5842 int	_QHSUM_I[5]	varh	0
5844 int	_QHSUM_I[6]	varh	0
5846 int	_QHSUM_I[7]	varh	0
5848 int	_IQHSUM_I[0]	varh	0
5850 int	_IQHSUM_I[1]	varh	0
5852 int	_IQHSUM_I[2]	varh	0
5854 int	_IQHSUM_I[3]	varh	0
5856 int	_IQHSUM_I[4]	varh	0
5858 int	_IQHSUM_I[5]	varh	0
5860 int	_IQHSUM_I[6]	varh	0
5862 int	_IQHSUM_I[7]	varh	0
5864 int	_CQH3_I[0]	varh	0
5866 int	_CQH3_I[1]	varh	0
5868 int	_CQH3_I[2]	varh	0
5870 int	_CQH3_I[3]	varh	0
5872 int	_CQH3_I[4]	varh	0
5874 int	_CQH3_I[5]	varh	0
5876 int	_CQH3_I[6]	varh	0
5878 int	_CQH3_I[7]	varh	0
5880 int	_WH_SSUM_I[0]	VAh	0
5882 int	_WH_SSUM_I[1]	VAh	0
5884 int	_WH_SSUM_I[2]	VAh	0
5886 int	_WH_SSUM_I[3]	VAh	0
5888 int	_WH_SSUM_I[4]	VAh	0
5890 int	_WH_SSUM_I[5]	VAh	0
5892 int	_WH_SSUM_I[6]	VAh	0
5894 int	_WH_SSUM_I[7]	VAh	0
5896 int	_EEPROM_TIMER_I	s	0
10000 float	_FFT_IL4[0]	A	0
10002 float	_FFT_IL4[1]	A	0
10004 float	_FFT_IL4[2]	A	0
10006 float	_FFT_IL4[3]	A	0
10008 float	_FFT_IL4[4]	A	0
10010 float	_FFT_IL4[5]	A	0
10012 float	_FFT_IL4[6]	A	0
10014 float	_FFT_IL4[7]	A	0
10016 float	_FFT_IL4[8]	A	0
10018 float	_FFT_IL4[9]	A	0
10020 float	_FFT_IL4[10]	A	0
10022 float	_FFT_IL4[11]	A	0
10024 float	_FFT_IL4[12]	A	0
10026 float	_FFT_IL4[13]	A	0
10028 float	_FFT_IL4[14]	A	0
10030 float	_FFT_IL4[15]	A	0
10032 float	_FFT_IL4[16]	A	0
10034 float	_FFT_IL4[17]	A	0
10036 float	_FFT_IL4[18]	A	0
10038 float	_FFT_IL4[19]	A	0
10040 float	_FFT_IL4[20]	A	0
10042 float	_FFT_IL4[21]	A	0
10044 float	_FFT_IL4[22]	A	0
10046 float	_FFT_IL4[23]	A	0
10048 float	_FFT_IL4[24]	A	0

10050 float	_FFT_IL4[25]	A	0
10052 float	_FFT_IL4[26]	A	0
10054 float	_FFT_IL4[27]	A	0
10056 float	_FFT_IL4[28]	A	0
10058 float	_FFT_IL4[29]	A	0
10060 float	_FFT_IL4[30]	A	0
10062 float	_FFT_IL4[31]	A	0
10064 float	_FFT_IL4[32]	A	0
10066 float	_FFT_IL4[33]	A	0
10068 float	_FFT_IL4[34]	A	0
10070 float	_FFT_IL4[35]	A	0
10072 float	_FFT_IL4[36]	A	0
10074 float	_FFT_IL4[37]	A	0
10076 float	_FFT_IL4[38]	A	0
10078 float	_FFT_IL4[39]	A	0
10080 short	_DIGOUT_STAT[0]		0
10081 short	_DIGOUT_STAT[1]		0
10082 short	_DIGOUT_STAT[2]		0
10083 short	_DIGOUT_STAT[3]		0
10084 short	_DIGOUT_STAT[4]		0
10085 float	_ILN4	A	0
10087 float	_THD_ILN4	%	0
10089 float	_TDD_ILN4	%	0
10091 float	_S0_POWER[0]	W	0
10093 float	_S0_POWER[1]	W	0
10095 float	_S0_POWER[2]	W	0
10097 float	_S0_POWER_AVG[0]	W	0
10099 float	_S0_POWER_AVG[1]	W	0
10101 float	_S0_POWER_AVG[2]	W	0
10103 float	_S0_POWER_MAX[0]	W	0
10105 float	_S0_POWER_MAX[1]	W	0
10107 float	_S0_POWER_MAX[2]	W	0
10109 short	_DIGIN_STAT[0]		0
10110 short	_DIGIN_STAT[1]		0
10111 short	_DIGIN_STAT[2]		0
10112 int	_MB_OVER		0
10114 ushort	_DIGIN_BIT		0
10115 float	_ILN4_AVG	A	0
10117 float	_THD_ILN4_AVG	%	0
10119 float	_TDD_ILN4_AVG	%	0
10121 float	_ILN4_MAX	A	0
10123 float	_THD_ILN4_MAX	%	0
10125 float	_TDD_ILN4_MAX	%	0
10127 float	_ILN4_AVG_MAX	A	0
10129 dfloat	_COMP_TIMER[0]	s	0
10131 dfloat	_COMP_TIMER[1]	s	0
10133 dfloat	_COMP_TIMER[2]	s	0
10135 dfloat	_COMP_TIMER[3]	s	0
10137 dfloat	_COMP_TIMER[4]	s	0
10139 dfloat	_COMP_TIMER[5]	s	0
10141 dfloat	_COMP_TIMER[6]	s	0
10143 dfloat	_COMP_TIMER[7]	s	0
10145 dfloat	_COMP_TIMER[8]	s	0

10147 dfloat	_COMP_TIMER[9]	s	0
10149 dfloat	_COMP_TIMER[10]	s	0
10151 dfloat	_COMP_TIMER[11]	s	0
10153 dfloat	_COMP_TIMER[12]	s	0
10155 dfloat	_COMP_TIMER[13]	s	0
10157 dfloat	_COMP_TIMER[14]	s	0
10159 dfloat	_COMP_TIMER[15]	s	0
10161 dfloat	_COMP_TIMER[16]	s	0
10163 dfloat	_COMP_TIMER[17]	s	0
10165 dfloat	_COMP_TIMER[18]	s	0
10167 dfloat	_COMP_TIMER[19]	s	0
10169 dfloat	_COMP_TIMER[20]	s	0
10171 dfloat	_COMP_TIMER[21]	s	0
10173 dfloat	_COMP_TIMER[22]	s	0
10175 dfloat	_COMP_TIMER[23]	s	0
10177 dfloat	_COMP_TIMER[24]	s	0
10179 dfloat	_COMP_TIMER[25]	s	0
10181 dfloat	_COMP_TIMER[26]	s	0
10183 dfloat	_COMP_TIMER[27]	s	0
10185 dfloat	_COMP_TIMER[28]	s	0
10187 dfloat	_COMP_TIMER[29]	s	0
10189 dfloat	_COMP_TIMER[30]	s	0
10191 dfloat	_COMP_TIMER[31]	s	0
10193 dfloat	_COMP_TIMER[32]	s	0
10195 dfloat	_COMP_TIMER[33]	s	0
10197 dfloat	_COMP_TIMER[34]	s	0
10199 dfloat	_COMP_TIMER[35]	s	0
10201 dfloat	_COMP_TIMER[36]	s	0
10203 dfloat	_COMP_TIMER[37]	s	0
10205 dfloat	_COMP_TIMER[38]	s	0
10207 dfloat	_COMP_TIMER[39]	s	0
10209 dfloat	_COMP_TIMER[40]	s	0
10211 dfloat	_COMP_TIMER[41]	s	0
10213 dfloat	_COMP_TIMER[42]	s	0
10215 dfloat	_COMP_TIMER[43]	s	0
10217 dfloat	_COMP_TIMER[44]	s	0
10219 dfloat	_COMP_TIMER[45]	s	0
10221 dfloat	_COMP_TIMER[46]	s	0
10223 dfloat	_COMP_TIMER[47]	s	0
10225 dfloat	_COMP_TIMER[48]	s	0
10227 dfloat	_COMP_TIMER[49]	s	0
10229 int	_COMP_TIMER_I[0]	s	0
10231 int	_COMP_TIMER_I[1]	s	0
10233 int	_COMP_TIMER_I[2]	s	0
10235 int	_COMP_TIMER_I[3]	s	0
10237 int	_COMP_TIMER_I[4]	s	0
10239 int	_COMP_TIMER_I[5]	s	0
10241 int	_COMP_TIMER_I[6]	s	0
10243 int	_COMP_TIMER_I[7]	s	0
10245 int	_COMP_TIMER_I[8]	s	0
10247 int	_COMP_TIMER_I[9]	s	0
10249 int	_COMP_TIMER_I[10]	s	0
10251 int	_COMP_TIMER_I[11]	s	0

10253 int	_COMP_TIMER_I[12]	s	0
10255 int	_COMP_TIMER_I[13]	s	0
10257 int	_COMP_TIMER_I[14]	s	0
10259 int	_COMP_TIMER_I[15]	s	0
10261 int	_COMP_TIMER_I[16]	s	0
10263 int	_COMP_TIMER_I[17]	s	0
10265 int	_COMP_TIMER_I[18]	s	0
10267 int	_COMP_TIMER_I[19]	s	0
10269 int	_COMP_TIMER_I[20]	s	0
10271 int	_COMP_TIMER_I[21]	s	0
10273 int	_COMP_TIMER_I[22]	s	0
10275 int	_COMP_TIMER_I[23]	s	0
10277 int	_COMP_TIMER_I[24]	s	0
10279 int	_COMP_TIMER_I[25]	s	0
10281 int	_COMP_TIMER_I[26]	s	0
10283 int	_COMP_TIMER_I[27]	s	0
10285 int	_COMP_TIMER_I[28]	s	0
10287 int	_COMP_TIMER_I[29]	s	0
10289 int	_COMP_TIMER_I[30]	s	0
10291 int	_COMP_TIMER_I[31]	s	0
10293 int	_COMP_TIMER_I[32]	s	0
10295 int	_COMP_TIMER_I[33]	s	0
10297 int	_COMP_TIMER_I[34]	s	0
10299 int	_COMP_TIMER_I[35]	s	0
10301 int	_COMP_TIMER_I[36]	s	0
10303 int	_COMP_TIMER_I[37]	s	0
10305 int	_COMP_TIMER_I[38]	s	0
10307 int	_COMP_TIMER_I[39]	s	0
10309 int	_COMP_TIMER_I[40]	s	0
10311 int	_COMP_TIMER_I[41]	s	0
10313 int	_COMP_TIMER_I[42]	s	0
10315 int	_COMP_TIMER_I[43]	s	0
10317 int	_COMP_TIMER_I[44]	s	0
10319 int	_COMP_TIMER_I[45]	s	0
10321 int	_COMP_TIMER_I[46]	s	0
10323 int	_COMP_TIMER_I[47]	s	0
10325 int	_COMP_TIMER_I[48]	s	0
10327 int	_COMP_TIMER_I[49]	s	0
10329 uint	_S0_CNT[0]		0
10331 uint	_S0_CNT[1]		0
10333 uint	_S0_CNT[2]		0
10335 uint	_ILN4_MAX_T		0
10337 uint	_THD_ILN4_MAX_T		0
10339 uint	_TDD_ILN4_MAX_T		0
10341 uint	_ILN4_AVG_MAX_T		0
10343 uint	_S0_POWER_MAX_T[0]		0
10345 uint	_S0_POWER_MAX_T[1]		0
10347 uint	_S0_POWER_MAX_T[2]		0
10349 uint	_FREQ_MAX_T		0
10351 uint	_N_MAX_T		0
10353 uint	_G_MAX_T		0
10355 uint	_M_MAX_T		0
10357 uint	_ULN_MAX_T[0]		0

10359	uint	_ULN_MAX_T[1]	0
10361	uint	_ULN_MAX_T[2]	0
10363	uint	_ULL_MAX_T[0]	0
10365	uint	_ULL_MAX_T[1]	0
10367	uint	_ULL_MAX_T[2]	0
10369	uint	_COS_PHI_MAX_T[0]	0
10371	uint	_COS_PHI_MAX_T[1]	0
10373	uint	_COS_PHI_MAX_T[2]	0
10375	uint	_COS_PHI_MAX_T[3]	0
10377	uint	_PF_MAX_T[0]	0
10379	uint	_PF_MAX_T[1]	0
10381	uint	_PF_MAX_T[2]	0
10383	uint	_PF_MAX_T[3]	0
10385	uint	_THD_ULN_MAX_T[0]	0
10387	uint	_THD_ULN_MAX_T[1]	0
10389	uint	_THD_ULN_MAX_T[2]	0
10391	uint	_THD_ULL_MAX_T[0]	0
10393	uint	_THD_ULL_MAX_T[1]	0
10395	uint	_THD_ULL_MAX_T[2]	0
10397	uint	_ULN_RE_MAX_T[0]	0
10399	uint	_ULN_RE_MAX_T[1]	0
10401	uint	_ULN_RE_MAX_T[2]	0
10403	uint	_ULN_IM_MAX_T[0]	0
10405	uint	_ULN_IM_MAX_T[1]	0
10407	uint	_ULN_IM_MAX_T[2]	0
10409	uint	_ILN_MAX_T[0]	0
10411	uint	_ILN_MAX_T[1]	0
10413	uint	_ILN_MAX_T[2]	0
10415	uint	_ILN_MAX_T[3]	0
10417	uint	_PLN_MAX_T[0]	0
10419	uint	_PLN_MAX_T[1]	0
10421	uint	_PLN_MAX_T[2]	0
10423	uint	_PLN_MAX_T[3]	0
10425	uint	_QLN_MAX_T[0]	0
10427	uint	_QLN_MAX_T[1]	0
10429	uint	_QLN_MAX_T[2]	0
10431	uint	_QLN_MAX_T[3]	0
10433	uint	_SLN_MAX_T[0]	0
10435	uint	_SLN_MAX_T[1]	0
10437	uint	_SLN_MAX_T[2]	0
10439	uint	_SLN_MAX_T[3]	0
10441	uint	_P0_MAX_T[0]	0
10443	uint	_P0_MAX_T[1]	0
10445	uint	_P0_MAX_T[2]	0
10447	uint	_P0_MAX_T[3]	0
10449	uint	_DLN_MAX_T[0]	0
10451	uint	_DLN_MAX_T[1]	0
10453	uint	_DLN_MAX_T[2]	0
10455	uint	_DLN_MAX_T[3]	0
10457	uint	_THD_ILN_MAX_T[0]	0
10459	uint	_THD_ILN_MAX_T[1]	0
10461	uint	_THD_ILN_MAX_T[2]	0
10463	uint	_TDD_ILN_MAX_T[0]	0

10465 uint	_TDD_ILN_MAX_T[1]		0
10467 uint	_TDD_ILN_MAX_T[2]		0
10469 uint	_IN_MAX_T		0
10471 uint	_IG_MAX_T		0
10473 uint	_IM_MAX_T		0
10475 uint	_ILN_RE_MAX_T[0]		0
10477 uint	_ILN_RE_MAX_T[1]		0
10479 uint	_ILN_RE_MAX_T[2]		0
10481 uint	_ILN_IM_MAX_T[0]		0
10483 uint	_ILN_IM_MAX_T[1]		0
10485 uint	_ILN_IM_MAX_T[2]		0
10487 uint	_ILN_AVG_MAX_T[0]		0
10489 uint	_ILN_AVG_MAX_T[1]		0
10491 uint	_ILN_AVG_MAX_T[2]		0
10493 uint	_ILN_AVG_MAX_T[3]		0
10495 uint	_PLN_AVG_MAX_T[0]		0
10497 uint	_PLN_AVG_MAX_T[1]		0
10499 uint	_PLN_AVG_MAX_T[2]		0
10501 uint	_PLN_AVG_MAX_T[3]		0
10503 uint	_FREQ_MIN_T		0
10505 uint	_N_MIN_T		0
10507 uint	_G_MIN_T		0
10509 uint	_M_MIN_T		0
10511 uint	_ULN_MIN_T[0]		0
10513 uint	_ULN_MIN_T[1]		0
10515 uint	_ULN_MIN_T[2]		0
10517 uint	_ULL_MIN_T[0]		0
10519 uint	_ULL_MIN_T[1]		0
10521 uint	_ULL_MIN_T[2]		0
10523 uint	_COS_PHI_MIN_T[0]		0
10525 uint	_COS_PHI_MIN_T[1]		0
10527 uint	_COS_PHI_MIN_T[2]		0
10529 uint	_COS_PHI_MIN_T[3]		0
10531 uint	_PCALC_FMIN_MEASUREMENTS_T[0]		0
10533 uint	_PCALC_FMIN_MEASUREMENTS_T[1]		0
10535 uint	_PCALC_FMIN_MEASUREMENTS_T[2]		0
10537 uint	_PCALC_FMIN_MEASUREMENTS_T[3]		0
10539 uint	_THD_ULN_MIN_T[0]		0
10541 uint	_THD_ULN_MIN_T[1]		0
10543 uint	_THD_ULN_MIN_T[2]		0
10545 uint	_THD_ULL_MIN_T[0]		0
10547 uint	_THD_ULL_MIN_T[1]		0
10549 uint	_THD_ULL_MIN_T[2]		0
10551 uint	_ULN_RE_MIN_T[0]		0
10553 uint	_ULN_RE_MIN_T[1]		0
10555 uint	_ULN_RE_MIN_T[2]		0
10557 uint	_ULN_IM_MIN_T[0]		0
10559 uint	_ULN_IM_MIN_T[1]		0
10561 uint	_ULN_IM_MIN_T[2]		0
10563 float	_FFT_ILN4_MAX[0]	A	0
10565 float	_FFT_ILN4_MAX[1]	A	0
10567 float	_FFT_ILN4_MAX[2]	A	0
10569 float	_FFT_ILN4_MAX[3]	A	0

10571 float	_FFT_ILN4_MAX[4]	A	0
10573 float	_FFT_ILN4_MAX[5]	A	0
10575 float	_FFT_ILN4_MAX[6]	A	0
10577 float	_FFT_ILN4_MAX[7]	A	0
10579 float	_FFT_ILN4_MAX[8]	A	0
10581 float	_FFT_ILN4_MAX[9]	A	0
10583 float	_FFT_ILN4_MAX[10]	A	0
10585 float	_FFT_ILN4_MAX[11]	A	0
10587 float	_FFT_ILN4_MAX[12]	A	0
10589 float	_FFT_ILN4_MAX[13]	A	0
10591 float	_FFT_ILN4_MAX[14]	A	0
10593 float	_FFT_ILN4_MAX[15]	A	0
10595 float	_FFT_ILN4_MAX[16]	A	0
10597 float	_FFT_ILN4_MAX[17]	A	0
10599 float	_FFT_ILN4_MAX[18]	A	0
10601 float	_FFT_ILN4_MAX[19]	A	0
10603 float	_FFT_ILN4_MAX[20]	A	0
10605 float	_FFT_ILN4_MAX[21]	A	0
10607 float	_FFT_ILN4_MAX[22]	A	0
10609 float	_FFT_ILN4_MAX[23]	A	0
10611 float	_FFT_ILN4_MAX[24]	A	0
10613 float	_FFT_ILN4_MAX[25]	A	0
10615 float	_FFT_ILN4_MAX[26]	A	0
10617 float	_FFT_ILN4_MAX[27]	A	0
10619 float	_FFT_ILN4_MAX[28]	A	0
10621 float	_FFT_ILN4_MAX[29]	A	0
10623 float	_FFT_ILN4_MAX[30]	A	0
10625 float	_FFT_ILN4_MAX[31]	A	0
10627 float	_FFT_ILN4_MAX[32]	A	0
10629 float	_FFT_ILN4_MAX[33]	A	0
10631 float	_FFT_ILN4_MAX[34]	A	0
10633 float	_FFT_ILN4_MAX[35]	A	0
10635 float	_FFT_ILN4_MAX[36]	A	0
10637 float	_FFT_ILN4_MAX[37]	A	0
10639 float	_FFT_ILN4_MAX[38]	A	0
10641 float	_FFT_ILN4_MAX[39]	A	0
10643 float	_FFT_ILN4_AVG[0]	A	0
10645 float	_FFT_ILN4_AVG[1]	A	0
10647 float	_FFT_ILN4_AVG[2]	A	0
10649 float	_FFT_ILN4_AVG[3]	A	0
10651 float	_FFT_ILN4_AVG[4]	A	0
10653 float	_FFT_ILN4_AVG[5]	A	0
10655 float	_FFT_ILN4_AVG[6]	A	0
10657 float	_FFT_ILN4_AVG[7]	A	0
10659 float	_FFT_ILN4_AVG[8]	A	0
10661 float	_FFT_ILN4_AVG[9]	A	0
10663 float	_FFT_ILN4_AVG[10]	A	0
10665 float	_FFT_ILN4_AVG[11]	A	0
10667 float	_FFT_ILN4_AVG[12]	A	0
10669 float	_FFT_ILN4_AVG[13]	A	0
10671 float	_FFT_ILN4_AVG[14]	A	0
10673 float	_FFT_ILN4_AVG[15]	A	0
10675 float	_FFT_ILN4_AVG[16]	A	0

10677 float	_FFT_ILN4_AVG[17]	A	0
10679 float	_FFT_ILN4_AVG[18]	A	0
10681 float	_FFT_ILN4_AVG[19]	A	0
10683 float	_FFT_ILN4_AVG[20]	A	0
10685 float	_FFT_ILN4_AVG[21]	A	0
10687 float	_FFT_ILN4_AVG[22]	A	0
10689 float	_FFT_ILN4_AVG[23]	A	0
10691 float	_FFT_ILN4_AVG[24]	A	0
10693 float	_FFT_ILN4_AVG[25]	A	0
10695 float	_FFT_ILN4_AVG[26]	A	0
10697 float	_FFT_ILN4_AVG[27]	A	0
10699 float	_FFT_ILN4_AVG[28]	A	0
10701 float	_FFT_ILN4_AVG[29]	A	0
10703 float	_FFT_ILN4_AVG[30]	A	0
10705 float	_FFT_ILN4_AVG[31]	A	0
10707 float	_FFT_ILN4_AVG[32]	A	0
10709 float	_FFT_ILN4_AVG[33]	A	0
10711 float	_FFT_ILN4_AVG[34]	A	0
10713 float	_FFT_ILN4_AVG[35]	A	0
10715 float	_FFT_ILN4_AVG[36]	A	0
10717 float	_FFT_ILN4_AVG[37]	A	0
10719 float	_FFT_ILN4_AVG[38]	A	0
10721 float	_FFT_ILN4_AVG[39]	A	0
10723 short	_ILN4_I	mA	0
10724 short	_THD_ILN4_I	%	0
10725 short	_TDD_ILN4_I	%	0
10726 short	_S0_POWER_I[0]	100mW	0
10727 short	_S0_POWER_I[1]	100mW	0
10728 short	_S0_POWER_I[2]	100mW	0
10730 short	_FFT_ILN4_I[0]	A	0
10731 short	_FFT_ILN4_I[1]	A	0
10732 short	_FFT_ILN4_I[2]	A	0
10733 short	_FFT_ILN4_I[3]	A	0
10734 short	_FFT_ILN4_I[4]	A	0
10735 short	_FFT_ILN4_I[5]	A	0
10736 short	_FFT_ILN4_I[6]	A	0
10737 short	_FFT_ILN4_I[7]	A	0
10738 short	_FFT_ILN4_I[8]	A	0
10739 short	_FFT_ILN4_I[9]	A	0
10740 short	_FFT_ILN4_I[10]	A	0
10741 short	_FFT_ILN4_I[11]	A	0
10742 short	_FFT_ILN4_I[12]	A	0
10743 short	_FFT_ILN4_I[13]	A	0
10744 short	_FFT_ILN4_I[14]	A	0
10745 short	_FFT_ILN4_I[15]	A	0
10746 short	_FFT_ILN4_I[16]	A	0
10747 short	_FFT_ILN4_I[17]	A	0
10748 short	_FFT_ILN4_I[18]	A	0
10749 short	_FFT_ILN4_I[19]	A	0
10750 short	_FFT_ILN4_I[20]	A	0
10751 short	_FFT_ILN4_I[21]	A	0
10752 short	_FFT_ILN4_I[22]	A	0
10753 short	_FFT_ILN4_I[23]	A	0

10754 short	_FFT_ILN4_I[24]	A	0
10755 short	_FFT_ILN4_I[25]	A	0
10756 short	_FFT_ILN4_I[26]	A	0
10757 short	_FFT_ILN4_I[27]	A	0
10758 short	_FFT_ILN4_I[28]	A	0
10759 short	_FFT_ILN4_I[29]	A	0
10760 short	_FFT_ILN4_I[30]	A	0
10761 short	_FFT_ILN4_I[31]	A	0
10762 short	_FFT_ILN4_I[32]	A	0
10763 short	_FFT_ILN4_I[33]	A	0
10764 short	_FFT_ILN4_I[34]	A	0
10765 short	_FFT_ILN4_I[35]	A	0
10766 short	_FFT_ILN4_I[36]	A	0
10767 short	_FFT_ILN4_I[37]	A	0
10768 short	_FFT_ILN4_I[38]	A	0
10769 short	_FFT_ILN4_I[39]	A	0
10770 short	_ILN4_AVG_I	mA	0
10771 short	_THD_ILN4_AVG_I	%	0
10772 short	_TDD_ILN4_AVG_I	%	0
10773 short	_S0_POWER_AVG_I[0]	100mW	0
10774 short	_S0_POWER_AVG_I[1]	100mW	0
10775 short	_S0_POWER_AVG_I[2]	100mW	0
10777 short	_FFT_ILN4_AVG_I[0]	A	0
10778 short	_FFT_ILN4_AVG_I[1]	A	0
10779 short	_FFT_ILN4_AVG_I[2]	A	0
10780 short	_FFT_ILN4_AVG_I[3]	A	0
10781 short	_FFT_ILN4_AVG_I[4]	A	0
10782 short	_FFT_ILN4_AVG_I[5]	A	0
10783 short	_FFT_ILN4_AVG_I[6]	A	0
10784 short	_FFT_ILN4_AVG_I[7]	A	0
10785 short	_FFT_ILN4_AVG_I[8]	A	0
10786 short	_FFT_ILN4_AVG_I[9]	A	0
10787 short	_FFT_ILN4_AVG_I[10]	A	0
10788 short	_FFT_ILN4_AVG_I[11]	A	0
10789 short	_FFT_ILN4_AVG_I[12]	A	0
10790 short	_FFT_ILN4_AVG_I[13]	A	0
10791 short	_FFT_ILN4_AVG_I[14]	A	0
10792 short	_FFT_ILN4_AVG_I[15]	A	0
10793 short	_FFT_ILN4_AVG_I[16]	A	0
10794 short	_FFT_ILN4_AVG_I[17]	A	0
10795 short	_FFT_ILN4_AVG_I[18]	A	0
10796 short	_FFT_ILN4_AVG_I[19]	A	0
10797 short	_FFT_ILN4_AVG_I[20]	A	0
10798 short	_FFT_ILN4_AVG_I[21]	A	0
10799 short	_FFT_ILN4_AVG_I[22]	A	0
10800 short	_FFT_ILN4_AVG_I[23]	A	0
10801 short	_FFT_ILN4_AVG_I[24]	A	0
10802 short	_FFT_ILN4_AVG_I[25]	A	0
10803 short	_FFT_ILN4_AVG_I[26]	A	0
10804 short	_FFT_ILN4_AVG_I[27]	A	0
10805 short	_FFT_ILN4_AVG_I[28]	A	0
10806 short	_FFT_ILN4_AVG_I[29]	A	0
10807 short	_FFT_ILN4_AVG_I[30]	A	0

10808 short	_FFT_ILN4_AVG_I[31]	A	0
10809 short	_FFT_ILN4_AVG_I[32]	A	0
10810 short	_FFT_ILN4_AVG_I[33]	A	0
10811 short	_FFT_ILN4_AVG_I[34]	A	0
10812 short	_FFT_ILN4_AVG_I[35]	A	0
10813 short	_FFT_ILN4_AVG_I[36]	A	0
10814 short	_FFT_ILN4_AVG_I[37]	A	0
10815 short	_FFT_ILN4_AVG_I[38]	A	0
10816 short	_FFT_ILN4_AVG_I[39]	A	0
10817 short	_ILN4_MAX_I	mA	0
10818 short	_THD_ILN4_MAX_I	%	0
10819 short	_TDD_ILN4_MAX_I	%	0
10820 short	_S0_POWER_MAX_I[0]	100mW	0
10821 short	_S0_POWER_MAX_I[1]	100mW	0
10822 short	_S0_POWER_MAX_I[2]	100mW	0
10824 short	_FFT_ILN4_MAX_I[0]	A	0
10825 short	_FFT_ILN4_MAX_I[1]	A	0
10826 short	_FFT_ILN4_MAX_I[2]	A	0
10827 short	_FFT_ILN4_MAX_I[3]	A	0
10828 short	_FFT_ILN4_MAX_I[4]	A	0
10829 short	_FFT_ILN4_MAX_I[5]	A	0
10830 short	_FFT_ILN4_MAX_I[6]	A	0
10831 short	_FFT_ILN4_MAX_I[7]	A	0
10832 short	_FFT_ILN4_MAX_I[8]	A	0
10833 short	_FFT_ILN4_MAX_I[9]	A	0
10834 short	_FFT_ILN4_MAX_I[10]	A	0
10835 short	_FFT_ILN4_MAX_I[11]	A	0
10836 short	_FFT_ILN4_MAX_I[12]	A	0
10837 short	_FFT_ILN4_MAX_I[13]	A	0
10838 short	_FFT_ILN4_MAX_I[14]	A	0
10839 short	_FFT_ILN4_MAX_I[15]	A	0
10840 short	_FFT_ILN4_MAX_I[16]	A	0
10841 short	_FFT_ILN4_MAX_I[17]	A	0
10842 short	_FFT_ILN4_MAX_I[18]	A	0
10843 short	_FFT_ILN4_MAX_I[19]	A	0
10844 short	_FFT_ILN4_MAX_I[20]	A	0
10845 short	_FFT_ILN4_MAX_I[21]	A	0
10846 short	_FFT_ILN4_MAX_I[22]	A	0
10847 short	_FFT_ILN4_MAX_I[23]	A	0
10848 short	_FFT_ILN4_MAX_I[24]	A	0
10849 short	_FFT_ILN4_MAX_I[25]	A	0
10850 short	_FFT_ILN4_MAX_I[26]	A	0
10851 short	_FFT_ILN4_MAX_I[27]	A	0
10852 short	_FFT_ILN4_MAX_I[28]	A	0
10853 short	_FFT_ILN4_MAX_I[29]	A	0
10854 short	_FFT_ILN4_MAX_I[30]	A	0
10855 short	_FFT_ILN4_MAX_I[31]	A	0
10856 short	_FFT_ILN4_MAX_I[32]	A	0
10857 short	_FFT_ILN4_MAX_I[33]	A	0
10858 short	_FFT_ILN4_MAX_I[34]	A	0
10859 short	_FFT_ILN4_MAX_I[35]	A	0
10860 short	_FFT_ILN4_MAX_I[36]	A	0
10861 short	_FFT_ILN4_MAX_I[37]	A	0

10862 short	_FFT_ILN4_MAX_I[38]	A	0
10863 short	_FFT_ILN4_MAX_I[39]	A	0
10864 short	_IL4_AVG_MAX_I	mA	0
10865 float	_TEMPERATURE1	Â°C	0
10867 float	_TEMPERATURE2	Â°C	0
10869 float	_I_4_20_DIFF1	%	0
10871 float	_I_4_20_DIFF2	%	0
10873 float	_I_DIFF1	A	0
10875 float	_I_DIFF2	A	0
10877 float	_THD_I_DIFF1	%	0
10879 float	_THD_I_DIFF2	%	0
10881 float	_FFT_DIFF1[0]	A	0
10883 float	_FFT_DIFF1[1]	A	0
10885 float	_FFT_DIFF1[2]	A	0
10887 float	_FFT_DIFF1[3]	A	0
10889 float	_FFT_DIFF1[4]	A	0
10891 float	_FFT_DIFF1[5]	A	0
10893 float	_FFT_DIFF1[6]	A	0
10895 float	_FFT_DIFF1[7]	A	0
10897 float	_FFT_DIFF1[8]	A	0
10899 float	_FFT_DIFF1[9]	A	0
10901 float	_FFT_DIFF1[10]	A	0
10903 float	_FFT_DIFF1[11]	A	0
10905 float	_FFT_DIFF1[12]	A	0
10907 float	_FFT_DIFF1[13]	A	0
10909 float	_FFT_DIFF1[14]	A	0
10911 float	_FFT_DIFF1[15]	A	0
10913 float	_FFT_DIFF1[16]	A	0
10915 float	_FFT_DIFF1[17]	A	0
10917 float	_FFT_DIFF1[18]	A	0
10919 float	_FFT_DIFF1[19]	A	0
10921 float	_FFT_DIFF1[20]	A	0
10923 float	_FFT_DIFF1[21]	A	0
10925 float	_FFT_DIFF1[22]	A	0
10927 float	_FFT_DIFF1[23]	A	0
10929 float	_FFT_DIFF1[24]	A	0
10931 float	_FFT_DIFF1[25]	A	0
10933 float	_FFT_DIFF1[26]	A	0
10935 float	_FFT_DIFF1[27]	A	0
10937 float	_FFT_DIFF1[28]	A	0
10939 float	_FFT_DIFF1[29]	A	0
10941 float	_FFT_DIFF1[30]	A	0
10943 float	_FFT_DIFF1[31]	A	0
10945 float	_FFT_DIFF1[32]	A	0
10947 float	_FFT_DIFF1[33]	A	0
10949 float	_FFT_DIFF1[34]	A	0
10951 float	_FFT_DIFF1[35]	A	0
10953 float	_FFT_DIFF1[36]	A	0
10955 float	_FFT_DIFF1[37]	A	0
10957 float	_FFT_DIFF1[38]	A	0
10959 float	_FFT_DIFF1[39]	A	0
10961 float	_FFT_DIFF2[0]	A	0
10963 float	_FFT_DIFF2[1]	A	0

10965 float	_FFT_DIFF2[2]	A	0
10967 float	_FFT_DIFF2[3]	A	0
10969 float	_FFT_DIFF2[4]	A	0
10971 float	_FFT_DIFF2[5]	A	0
10973 float	_FFT_DIFF2[6]	A	0
10975 float	_FFT_DIFF2[7]	A	0
10977 float	_FFT_DIFF2[8]	A	0
10979 float	_FFT_DIFF2[9]	A	0
10981 float	_FFT_DIFF2[10]	A	0
10983 float	_FFT_DIFF2[11]	A	0
10985 float	_FFT_DIFF2[12]	A	0
10987 float	_FFT_DIFF2[13]	A	0
10989 float	_FFT_DIFF2[14]	A	0
10991 float	_FFT_DIFF2[15]	A	0
10993 float	_FFT_DIFF2[16]	A	0
10995 float	_FFT_DIFF2[17]	A	0
10997 float	_FFT_DIFF2[18]	A	0
10999 float	_FFT_DIFF2[19]	A	0
11001 float	_FFT_DIFF2[20]	A	0
11003 float	_FFT_DIFF2[21]	A	0
11005 float	_FFT_DIFF2[22]	A	0
11007 float	_FFT_DIFF2[23]	A	0
11009 float	_FFT_DIFF2[24]	A	0
11011 float	_FFT_DIFF2[25]	A	0
11013 float	_FFT_DIFF2[26]	A	0
11015 float	_FFT_DIFF2[27]	A	0
11017 float	_FFT_DIFF2[28]	A	0
11019 float	_FFT_DIFF2[29]	A	0
11021 float	_FFT_DIFF2[30]	A	0
11023 float	_FFT_DIFF2[31]	A	0
11025 float	_FFT_DIFF2[32]	A	0
11027 float	_FFT_DIFF2[33]	A	0
11029 float	_FFT_DIFF2[34]	A	0
11031 float	_FFT_DIFF2[35]	A	0
11033 float	_FFT_DIFF2[36]	A	0
11035 float	_FFT_DIFF2[37]	A	0
11037 float	_FFT_DIFF2[38]	A	0
11039 float	_FFT_DIFF2[39]	A	0
11041 float	_TEMPERATURE1_AVG	Â°C	0
11043 float	_TEMPERATURE2_AVG	Â°C	0
11045 float	_I_4_20_DIFF1_AVG	%	0
11047 float	_I_4_20_DIFF2_AVG	%	0
11049 float	_I_DIFF1_AVG	A	0
11051 float	_I_DIFF2_AVG	A	0
11053 float	_THD_I_DIFF1_AVG	%	0
11055 float	_THD_I_DIFF2_AVG	%	0
11057 float	_FFT_DIFF1_AVG[0]	A	0
11059 float	_FFT_DIFF1_AVG[1]	A	0
11061 float	_FFT_DIFF1_AVG[2]	A	0
11063 float	_FFT_DIFF1_AVG[3]	A	0
11065 float	_FFT_DIFF1_AVG[4]	A	0
11067 float	_FFT_DIFF1_AVG[5]	A	0
11069 float	_FFT_DIFF1_AVG[6]	A	0

11071 float	_FFT_DIFF1_AVG[7]	A	0
11073 float	_FFT_DIFF1_AVG[8]	A	0
11075 float	_FFT_DIFF1_AVG[9]	A	0
11077 float	_FFT_DIFF1_AVG[10]	A	0
11079 float	_FFT_DIFF1_AVG[11]	A	0
11081 float	_FFT_DIFF1_AVG[12]	A	0
11083 float	_FFT_DIFF1_AVG[13]	A	0
11085 float	_FFT_DIFF1_AVG[14]	A	0
11087 float	_FFT_DIFF1_AVG[15]	A	0
11089 float	_FFT_DIFF1_AVG[16]	A	0
11091 float	_FFT_DIFF1_AVG[17]	A	0
11093 float	_FFT_DIFF1_AVG[18]	A	0
11095 float	_FFT_DIFF1_AVG[19]	A	0
11097 float	_FFT_DIFF1_AVG[20]	A	0
11099 float	_FFT_DIFF1_AVG[21]	A	0
11101 float	_FFT_DIFF1_AVG[22]	A	0
11103 float	_FFT_DIFF1_AVG[23]	A	0
11105 float	_FFT_DIFF1_AVG[24]	A	0
11107 float	_FFT_DIFF1_AVG[25]	A	0
11109 float	_FFT_DIFF1_AVG[26]	A	0
11111 float	_FFT_DIFF1_AVG[27]	A	0
11113 float	_FFT_DIFF1_AVG[28]	A	0
11115 float	_FFT_DIFF1_AVG[29]	A	0
11117 float	_FFT_DIFF1_AVG[30]	A	0
11119 float	_FFT_DIFF1_AVG[31]	A	0
11121 float	_FFT_DIFF1_AVG[32]	A	0
11123 float	_FFT_DIFF1_AVG[33]	A	0
11125 float	_FFT_DIFF1_AVG[34]	A	0
11127 float	_FFT_DIFF1_AVG[35]	A	0
11129 float	_FFT_DIFF1_AVG[36]	A	0
11131 float	_FFT_DIFF1_AVG[37]	A	0
11133 float	_FFT_DIFF1_AVG[38]	A	0
11135 float	_FFT_DIFF1_AVG[39]	A	0
11137 float	_FFT_DIFF2_AVG[0]	A	0
11139 float	_FFT_DIFF2_AVG[1]	A	0
11141 float	_FFT_DIFF2_AVG[2]	A	0
11143 float	_FFT_DIFF2_AVG[3]	A	0
11145 float	_FFT_DIFF2_AVG[4]	A	0
11147 float	_FFT_DIFF2_AVG[5]	A	0
11149 float	_FFT_DIFF2_AVG[6]	A	0
11151 float	_FFT_DIFF2_AVG[7]	A	0
11153 float	_FFT_DIFF2_AVG[8]	A	0
11155 float	_FFT_DIFF2_AVG[9]	A	0
11157 float	_FFT_DIFF2_AVG[10]	A	0
11159 float	_FFT_DIFF2_AVG[11]	A	0
11161 float	_FFT_DIFF2_AVG[12]	A	0
11163 float	_FFT_DIFF2_AVG[13]	A	0
11165 float	_FFT_DIFF2_AVG[14]	A	0
11167 float	_FFT_DIFF2_AVG[15]	A	0
11169 float	_FFT_DIFF2_AVG[16]	A	0
11171 float	_FFT_DIFF2_AVG[17]	A	0
11173 float	_FFT_DIFF2_AVG[18]	A	0
11175 float	_FFT_DIFF2_AVG[19]	A	0

11177 float	_FFT_DIFF2_AVG[20]	A	0
11179 float	_FFT_DIFF2_AVG[21]	A	0
11181 float	_FFT_DIFF2_AVG[22]	A	0
11183 float	_FFT_DIFF2_AVG[23]	A	0
11185 float	_FFT_DIFF2_AVG[24]	A	0
11187 float	_FFT_DIFF2_AVG[25]	A	0
11189 float	_FFT_DIFF2_AVG[26]	A	0
11191 float	_FFT_DIFF2_AVG[27]	A	0
11193 float	_FFT_DIFF2_AVG[28]	A	0
11195 float	_FFT_DIFF2_AVG[29]	A	0
11197 float	_FFT_DIFF2_AVG[30]	A	0
11199 float	_FFT_DIFF2_AVG[31]	A	0
11201 float	_FFT_DIFF2_AVG[32]	A	0
11203 float	_FFT_DIFF2_AVG[33]	A	0
11205 float	_FFT_DIFF2_AVG[34]	A	0
11207 float	_FFT_DIFF2_AVG[35]	A	0
11209 float	_FFT_DIFF2_AVG[36]	A	0
11211 float	_FFT_DIFF2_AVG[37]	A	0
11213 float	_FFT_DIFF2_AVG[38]	A	0
11215 float	_FFT_DIFF2_AVG[39]	A	0
11217 float	_TEMPERATURE1_MAX	Â°C	0
11219 float	_TEMPERATURE2_MAX	Â°C	0
11221 float	_I_4_20_DIFF1_MAX	%	0
11223 float	_I_4_20_DIFF2_MAX	%	0
11225 float	_I_DIFF1_MAX	A	0
11227 float	_I_DIFF2_MAX	A	0
11229 float	_THD_I_DIFF1_MAX	%	0
11231 float	_THD_I_DIFF2_MAX	%	0
11233 float	_TEMPERATURE1_MAX_AVG	Â°C	0
11235 float	_TEMPERATURE2_MAX_AVG	Â°C	0
11237 float	_I_4_20_DIFF1_MAX_AVG	%	0
11239 float	_I_4_20_DIFF2_MAX_AVG	%	0
11241 float	_I_DIFF1_MAX_AVG	A	0
11243 float	_I_DIFF2_MAX_AVG	A	0
11245 uint	_TEMPERATURE1_MAX_T	Â°C	0
11247 uint	_TEMPERATURE2_MAX_T	Â°C	0
11249 uint	_I_4_20_DIFF1_MAX_T	%	0
11251 uint	_I_4_20_DIFF2_MAX_T	%	0
11253 uint	_I_DIFF1_MAX_T		0
11255 uint	_I_DIFF2_MAX_T		0
11257 uint	_THD_I_DIFF1_MAX_T		0
11259 uint	_THD_I_DIFF2_MAX_T		0
11261 uint	_TEMPERATURE1_MAX_AVG_T	Â°C	0
11263 uint	_TEMPERATURE2_MAX_AVG_T	Â°C	0
11265 uint	_I_4_20_DIFF1_MAX_AVG_T	%	0
11267 uint	_I_4_20_DIFF2_MAX_AVG_T	%	0
11269 uint	_I_DIFF1_MAX_AVG_T		0
11271 uint	_I_DIFF2_MAX_AVG_T		0
11273 short	_TEMPERATURE1_I	Â°C	0
11274 short	_TEMPERATURE2_I	Â°C	0
11275 short	_I_4_20_DIFF1_I	%	0
11276 short	_I_4_20_DIFF2_I	%	0
11277 short	_I_DIFF1_I	A	0

11278 short	_I_DIFF2_I	A	0
11279 short	_THD_I_DIFF1_I	%	0
11280 short	_THD_I_DIFF2_I	%	0
11281 short	_FFT_DIFF1_I[0]	A	0
11282 short	_FFT_DIFF1_I[1]	A	0
11283 short	_FFT_DIFF1_I[2]	A	0
11284 short	_FFT_DIFF1_I[3]	A	0
11285 short	_FFT_DIFF1_I[4]	A	0
11286 short	_FFT_DIFF1_I[5]	A	0
11287 short	_FFT_DIFF1_I[6]	A	0
11288 short	_FFT_DIFF1_I[7]	A	0
11289 short	_FFT_DIFF1_I[8]	A	0
11290 short	_FFT_DIFF1_I[9]	A	0
11291 short	_FFT_DIFF1_I[10]	A	0
11292 short	_FFT_DIFF1_I[11]	A	0
11293 short	_FFT_DIFF1_I[12]	A	0
11294 short	_FFT_DIFF1_I[13]	A	0
11295 short	_FFT_DIFF1_I[14]	A	0
11296 short	_FFT_DIFF1_I[15]	A	0
11297 short	_FFT_DIFF1_I[16]	A	0
11298 short	_FFT_DIFF1_I[17]	A	0
11299 short	_FFT_DIFF1_I[18]	A	0
11300 short	_FFT_DIFF1_I[19]	A	0
11301 short	_FFT_DIFF1_I[20]	A	0
11302 short	_FFT_DIFF1_I[21]	A	0
11303 short	_FFT_DIFF1_I[22]	A	0
11304 short	_FFT_DIFF1_I[23]	A	0
11305 short	_FFT_DIFF1_I[24]	A	0
11306 short	_FFT_DIFF1_I[25]	A	0
11307 short	_FFT_DIFF1_I[26]	A	0
11308 short	_FFT_DIFF1_I[27]	A	0
11309 short	_FFT_DIFF1_I[28]	A	0
11310 short	_FFT_DIFF1_I[29]	A	0
11311 short	_FFT_DIFF1_I[30]	A	0
11312 short	_FFT_DIFF1_I[31]	A	0
11313 short	_FFT_DIFF1_I[32]	A	0
11314 short	_FFT_DIFF1_I[33]	A	0
11315 short	_FFT_DIFF1_I[34]	A	0
11316 short	_FFT_DIFF1_I[35]	A	0
11317 short	_FFT_DIFF1_I[36]	A	0
11318 short	_FFT_DIFF1_I[37]	A	0
11319 short	_FFT_DIFF1_I[38]	A	0
11320 short	_FFT_DIFF1_I[39]	A	0
11321 short	_FFT_DIFF2_I[0]	A	0
11322 short	_FFT_DIFF2_I[1]	A	0
11323 short	_FFT_DIFF2_I[2]	A	0
11324 short	_FFT_DIFF2_I[3]	A	0
11325 short	_FFT_DIFF2_I[4]	A	0
11326 short	_FFT_DIFF2_I[5]	A	0
11327 short	_FFT_DIFF2_I[6]	A	0
11328 short	_FFT_DIFF2_I[7]	A	0
11329 short	_FFT_DIFF2_I[8]	A	0
11330 short	_FFT_DIFF2_I[9]	A	0

11331 short	_FFT_DIFF2_I[10]	A	0
11332 short	_FFT_DIFF2_I[11]	A	0
11333 short	_FFT_DIFF2_I[12]	A	0
11334 short	_FFT_DIFF2_I[13]	A	0
11335 short	_FFT_DIFF2_I[14]	A	0
11336 short	_FFT_DIFF2_I[15]	A	0
11337 short	_FFT_DIFF2_I[16]	A	0
11338 short	_FFT_DIFF2_I[17]	A	0
11339 short	_FFT_DIFF2_I[18]	A	0
11340 short	_FFT_DIFF2_I[19]	A	0
11341 short	_FFT_DIFF2_I[20]	A	0
11342 short	_FFT_DIFF2_I[21]	A	0
11343 short	_FFT_DIFF2_I[22]	A	0
11344 short	_FFT_DIFF2_I[23]	A	0
11345 short	_FFT_DIFF2_I[24]	A	0
11346 short	_FFT_DIFF2_I[25]	A	0
11347 short	_FFT_DIFF2_I[26]	A	0
11348 short	_FFT_DIFF2_I[27]	A	0
11349 short	_FFT_DIFF2_I[28]	A	0
11350 short	_FFT_DIFF2_I[29]	A	0
11351 short	_FFT_DIFF2_I[30]	A	0
11352 short	_FFT_DIFF2_I[31]	A	0
11353 short	_FFT_DIFF2_I[32]	A	0
11354 short	_FFT_DIFF2_I[33]	A	0
11355 short	_FFT_DIFF2_I[34]	A	0
11356 short	_FFT_DIFF2_I[35]	A	0
11357 short	_FFT_DIFF2_I[36]	A	0
11358 short	_FFT_DIFF2_I[37]	A	0
11359 short	_FFT_DIFF2_I[38]	A	0
11360 short	_FFT_DIFF2_I[39]	A	0
11361 short	_TEMPERATURE1_AVG_I	Â°C	0
11362 short	_TEMPERATURE2_AVG_I	Â°C	0
11363 short	_I_4_20_DIFF1_AVG_I	%	0
11364 short	_I_4_20_DIFF2_AVG_I	%	0
11365 short	_I_DIFF1_AVG_I	A	0
11366 short	_I_DIFF2_AVG_I	A	0
11367 short	_THD_I_DIFF1_AVG_I	%	0
11368 short	_THD_I_DIFF2_AVG_I	%	0
11369 short	_FFT_DIFF1_AVG_I[0]	A	0
11370 short	_FFT_DIFF1_AVG_I[1]	A	0
11371 short	_FFT_DIFF1_AVG_I[2]	A	0
11372 short	_FFT_DIFF1_AVG_I[3]	A	0
11373 short	_FFT_DIFF1_AVG_I[4]	A	0
11374 short	_FFT_DIFF1_AVG_I[5]	A	0
11375 short	_FFT_DIFF1_AVG_I[6]	A	0
11376 short	_FFT_DIFF1_AVG_I[7]	A	0
11377 short	_FFT_DIFF1_AVG_I[8]	A	0
11378 short	_FFT_DIFF1_AVG_I[9]	A	0
11379 short	_FFT_DIFF1_AVG_I[10]	A	0
11380 short	_FFT_DIFF1_AVG_I[11]	A	0
11381 short	_FFT_DIFF1_AVG_I[12]	A	0
11382 short	_FFT_DIFF1_AVG_I[13]	A	0
11383 short	_FFT_DIFF1_AVG_I[14]	A	0

11384 short	_FFT_DIFF1_AVG_I[15]	A	0
11385 short	_FFT_DIFF1_AVG_I[16]	A	0
11386 short	_FFT_DIFF1_AVG_I[17]	A	0
11387 short	_FFT_DIFF1_AVG_I[18]	A	0
11388 short	_FFT_DIFF1_AVG_I[19]	A	0
11389 short	_FFT_DIFF1_AVG_I[20]	A	0
11390 short	_FFT_DIFF1_AVG_I[21]	A	0
11391 short	_FFT_DIFF1_AVG_I[22]	A	0
11392 short	_FFT_DIFF1_AVG_I[23]	A	0
11393 short	_FFT_DIFF1_AVG_I[24]	A	0
11394 short	_FFT_DIFF1_AVG_I[25]	A	0
11395 short	_FFT_DIFF1_AVG_I[26]	A	0
11396 short	_FFT_DIFF1_AVG_I[27]	A	0
11397 short	_FFT_DIFF1_AVG_I[28]	A	0
11398 short	_FFT_DIFF1_AVG_I[29]	A	0
11399 short	_FFT_DIFF1_AVG_I[30]	A	0
11400 short	_FFT_DIFF1_AVG_I[31]	A	0
11401 short	_FFT_DIFF1_AVG_I[32]	A	0
11402 short	_FFT_DIFF1_AVG_I[33]	A	0
11403 short	_FFT_DIFF1_AVG_I[34]	A	0
11404 short	_FFT_DIFF1_AVG_I[35]	A	0
11405 short	_FFT_DIFF1_AVG_I[36]	A	0
11406 short	_FFT_DIFF1_AVG_I[37]	A	0
11407 short	_FFT_DIFF1_AVG_I[38]	A	0
11408 short	_FFT_DIFF1_AVG_I[39]	A	0
11409 short	_FFT_DIFF2_AVG_I[0]	A	0
11410 short	_FFT_DIFF2_AVG_I[1]	A	0
11411 short	_FFT_DIFF2_AVG_I[2]	A	0
11412 short	_FFT_DIFF2_AVG_I[3]	A	0
11413 short	_FFT_DIFF2_AVG_I[4]	A	0
11414 short	_FFT_DIFF2_AVG_I[5]	A	0
11415 short	_FFT_DIFF2_AVG_I[6]	A	0
11416 short	_FFT_DIFF2_AVG_I[7]	A	0
11417 short	_FFT_DIFF2_AVG_I[8]	A	0
11418 short	_FFT_DIFF2_AVG_I[9]	A	0
11419 short	_FFT_DIFF2_AVG_I[10]	A	0
11420 short	_FFT_DIFF2_AVG_I[11]	A	0
11421 short	_FFT_DIFF2_AVG_I[12]	A	0
11422 short	_FFT_DIFF2_AVG_I[13]	A	0
11423 short	_FFT_DIFF2_AVG_I[14]	A	0
11424 short	_FFT_DIFF2_AVG_I[15]	A	0
11425 short	_FFT_DIFF2_AVG_I[16]	A	0
11426 short	_FFT_DIFF2_AVG_I[17]	A	0
11427 short	_FFT_DIFF2_AVG_I[18]	A	0
11428 short	_FFT_DIFF2_AVG_I[19]	A	0
11429 short	_FFT_DIFF2_AVG_I[20]	A	0
11430 short	_FFT_DIFF2_AVG_I[21]	A	0
11431 short	_FFT_DIFF2_AVG_I[22]	A	0
11432 short	_FFT_DIFF2_AVG_I[23]	A	0
11433 short	_FFT_DIFF2_AVG_I[24]	A	0
11434 short	_FFT_DIFF2_AVG_I[25]	A	0
11435 short	_FFT_DIFF2_AVG_I[26]	A	0
11436 short	_FFT_DIFF2_AVG_I[27]	A	0

11437 short	_FFT_DIFF2_AVG_I[28]	A	0
11438 short	_FFT_DIFF2_AVG_I[29]	A	0
11439 short	_FFT_DIFF2_AVG_I[30]	A	0
11440 short	_FFT_DIFF2_AVG_I[31]	A	0
11441 short	_FFT_DIFF2_AVG_I[32]	A	0
11442 short	_FFT_DIFF2_AVG_I[33]	A	0
11443 short	_FFT_DIFF2_AVG_I[34]	A	0
11444 short	_FFT_DIFF2_AVG_I[35]	A	0
11445 short	_FFT_DIFF2_AVG_I[36]	A	0
11446 short	_FFT_DIFF2_AVG_I[37]	A	0
11447 short	_FFT_DIFF2_AVG_I[38]	A	0
11448 short	_FFT_DIFF2_AVG_I[39]	A	0
11449 short	_TEMPERATURE1_MAX_I	Â°C	0
11450 short	_TEMPERATURE2_MAX_I	Â°C	0
11451 short	_I_4_20_DIFF1_MAX_I	%	0
11452 short	_I_4_20_DIFF2_MAX_I	%	0
11453 short	_I_DIFF1_MAX_I	A	0
11454 short	_I_DIFF2_MAX_I	A	0
11455 short	_THD_I_DIFF1_MAX_I	%	0
11456 short	_THD_I_DIFF2_MAX_I	%	0
11457 short	_TEMPERATURE1_MAX_AVG_I	Â°C	0
11458 short	_TEMPERATURE2_MAX_AVG_I	Â°C	0
11459 short	_I_4_20_DIFF1_MAX_AVG_I	%	0
11460 short	_I_4_20_DIFF2_MAX_AVG_I	%	0
11461 short	_I_DIFF1_MAX_AVG_I	A	0
11462 short	_I_DIFF2_MAX_AVG_I	A	0
11463 float	_IA	A	0
11465 float	_IA_AVG	A	0
11467 float	_IA_MAX	A	0
11469 float	_IA_MAX_AVG	A	0
11471 uint	_IA_MAX_T	s	0
11473 uint	_IA_MAX_AVG_T	s	0
11475 float	_MONTHLY_SH[0]	VAh	0
11477 float	_MONTHLY_SH[1]	VAh	0
11479 float	_MONTHLY_SH[2]	VAh	0
11481 float	_MONTHLY_SH[3]	VAh	0
11483 float	_MONTHLY_SH[4]	VAh	0
11485 float	_MONTHLY_SH[5]	VAh	0
11487 float	_MONTHLY_SH[6]	VAh	0
11489 float	_MONTHLY_SH[7]	VAh	0
11491 float	_MONTHLY_SH[8]	VAh	0
11493 float	_MONTHLY_SH[9]	VAh	0
11495 float	_MONTHLY_SH[10]	VAh	0
11497 float	_MONTHLY_SH[11]	VAh	0
11499 float	_MONTHLY_SH[12]	VAh	0
11501 float	_MONTHLY_SH[13]	VAh	0
11503 float	_MONTHLY_SH[14]	VAh	0
11505 float	_MONTHLY_SH[15]	VAh	0
11507 float	_MONTHLY_SH[16]	VAh	0
11509 float	_MONTHLY_SH[17]	VAh	0
11511 float	_MONTHLY_SH[18]	VAh	0
11513 float	_MONTHLY_SH[19]	VAh	0
11515 float	_MONTHLY_SH[20]	VAh	0

11517 float	_MONTHLY_SH[21]	VAh	0
11519 float	_MONTHLY_SH[22]	VAh	0
11521 float	_MONTHLY_SH[23]	VAh	0
11523 float	_MONTHLY_VWH[0]	Wh	0
11525 float	_MONTHLY_VWH[1]	Wh	0
11527 float	_MONTHLY_VWH[2]	Wh	0
11529 float	_MONTHLY_VWH[3]	Wh	0
11531 float	_MONTHLY_VWH[4]	Wh	0
11533 float	_MONTHLY_VWH[5]	Wh	0
11535 float	_MONTHLY_VWH[6]	Wh	0
11537 float	_MONTHLY_VWH[7]	Wh	0
11539 float	_MONTHLY_VWH[8]	Wh	0
11541 float	_MONTHLY_VWH[9]	Wh	0
11543 float	_MONTHLY_VWH[10]	Wh	0
11545 float	_MONTHLY_VWH[11]	Wh	0
11547 float	_MONTHLY_VWH[12]	Wh	0
11549 float	_MONTHLY_VWH[13]	Wh	0
11551 float	_MONTHLY_VWH[14]	Wh	0
11553 float	_MONTHLY_VWH[15]	Wh	0
11555 float	_MONTHLY_VWH[16]	Wh	0
11557 float	_MONTHLY_VWH[17]	Wh	0
11559 float	_MONTHLY_VWH[18]	Wh	0
11561 float	_MONTHLY_VWH[19]	Wh	0
11563 float	_MONTHLY_VWH[20]	Wh	0
11565 float	_MONTHLY_VWH[21]	Wh	0
11567 float	_MONTHLY_VWH[22]	Wh	0
11569 float	_MONTHLY_VWH[23]	Wh	0
11571 float	_MONTHLY_IQH[0]	varh	0
11573 float	_MONTHLY_IQH[1]	varh	0
11575 float	_MONTHLY_IQH[2]	varh	0
11577 float	_MONTHLY_IQH[3]	varh	0
11579 float	_MONTHLY_IQH[4]	varh	0
11581 float	_MONTHLY_IQH[5]	varh	0
11583 float	_MONTHLY_IQH[6]	varh	0
11585 float	_MONTHLY_IQH[7]	varh	0
11587 float	_MONTHLY_IQH[8]	varh	0
11589 float	_MONTHLY_IQH[9]	varh	0
11591 float	_MONTHLY_IQH[10]	varh	0
11593 float	_MONTHLY_IQH[11]	varh	0
11595 float	_MONTHLY_IQH[12]	varh	0
11597 float	_MONTHLY_IQH[13]	varh	0
11599 float	_MONTHLY_IQH[14]	varh	0
11601 float	_MONTHLY_IQH[15]	varh	0
11603 float	_MONTHLY_IQH[16]	varh	0
11605 float	_MONTHLY_IQH[17]	varh	0
11607 float	_MONTHLY_IQH[18]	varh	0
11609 float	_MONTHLY_IQH[19]	varh	0
11611 float	_MONTHLY_IQH[20]	varh	0
11613 float	_MONTHLY_IQH[21]	varh	0
11615 float	_MONTHLY_IQH[22]	varh	0
11617 float	_MONTHLY_IQH[23]	varh	0
11619 float	_RESISTANCE1	Ohm	0
11621 float	_RESISTANCE2	Ohm	0

11623 short	_DIFF_TRANS_NC[0]		0
11624 short	_DIFF_TRANS_NC[1]		0
11625 short	_DIFF_WARN[0]		0
11626 short	_DIFF_WARN[1]		0
11627 short	_DIFF_OVERCURRENT[0]		0
11628 short	_DIFF_OVERCURRENT[1]		0
11629 int	_DUMMY		0
19000 float	_ULN[0]	V	0
19002 float	_ULN[1]	V	0
19004 float	_ULN[2]	V	0
19006 float	_ULL[0]	V	0
19008 float	_ULL[1]	V	0
19010 float	_ULL[2]	V	0
19012 float	_ILN[0]	A	0
19014 float	_ILN[1]	A	0
19016 float	_ILN[2]	A	0
19018 float	_ILN[3]	A	0
19020 float	_PLN[0]	W	0
19022 float	_PLN[1]	W	0
19024 float	_PLN[2]	W	0
19026 float	_PLN[3]	W	0
19028 float	_SLN[0]	VA	0
19030 float	_SLN[1]	VA	0
19032 float	_SLN[2]	VA	0
19034 float	_SLN[3]	VA	0
19036 float	_QLN[0]	var	0
19038 float	_QLN[1]	var	0
19040 float	_QLN[2]	var	0
19042 float	_QLN[3]	var	0
19044 float	_PF0[0]	W	0
19046 float	_PF0[1]	W	0
19048 float	_PF0[2]	W	0
19050 float	_FREQ	Hz	0
19052 float	_PHASE_SEQ		0
19054 dfloat	_WH_0	Wh	0
19056 dfloat	_WH_1	Wh	0
19058 dfloat	_WH_2	Wh	0
19060 dfloat	_WH_SUM	Wh	0
19062 dfloat	_WH_V0	Wh	0
19064 dfloat	_WH_V1	Wh	0
19066 dfloat	_WH_V2	Wh	0
19068 dfloat	_WH_VSUM	Wh	0
19070 dfloat	_WH_Z0	Wh	0
19072 dfloat	_WH_Z1	Wh	0
19074 dfloat	_WH_Z2	Wh	0
19076 dfloat	_WH_ZSUM	Wh	0
19078 dfloat	_WH_S0	VAh	0
19080 dfloat	_WH_S1	VAh	0
19082 dfloat	_WH_S2	VAh	0
19084 dfloat	_WH_SSUM	VAh	0
19086 dfloat	_QH0	varh	0
19088 dfloat	_QH0	varh	0
19090 dfloat	_QH0	varh	0

19092 dfloat	_QHSUM	varh	0
19094 dfloat	_IQH0	varh	0
19096 dfloat	_IQH0	varh	0
19098 dfloat	_IQH0	varh	0
19100 dfloat	_IQHSUM	varh	0
19102 dfloat	_CQH0	varh	0
19104 dfloat	_CQH0	varh	0
19106 dfloat	_CQH0	varh	0
19108 dfloat	_CQH3	varh	0
19110 float	_THD_ULN[0]	%	0
19112 float	_THD_ULN[1]	%	0
19114 float	_THD_ULN[2]	%	0
19116 float	_THD_ILN[0]	%	0
19118 float	_THD_ILN[1]	%	0
19120 float	_THD_ILN[2]	%	0
20000 ushort	_CALIB_KEY		0
20002 uint	_SYSTIME_RO	s	0
20004 uint	_SYSTIME	s	0
20006 float	_TDD_FULL_LOAD_L4	A	0
20008 float	_CT_PRIM_L4	A	0
20010 float	_CT_SEC_L4	A	0
20012 float	_CT_PRIM_L5	A	0
20014 float	_CT_SEC_L5	A	0
20016 float	_CT_PRIM_L6	A	0
20018 float	_CT_SEC_L6	A	0
20020 data	_REC_CFG1		212
20126 data	_REC_CFG2		212
20232 data	_REC_CFG3		212
20338 data	_REC_CFG4		212
20444 float	_S0_IN_VAL[0]		0
20446 float	_S0_IN_VAL[1]		0
20448 float	_S0_IN_VAL[2]		0
20450 string	_S0_IN_TYPE0		32
20466 string	_S0_IN_TYPE1		32
20482 string	_S0_IN_TYPE2		32
20498 string	_S0_IN_UNIT0		32
20514 string	_S0_IN_UNIT1		32
20530 string	_S0_IN_UNIT2		32
20546 string	_S0_IN_DESC0		100
20596 string	_S0_IN_DESC1		100
20646 string	_S0_IN_DESC2		100
20696 ushort	_PULSE_OUT_ADDR[0]		0
20697 ushort	_PULSE_OUT_ADDR[1]		0
20698 ushort	_PULSE_OUT_ADDR[2]		0
20699 ushort	_PULSE_OUT_ADDR[3]		0
20700 ushort	_PULSE_OUT_ADDR[4]		0
20701 float	_PULSE_OUT_VAL[0]		0
20703 float	_PULSE_OUT_VAL[1]		0
20705 float	_PULSE_OUT_VAL[2]		0
20707 float	_PULSE_OUT_VAL[3]		0
20709 float	_PULSE_OUT_VAL[4]		0
20711 short	_COMP_MIN_EXCEED0	s	0
20712 short	_COMP_MIN_SET0	s	0

20713 short	_COMP_CONCATENATION0	AND/OR	0
20714 float	_COMP_THRESHOLD0[0]		0
20716 float	_COMP_THRESHOLD0[1]		0
20718 float	_COMP_THRESHOLD0[2]		0
20720 float	_COMP_THRESHOLD0[3]		0
20722 float	_COMP_THRESHOLD0[4]		0
20724 float	_COMP_THRESHOLD0[5]		0
20726 float	_COMP_THRESHOLD0[6]		0
20728 float	_COMP_THRESHOLD0[7]		0
20730 float	_COMP_THRESHOLD0[8]		0
20732 float	_COMP_THRESHOLD0[9]		0
20734 short	_COMP_ADDR0[0]		0
20735 short	_COMP_ADDR0[1]		0
20736 short	_COMP_ADDR0[2]		0
20737 short	_COMP_ADDR0[3]		0
20738 short	_COMP_ADDR0[4]		0
20739 short	_COMP_ADDR0[5]		0
20740 short	_COMP_ADDR0[6]		0
20741 short	_COMP_ADDR0[7]		0
20742 short	_COMP_ADDR0[8]		0
20743 short	_COMP_ADDR0[9]		0
20744 short	_COMP_INVERT0[0]		0
20745 short	_COMP_INVERT0[1]		0
20746 short	_COMP_INVERT0[2]		0
20747 short	_COMP_INVERT0[3]		0
20748 short	_COMP_INVERT0[4]		0
20749 short	_COMP_INVERT0[5]		0
20750 short	_COMP_INVERT0[6]		0
20751 short	_COMP_INVERT0[7]		0
20752 short	_COMP_INVERT0[8]		0
20753 short	_COMP_INVERT0[9]		0
20754 short	_COMP_MIN_EXCEED1	s	0
20755 short	_COMP_MIN_SET1	s	0
20756 short	_COMP_CONCATENATION1	AND/OR	0
20757 float	_COMP_THRESHOLD1[0]		0
20759 float	_COMP_THRESHOLD1[1]		0
20761 float	_COMP_THRESHOLD1[2]		0
20763 float	_COMP_THRESHOLD1[3]		0
20765 float	_COMP_THRESHOLD1[4]		0
20767 float	_COMP_THRESHOLD1[5]		0
20769 float	_COMP_THRESHOLD1[6]		0
20771 float	_COMP_THRESHOLD1[7]		0
20773 float	_COMP_THRESHOLD1[8]		0
20775 float	_COMP_THRESHOLD1[9]		0
20777 short	_COMP_ADDR1[0]		0
20778 short	_COMP_ADDR1[1]		0
20779 short	_COMP_ADDR1[2]		0
20780 short	_COMP_ADDR1[3]		0
20781 short	_COMP_ADDR1[4]		0
20782 short	_COMP_ADDR1[5]		0
20783 short	_COMP_ADDR1[6]		0
20784 short	_COMP_ADDR1[7]		0
20785 short	_COMP_ADDR1[8]		0

20786 short	_COMP_ADDR1[9]		0
20787 short	_COMP_INVERT1[0]		0
20788 short	_COMP_INVERT1[1]		0
20789 short	_COMP_INVERT1[2]		0
20790 short	_COMP_INVERT1[3]		0
20791 short	_COMP_INVERT1[4]		0
20792 short	_COMP_INVERT1[5]		0
20793 short	_COMP_INVERT1[6]		0
20794 short	_COMP_INVERT1[7]		0
20795 short	_COMP_INVERT1[8]		0
20796 short	_COMP_INVERT1[9]		0
20797 short	_COMP_MIN_EXCEED2	s	0
20798 short	_COMP_MIN_SET2	s	0
20799 short	_COMP_CONCATENATION2	AND/OR	0
20800 float	_COMP_THRESHOLD2[0]		0
20802 float	_COMP_THRESHOLD2[1]		0
20804 float	_COMP_THRESHOLD2[2]		0
20806 float	_COMP_THRESHOLD2[3]		0
20808 float	_COMP_THRESHOLD2[4]		0
20810 float	_COMP_THRESHOLD2[5]		0
20812 float	_COMP_THRESHOLD2[6]		0
20814 float	_COMP_THRESHOLD2[7]		0
20816 float	_COMP_THRESHOLD2[8]		0
20818 float	_COMP_THRESHOLD2[9]		0
20820 short	_COMP_ADDR2[0]		0
20821 short	_COMP_ADDR2[1]		0
20822 short	_COMP_ADDR2[2]		0
20823 short	_COMP_ADDR2[3]		0
20824 short	_COMP_ADDR2[4]		0
20825 short	_COMP_ADDR2[5]		0
20826 short	_COMP_ADDR2[6]		0
20827 short	_COMP_ADDR2[7]		0
20828 short	_COMP_ADDR2[8]		0
20829 short	_COMP_ADDR2[9]		0
20830 short	_COMP_INVERT2[0]		0
20831 short	_COMP_INVERT2[1]		0
20832 short	_COMP_INVERT2[2]		0
20833 short	_COMP_INVERT2[3]		0
20834 short	_COMP_INVERT2[4]		0
20835 short	_COMP_INVERT2[5]		0
20836 short	_COMP_INVERT2[6]		0
20837 short	_COMP_INVERT2[7]		0
20838 short	_COMP_INVERT2[8]		0
20839 short	_COMP_INVERT2[9]		0
20840 short	_COMP_MIN_EXCEED3	s	0
20841 short	_COMP_MIN_SET3	s	0
20842 short	_COMP_CONCATENATION3	AND/OR	0
20843 float	_COMP_THRESHOLD3[0]		0
20845 float	_COMP_THRESHOLD3[1]		0
20847 float	_COMP_THRESHOLD3[2]		0
20849 float	_COMP_THRESHOLD3[3]		0
20851 float	_COMP_THRESHOLD3[4]		0
20853 float	_COMP_THRESHOLD3[5]		0

20855 float	_COMP_THRESHOLD3[6]		0
20857 float	_COMP_THRESHOLD3[7]		0
20859 float	_COMP_THRESHOLD3[8]		0
20861 float	_COMP_THRESHOLD3[9]		0
20863 short	_COMP_ADDR3[0]		0
20864 short	_COMP_ADDR3[1]		0
20865 short	_COMP_ADDR3[2]		0
20866 short	_COMP_ADDR3[3]		0
20867 short	_COMP_ADDR3[4]		0
20868 short	_COMP_ADDR3[5]		0
20869 short	_COMP_ADDR3[6]		0
20870 short	_COMP_ADDR3[7]		0
20871 short	_COMP_ADDR3[8]		0
20872 short	_COMP_ADDR3[9]		0
20873 short	_COMP_INVERT3[0]		0
20874 short	_COMP_INVERT3[1]		0
20875 short	_COMP_INVERT3[2]		0
20876 short	_COMP_INVERT3[3]		0
20877 short	_COMP_INVERT3[4]		0
20878 short	_COMP_INVERT3[5]		0
20879 short	_COMP_INVERT3[6]		0
20880 short	_COMP_INVERT3[7]		0
20881 short	_COMP_INVERT3[8]		0
20882 short	_COMP_INVERT3[9]		0
20883 short	_COMP_MIN_EXCEED4	s	0
20884 short	_COMP_MIN_SET4	s	0
20885 short	_COMP_CONCATENATION4	AND/OR	0
20886 float	_COMP_THRESHOLD4[0]		0
20888 float	_COMP_THRESHOLD4[1]		0
20890 float	_COMP_THRESHOLD4[2]		0
20892 float	_COMP_THRESHOLD4[3]		0
20894 float	_COMP_THRESHOLD4[4]		0
20896 float	_COMP_THRESHOLD4[5]		0
20898 float	_COMP_THRESHOLD4[6]		0
20900 float	_COMP_THRESHOLD4[7]		0
20902 float	_COMP_THRESHOLD4[8]		0
20904 float	_COMP_THRESHOLD4[9]		0
20906 short	_COMP_ADDR4[0]		0
20907 short	_COMP_ADDR4[1]		0
20908 short	_COMP_ADDR4[2]		0
20909 short	_COMP_ADDR4[3]		0
20910 short	_COMP_ADDR4[4]		0
20911 short	_COMP_ADDR4[5]		0
20912 short	_COMP_ADDR4[6]		0
20913 short	_COMP_ADDR4[7]		0
20914 short	_COMP_ADDR4[8]		0
20915 short	_COMP_ADDR4[9]		0
20916 short	_COMP_INVERT4[0]		0
20917 short	_COMP_INVERT4[1]		0
20918 short	_COMP_INVERT4[2]		0
20919 short	_COMP_INVERT4[3]		0
20920 short	_COMP_INVERT4[4]		0
20921 short	_COMP_INVERT4[5]		0

20922 short	_COMP_INVERT4[6]	0
20923 short	_COMP_INVERT4[7]	0
20924 short	_COMP_INVERT4[8]	0
20925 short	_COMP_INVERT4[9]	0
20926 short	_COMP_OUTPUT0[0]	0
20927 short	_COMP_OUTPUT0[1]	0
20928 short	_COMP_OUTPUT0[2]	0
20929 short	_COMP_OUTPUT0[3]	0
20930 short	_COMP_OUTPUT0[4]	0
20931 short	_COMP_OUTPUT0[5]	0
20932 short	_COMP_OUTPUT0[6]	0
20933 short	_COMP_OUTPUT0[7]	0
20934 short	_COMP_OUTPUT0[8]	0
20935 short	_COMP_OUTPUT0[9]	0
20936 short	_COMP_OUTPUT1[0]	0
20937 short	_COMP_OUTPUT1[1]	0
20938 short	_COMP_OUTPUT1[2]	0
20939 short	_COMP_OUTPUT1[3]	0
20940 short	_COMP_OUTPUT1[4]	0
20941 short	_COMP_OUTPUT1[5]	0
20942 short	_COMP_OUTPUT1[6]	0
20943 short	_COMP_OUTPUT1[7]	0
20944 short	_COMP_OUTPUT1[8]	0
20945 short	_COMP_OUTPUT1[9]	0
20946 short	_COMP_OUTPUT2[0]	0
20947 short	_COMP_OUTPUT2[1]	0
20948 short	_COMP_OUTPUT2[2]	0
20949 short	_COMP_OUTPUT2[3]	0
20950 short	_COMP_OUTPUT2[4]	0
20951 short	_COMP_OUTPUT2[5]	0
20952 short	_COMP_OUTPUT2[6]	0
20953 short	_COMP_OUTPUT2[7]	0
20954 short	_COMP_OUTPUT2[8]	0
20955 short	_COMP_OUTPUT2[9]	0
20956 short	_COMP_OUTPUT3[0]	0
20957 short	_COMP_OUTPUT3[1]	0
20958 short	_COMP_OUTPUT3[2]	0
20959 short	_COMP_OUTPUT3[3]	0
20960 short	_COMP_OUTPUT3[4]	0
20961 short	_COMP_OUTPUT3[5]	0
20962 short	_COMP_OUTPUT3[6]	0
20963 short	_COMP_OUTPUT3[7]	0
20964 short	_COMP_OUTPUT3[8]	0
20965 short	_COMP_OUTPUT3[9]	0
20966 short	_COMP_OUTPUT4[0]	0
20967 short	_COMP_OUTPUT4[1]	0
20968 short	_COMP_OUTPUT4[2]	0
20969 short	_COMP_OUTPUT4[3]	0
20970 short	_COMP_OUTPUT4[4]	0
20971 short	_COMP_OUTPUT4[5]	0
20972 short	_COMP_OUTPUT4[6]	0
20973 short	_COMP_OUTPUT4[7]	0
20974 short	_COMP_OUTPUT4[8]	0

20975 short	_COMP_OUTPUT4[9]		0
20976 short	_COMP_OUTPUT0		0
20977 short	_COMP_OUTPUT1		0
20978 short	_COMP_OUTPUT2		0
20979 short	_COMP_OUTPUT3		0
20980 short	_COMP_OUTPUT4		0
20981 short	_COMP_DIFF_TYPE0	s	0
20982 ushort	_COMP_DIFF_REF_ADDR0	s	0
20983 float	_COMP_DIFF_PER_DEV0	s	0
20985 short	_COMP_DIFF_DEV_CNT0	s	0
20986 float	_COMP_DIFF_CUR_PER0	s	0
20988 float	_COMP_DIFF_CUR_OFFSET0	s	0
20990 float	_COMP_DIFF_TOLERANCE0	s	0
20992 float	_COMP_DIFF_WARNLEVEL0	s	0
20994 float	_COMP_DIFF_CUR_THRESHOLD0[0]	s	0
20996 float	_COMP_DIFF_CUR_THRESHOLD0[1]	s	0
20998 float	_COMP_DIFF_CUR_THRESHOLD0[2]	s	0
21000 float	_COMP_DIFF_CUR_THRESHOLD0[3]	s	0
21002 float	_COMP_DIFF_CUR_THRESHOLD0[4]	s	0
21004 float	_COMP_DIFF_CUR_THRESHOLD0[5]	s	0
21006 float	_COMP_DIFF_CUR_THRESHOLD0[6]	s	0
21008 float	_COMP_DIFF_CUR_THRESHOLD0[7]	s	0
21010 float	_COMP_DIFF_CUR_THRESHOLD0[8]	s	0
21012 float	_COMP_DIFF_CUR_THRESHOLD0[9]	s	0
21014 float	_COMP_DIFF_STEPS0[0]	s	0
21016 float	_COMP_DIFF_STEPS0[1]	s	0
21018 float	_COMP_DIFF_STEPS0[2]	s	0
21020 float	_COMP_DIFF_STEPS0[3]	s	0
21022 float	_COMP_DIFF_STEPS0[4]	s	0
21024 float	_COMP_DIFF_STEPS0[5]	s	0
21026 float	_COMP_DIFF_STEPS0[6]	s	0
21028 float	_COMP_DIFF_STEPS0[7]	s	0
21030 float	_COMP_DIFF_STEPS0[8]	s	0
21032 float	_COMP_DIFF_STEPS0[9]	s	0
21034 float	_COMP_DIFF_CUR_THRESHOLD0	s	0
21036 float	_COMP_DIFF_MIN_TIME0	s	0
21038 short	_COMP_DIFF_TYPE1	s	0
21039 ushort	_COMP_DIFF_REF_ADDR1	s	0
21040 float	_COMP_DIFF_PER_DEV1	s	0
21042 short	_COMP_DIFF_DEV_CNT1	s	0
21043 float	_COMP_DIFF_CUR_PER1	s	0
21045 float	_COMP_DIFF_CUR_OFFSET1	s	0
21047 float	_COMP_DIFF_TOLERANCE1	s	0
21049 float	_COMP_DIFF_WARNLEVEL1	s	0
21051 float	_COMP_DIFF_CUR_THRESHOLD1[0]	s	0
21053 float	_COMP_DIFF_CUR_THRESHOLD1[1]	s	0
21055 float	_COMP_DIFF_CUR_THRESHOLD1[2]	s	0
21057 float	_COMP_DIFF_CUR_THRESHOLD1[3]	s	0
21059 float	_COMP_DIFF_CUR_THRESHOLD1[4]	s	0
21061 float	_COMP_DIFF_CUR_THRESHOLD1[5]	s	0
21063 float	_COMP_DIFF_CUR_THRESHOLD1[6]	s	0
21065 float	_COMP_DIFF_CUR_THRESHOLD1[7]	s	0
21067 float	_COMP_DIFF_CUR_THRESHOLD1[8]	s	0

21069 float	_COMP_DIFF_CUR_THRESHOLD1[9]	s	0
21071 float	_COMP_DIFF_STEPS1[0]	s	0
21073 float	_COMP_DIFF_STEPS1[1]	s	0
21075 float	_COMP_DIFF_STEPS1[2]	s	0
21077 float	_COMP_DIFF_STEPS1[3]	s	0
21079 float	_COMP_DIFF_STEPS1[4]	s	0
21081 float	_COMP_DIFF_STEPS1[5]	s	0
21083 float	_COMP_DIFF_STEPS1[6]	s	0
21085 float	_COMP_DIFF_STEPS1[7]	s	0
21087 float	_COMP_DIFF_STEPS1[8]	s	0
21089 float	_COMP_DIFF_STEPS1[9]	s	0
21091 float	_COMP_DIFF_CUR_THRESHOLD1	s	0
21093 float	_COMP_DIFF_MIN_TIME1	s	0
21095 short	_COMP_DIFF_STATUS0	s	0
21096 short	_COMP_DIFF_STATUS1	s	0
21097 float	_COMP_DIFF_RUN_TIME0	s	0
21099 float	_COMP_DIFF_RUN_TIME1	s	0
21101 float	_COMP_DIFF_LIMIT0	A	0
21103 float	_COMP_DIFF_LIMIT1	A	0
21105 short	_EVENT_COMP_EXCEED_TIME	s	0
21106 float	_EVENT_COMP_UPPER_LIMIT[0]		0
21108 float	_EVENT_COMP_UPPER_LIMIT[1]		0
21110 float	_EVENT_COMP_UPPER_LIMIT[2]		0
21112 float	_EVENT_COMP_UPPER_LIMIT[3]		0
21114 float	_EVENT_COMP_UPPER_LIMIT[4]		0
21116 float	_EVENT_COMP_UPPER_LIMIT[5]		0
21118 float	_EVENT_COMP_UPPER_LIMIT[6]		0
21120 float	_EVENT_COMP_UPPER_LIMIT[7]		0
21122 float	_EVENT_COMP_UPPER_LIMIT[8]		0
21124 float	_EVENT_COMP_LOWER_LIMIT[0]		0
21126 float	_EVENT_COMP_LOWER_LIMIT[1]		0
21128 float	_EVENT_COMP_LOWER_LIMIT[2]		0
21130 uint	_EVENT_COMP_OUTPUT		0
21132 short	_DIG_IO_TYPE[0]		0
21133 short	_DIG_IO_TYPE[1]		0
21134 short	_DIG_IO_TYPE[2]		0
21135 short	_DIG_IO_TYPE[3]		0
21136 short	_DIG_IO_TYPE[4]		0
21137 short	_DIG_IO_INVERT[0]		0
21138 short	_DIG_IO_INVERT[1]		0
21139 short	_DIG_IO_INVERT[2]		0
21140 short	_DIG_IO_INVERT[3]		0
21141 short	_DIG_IO_INVERT[4]		0
21142 short	_DIG_OUT[0]		0
21143 short	_DIG_OUT[1]		0
21144 short	_DIG_OUT[2]		0
21145 short	_DIG_OUT[3]		0
21146 short	_DIG_OUT[4]		0
21147 byte	_TARIFF_WH_SRC[0]		0
21148 byte	_TARIFF_WH_SRC[1]		0
21149 byte	_TARIFF_WH_SRC[2]		0
21150 byte	_TARIFF_WH_SRC[3]		0
21151 byte	_TARIFF_WH_SRC[4]		0

21152 byte	_TARIFF_WH_SRC[5]	0
21153 byte	_TARIFF_WH_SRC[6]	0
21154 byte	_TARIFF_WH_V_SRC[0]	0
21155 byte	_TARIFF_WH_V_SRC[1]	0
21156 byte	_TARIFF_WH_V_SRC[2]	0
21157 byte	_TARIFF_WH_V_SRC[3]	0
21158 byte	_TARIFF_WH_V_SRC[4]	0
21159 byte	_TARIFF_WH_V_SRC[5]	0
21160 byte	_TARIFF_WH_V_SRC[6]	0
21161 byte	_TARIFF_WH_Z_SRC[0]	0
21162 byte	_TARIFF_WH_Z_SRC[1]	0
21163 byte	_TARIFF_WH_Z_SRC[2]	0
21164 byte	_TARIFF_WH_Z_SRC[3]	0
21165 byte	_TARIFF_WH_Z_SRC[4]	0
21166 byte	_TARIFF_WH_Z_SRC[5]	0
21167 byte	_TARIFF_WH_Z_SRC[6]	0
21168 byte	_TARIFF_QH_SRC[0]	0
21169 byte	_TARIFF_QH_SRC[1]	0
21170 byte	_TARIFF_QH_SRC[2]	0
21171 byte	_TARIFF_QH_SRC[3]	0
21172 byte	_TARIFF_QH_SRC[4]	0
21173 byte	_TARIFF_QH_SRC[5]	0
21174 byte	_TARIFF_QH_SRC[6]	0
21175 byte	_TARIFF_IQH_SRC[0]	0
21176 byte	_TARIFF_IQH_SRC[1]	0
21177 byte	_TARIFF_IQH_SRC[2]	0
21178 byte	_TARIFF_IQH_SRC[3]	0
21179 byte	_TARIFF_IQH_SRC[4]	0
21180 byte	_TARIFF_IQH_SRC[5]	0
21181 byte	_TARIFF_IQH_SRC[6]	0
21182 byte	_TARIFF_CQH_SRC[0]	0
21183 byte	_TARIFF_CQH_SRC[1]	0
21184 byte	_TARIFF_CQH_SRC[2]	0
21185 byte	_TARIFF_CQH_SRC[3]	0
21186 byte	_TARIFF_CQH_SRC[4]	0
21187 byte	_TARIFF_CQH_SRC[5]	0
21188 byte	_TARIFF_CQH_SRC[6]	0
21189 byte	_TARIFF_WH_S_SRC[0]	0
21190 byte	_TARIFF_WH_S_SRC[1]	0
21191 byte	_TARIFF_WH_S_SRC[2]	0
21192 byte	_TARIFF_WH_S_SRC[3]	0
21193 byte	_TARIFF_WH_S_SRC[4]	0
21194 byte	_TARIFF_WH_S_SRC[5]	0
21195 byte	_TARIFF_WH_S_SRC[6]	0
21196 int	_REC_DEL	0
21198 custom	_REC_SESSION	0
21200 int	_REC_EVENT_DEL	0
21202 custom	_REC_EVENT_SESSION	0
21204 short	_RTC_STATUS	0
21205 short	_RELEASE	0
21206 short	_DAY	0
21207 short	_MONTH	0
21208 short	_YEAR	0

21209 short	_HOUR	h	0
21210 short	_MIN	min	0
21211 short	_SEC	s	0
21212 short	_WEEKDAY		0
21213 double	_DATA_STIME	s	0
21217 double	_DATA_ETIME	s	0
21221 float	_CTPRIM[0]		0
21223 float	_CTPRIM[1]		0
21225 float	_CTPRIM[2]		0
21227 float	_VTPRIM[0]		0
21229 float	_VTPRIM[1]		0
21231 float	_VTPRIM[2]		0
21233 float	_CTSEC[0]		0
21235 float	_CTSEC[1]		0
21237 float	_CTSEC[2]		0
21239 float	_VTSEC[0]		0
21241 float	_VTSEC[1]		0
21243 float	_VTSEC[2]		0
21245 short	_DIFF_MODE[0]		0
21246 short	_DIFF_MODE[1]		0
21249 short	_TEMP_CONF[0]		0
21250 short	_TEMP_CONF[1]		0
21251 short	_KEY1		0
21252 short	_KEY2		0
21253 long64	_REALTIME	2ns	0
21257 short	_BOOT_RELEASE		0
21258 float	_TEMP_OFFSET[0]		0
21260 float	_TEMP_OFFSET[1]		0
21262 ushort	_BASE_RELEASE		0
21263 ushort	_DIFF_TRANS_THRESHOLD		0
21264 short	_DIFF_TRANS_MON_ENABLE[0]		0
21265 short	_DIFF_TRANS_MON_ENABLE[1]		0
21266 short	_DIFF_ALARM[0]		0
21267 short	_DIFF_ALARM[1]		0
25400 short	_CALIB_STATE		0
25401 short	_CALIB_REMAINING		0
25402 short	_CALIB_TYPE		0
25403 uint	_CALIB_DURATION	ms	0
25405 dfloat	_CALIB_CURRENT	A	0
25407 dfloat	_CALIB_VOLTAGE	V	0
25409 dfloat	_CALIB_FREQUENCY	Hz	0
25411 dfloat	_CALIB_PHASE	Â°	0
25413 custom	_CALIB_RESISTANCE	Ohm	0
25415 ushort	_CALIB_ERROR_CODE		0
25416 custom	_CALIB_RESULT		0
25418 short	_RESET		0
25419 short	_BOOTLOADER		0
25420 short	_TEST_STATE		0
25421 custom	_TEST_RESULT		0
25423 short	_DEBUG		0
25450 uint	_UPDATE_OFFSET		0
25452 uint	_UPDATE_LENGTH		0
25454 short	_CONFIG_FLUSH		0

25455 short	_DISABLE_UPDATEMODE		0
25500 string	_DEV_NAME		64
25532 string	_DEV_DESC		128
25596 time	_SYSTIMEUP		0
25598 float	_SNMP_USERVAR[0]		0
25600 float	_SNMP_USERVAR[1]		0
25602 float	_SNMP_USERVAR[2]		0
25604 float	_SNMP_USERVAR[3]		0
25606 float	_SNMP_USERVAR[4]		0
25608 float	_SNMP_USERVAR[5]		0
25610 float	_SNMP_USERVAR[6]		0
25612 float	_SNMP_USERVAR[7]		0
25614 float	_SNMP_USERVAR[8]		0
25616 float	_SNMP_USERVAR[9]		0
25618 float	_SNMP_USERVAR[10]		0
25620 float	_SNMP_USERVAR[11]		0
25622 float	_SNMP_USERVAR[12]		0
25624 float	_SNMP_USERVAR[13]		0
25626 float	_SNMP_USERVAR[14]		0
25628 float	_SNMP_USERVAR[15]		0
25630 string	_HOSTNAME		32
25646 string	_IP_ADDR		32
25662 string	_IP_MASK		32
25678 string	_GATEWAY		32
25694 string	_SERVERIP		32
25710 string	_ETHADDR		18
25719 int	_DHCPMODE	0/1	0
25721 string	_DNSSERVER		16
25729 byte	_NTP_BROADCAST		0
25730 string	_NTP_SERVER1		32
25746 string	_NTP_SERVER2		32
25762 string	_NTP_SERVER3		32
25778 string	_NTP_SERVER4		32
25794 string	_NTP_SERVER5		32
25810 string	_NTP_SERVER6		32
25826 string	_NTP_SERVER7		32
25842 string	_NTP_SERVER8		32
25858 uint	_MODBUS_MASTER_TIMEOUT		0
25860 uint	_SNMP_TRAP_SERVER		0
25862 uint	_BACNET_INSTANCE		0
25864 int	_BACNET_SENDIAM_TIME	s	0
25866 string	_LANGUAGE		16
25874 uint	_SERNR		0
25876 string	_MAILSERVER		128
25940 ushort	_MAILPORT		0
25941 int	_MAIL_AUTH		0
25943 string	_MAILUSER		128
26007 string	_MAILPASS		128
26071 string	_MAILFROM		48
26095 string	_MAILTO		256
26223 string	_MAIL_STATUS		256
26351 short	_MAIL_ACTIVE[0]		0
26352 short	_MAIL_ACTIVE[1]		0

26353 short	_MAIL_ACTIVE[2]	0
26354 short	_MAIL_ACTIVE[3]	0
26355 short	_MAIL_ACTIVE[4]	0
26356 short	_MAIL_ACTIVE[5]	0
26357 short	_MAIL_ACTIVE[6]	0
26358 short	_MAIL_ACTIVE[7]	0
26359 string	_MAIL_SUBJECT_COMP1	128
26423 string	_MAIL_SUBJECT_COMP2	128
26487 string	_MAIL_SUBJECT_COMP3	128
26551 string	_MAIL_SUBJECT_COMP4	128
26615 string	_MAIL_SUBJECT_COMP5	128
26679 string	_MAIL_SUBJECT_UNDERVOLT	128
26743 string	_MAIL_SUBJECT_OVERVOLT	128
26807 string	_MAIL_SUBJECT_OVERCURR	128
26871 string	_MAIL_TEXT_COMP1	400
27071 string	_MAIL_TEXT_COMP2	400
27271 string	_MAIL_TEXT_COMP3	400
27471 string	_MAIL_TEXT_COMP4	400
27671 string	_MAIL_TEXT_COMP5	400
27871 string	_MAIL_TEXT_UNDERVOLT	400
28071 string	_MAIL_TEXT_OVERVOLT	400
28271 string	_MAIL_TEXT_OVERCURR	400
28471 uint	_BACNET_BBMD_IP	0
28473 ushort	_BACNET_BBMD_PORT	0
28474 long64	_ETHADDR_LONG	0
28478 uint	_BACNET_PASSWORD	0
28480 string	_BACNET_LOCATION	64
28512 custom	_BACNET_CONFIG0	0
28513 custom	_BACNET_CONFIG1	0
28514 custom	_BACNET_CONFIG2	0
28515 custom	_BACNET_CONFIG3	0
28516 custom	_BACNET_CONFIG4	0
28517 custom	_BACNET_CONFIG5	0
28518 custom	_BACNET_CONFIG6	0
28519 custom	_BACNET_CONFIG7	0
28520 custom	_BACNET_CONFIG8	0
28521 custom	_BACNET_CONFIG9	0
28522 custom	_BACNET_CONFIG10	0
28523 custom	_BACNET_CONFIG11	0
28524 custom	_BACNET_CONFIG12	0
28525 custom	_BACNET_CONFIG13	0
28526 custom	_BACNET_CONFIG14	0
28527 custom	_BACNET_CONFIG15	0
28528 custom	_BACNET_CONFIG16	0
28529 custom	_BACNET_CONFIG17	0
28530 custom	_BACNET_CONFIG18	0
28531 custom	_BACNET_CONFIG19	0
28532 custom	_BACNET_CONFIG20	0
28533 custom	_BACNET_CONFIG21	0
28534 custom	_BACNET_CONFIG22	0
28535 custom	_BACNET_CONFIG23	0
28536 custom	_BACNET_CONFIG24	0
28537 custom	_BACNET_CONFIG25	0

28538 custom	_BACNET_CONFIG26	0
28539 custom	_BACNET_CONFIG27	0
28540 custom	_BACNET_CONFIG28	0
28541 custom	_BACNET_CONFIG29	0
28542 custom	_BACNET_CONFIG30	0
28543 custom	_BACNET_CONFIG31	0
28544 custom	_BACNET_CONFIG32	0
28545 custom	_BACNET_CONFIG33	0
28546 custom	_BACNET_CONFIG34	0
28547 custom	_BACNET_CONFIG35	0
28548 custom	_BACNET_CONFIG36	0
28549 custom	_BACNET_CONFIG37	0
28550 custom	_BACNET_CONFIG38	0
28551 custom	_BACNET_CONFIG39	0
28552 custom	_BACNET_CONFIG40	0
28553 custom	_BACNET_CONFIG41	0
28554 custom	_BACNET_CONFIG42	0
28555 custom	_BACNET_CONFIG43	0
28556 custom	_BACNET_CONFIG44	0
28557 custom	_BACNET_CONFIG45	0
28558 custom	_BACNET_CONFIG46	0
28559 custom	_BACNET_CONFIG47	0
28560 custom	_BACNET_CONFIG48	0
28561 custom	_BACNET_CONFIG49	0
28562 custom	_BACNET_CONFIG50	0
28563 custom	_BACNET_CONFIG51	0
28564 custom	_BACNET_CONFIG52	0
28565 custom	_BACNET_CONFIG53	0
28566 custom	_BACNET_CONFIG54	0
28567 custom	_BACNET_CONFIG55	0
28568 custom	_BACNET_CONFIG56	0
28569 custom	_BACNET_CONFIG57	0
28570 custom	_BACNET_CONFIG58	0
28571 custom	_BACNET_CONFIG59	0
28572 custom	_BACNET_CONFIG60	0
28573 custom	_BACNET_CONFIG61	0
28574 custom	_BACNET_CONFIG62	0
28575 custom	_BACNET_CONFIG63	0
28576 custom	_BACNET_CONFIG64	0
28577 custom	_BACNET_CONFIG65	0
28578 custom	_BACNET_CONFIG66	0
28579 custom	_BACNET_CONFIG67	0
28580 custom	_BACNET_CONFIG68	0
28581 custom	_BACNET_CONFIG69	0
28582 custom	_BACNET_CONFIG70	0
28583 custom	_BACNET_CONFIG71	0
28584 custom	_BACNET_CONFIG72	0
28585 custom	_BACNET_CONFIG73	0
28586 custom	_BACNET_CONFIG74	0
28587 custom	_BACNET_CONFIG75	0
28588 custom	_BACNET_CONFIG76	0
28589 custom	_BACNET_CONFIG77	0
28590 custom	_BACNET_CONFIG78	0

28591 custom	_BACNET_CONFIG79		0
28592 string	_SNMP_OID		32
28608 custom	_SNMP_TRAPCFG0		0
28609 custom	_SNMP_TRAPCFG1		0
28610 custom	_SNMP_TRAPCFG2		0
28611 custom	_SNMP_TRAPCFG3		0
28612 custom	_SNMP_TRAPCFG4		0
28613 custom	_SNMP_TRAPCFG5		0
28614 custom	_SNMP_TRAPCFG6		0
28615 custom	_SNMP_TRAPCFG7		0
28616 custom	_SNMP_TRAPCFG8		0
28617 custom	_SNMP_TRAPCFG9		0
28618 custom	_SNMP_TRAPCFG10		0
28619 custom	_SNMP_TRAPCFG11		0
28620 custom	_SNMP_TRAPCFG12		0
28621 custom	_SNMP_TRAPCFG13		0
28622 custom	_SNMP_TRAPCFG14		0
28623 custom	_SNMP_TRAPCFG15		0
28624 uint	_SNMP_TRAP_LEADTIME	ms	0
28626 ushort	_SNMP_TRAP_HYSTERESIS	0.1%	0
29000 custom	_UPDATE		0

Measured frequency
Voltage, zero sequence
Voltage, negative sequence
Voltage, positive sequence
Voltage L-N
Voltage L-N
Voltage L-N
Voltage L-L
Voltage L-L
Voltage L-L
Fund. power factor, CosPhi; ULN IL
Fund. power factor, CosPhi; ULN IL
Fund. power factor, CosPhi; ULN IL
Fund. power factor, CosPhi; ULN IL
Power factor; ULN IL
Power factor; ULN IL
Power factor; ULN IL
Power factor; ULN IL
THD, U LN
THD, U LN
THD, U LN
THD, U LL
THD, U LL
THD, U LL
Voltage, real part U LN
Voltage, real part U LN
Voltage, real part U LN
Voltage, imaginary part U LN
Voltage, imaginary part U LN
Voltage, imaginary part U LN
Current I L
Current I L
Current I L
Current I L
Real power P LN
Real power P LN
Real power P LN
Real power P LN
Fund. reactive power Q LN
Fund. reactive power Q LN
Fund. reactive power Q LN
Fund. reactive power Q LN
Apparent power S LN
Apparent power S LN
Apparent power S LN
Apparent power S LN
Fund. real power P0 LN
Fund. real power P0 LN
Fund. real power P0 LN
Fund. real power P0 LN
Harmonic distortion power D LN
Harmonic distortion power D LN
Harmonic distortion power D LN

Harmonic distortion power D LN
THD I
THD I
THD I
TDD I
TDD I
TDD I
Current, zero sequence
Current, negative sequence
Current, positive sequence
Current, real part I L
Current, real part I L
Current, real part I L
Current, imaginary part I L
Current, imaginary part I L
Current, imaginary part I L
Rotation field; 1=right, 0=none, -1=left
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Average, Measured frequency
Average, Voltage, zero sequence
Average, Voltage, negative sequence
Average, Voltage, positive sequence
Average, Voltage L-N
Average, Voltage L-N
Average, Voltage L-N
Average, Voltage L-L
Average, Voltage L-L
Average, Voltage L-L
Average, Harmonic U L1
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Average, Fund. power factor, CosPhi; ULN IL
Average, Fund. power factor, CosPhi; ULN IL
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Average, THD, U LN

Average, THD, U LN
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Average, Voltage, real part U LN
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Average, Voltage, imaginary part U LN
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Average, Real power P LN
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Average, Fund. reactive power Q LN
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Average, Apparent power S LN
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Average, Fund. real power P0 LN
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Average, Current, zero sequence
Average, Current, negative sequence
Average, Current, positive sequence
Average, Current, real part I L
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Average, Current, real part I L
Average, Current, imaginary part I L
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Maximum, Measured frequency
Maximum, Voltage, zero sequence
Maximum, Voltage, negative sequence
Maximum, Voltage, positive sequence
Maximum, Voltage L-N
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Maximum, Fund. power factor, CosPhi; ULN IL
Maximum, Fund. power factor, CosPhi; ULN IL
Maximum, Fund. power factor, CosPhi; ULN IL
Maximum, Fund. power factor, CosPhi; ULN IL

Maximum, Power factor; ULN IL
Maximum, Power factor; ULN IL
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Maximum, Power factor; ULN IL
Maximum, THD, U LN
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Maximum, THD, U LL
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Maximum, Voltage, real part U LN
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Maximum, Voltage, imaginary part U LN
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Maximum, Current I L
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Maximum, Real power P LN
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Maximum, Fund. reactive power Q LN
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Maximum, Apparent power S LN
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Maximum, Fund. real power P0 LN
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Maximum, Harmonic distortion power D LN
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Maximum, Harmonic distortion power D LN
Maximum, THD I
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Maximum, TDD I
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Maximum, Current, zero sequence
Maximum, Current, negative sequence
Maximum, Current, positive sequence
Maximum, Current, real part I L
Maximum, Current, real part I L
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Maximum, Current, imaginary part I L
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Time of Minimum, Measured frequency
Time of Minimum, Voltage, zero sequence
Time of Minimum, Voltage, negative sequence
Time of Minimum, Voltage, positive sequence
Time of Minimum, Voltage L-N
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Time of Minimum, Voltage L-N
Time of Minimum, Voltage L-L
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Time of Minimum, Voltage L-L
Time of Minimum, Fund. power factor, CosPhi; ULN IL
Time of Minimum, Fund. power factor, CosPhi; ULN IL
Time of Minimum, Fund. power factor, CosPhi; ULN IL
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Time of Minimum, THD, U LN
Time of Minimum, THD, U LN
Time of Minimum, THD, U LN
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Time of Minimum, THD, U LL
Time of Minimum, Voltage, real part U LN
Time of Minimum, Voltage, real part U LN
Time of Minimum, Voltage, real part U LN
Time of Minimum, Voltage, imaginary part U LN
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Time of Minimum, Voltage, imaginary part U LN
Maximum average, current I L
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Maximum average, current I L
Maximum average, current I L
Maximum average, Real Power P LN
Maximum average, Real Power P LN
Maximum average, Real Power P LN

Maximum average, Real Power P LN
System uptime
Measured frequency
Voltage, zero sequence
Voltage, negative sequence
Voltage, positive sequence
Voltage L-N
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Voltage L-L
Harmonic U L1
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Fund. power factor, CosPhi; ULN IL
Fund. power factor, CosPhi; ULN IL
Fund. power factor, CosPhi; ULN IL
Fund. power factor, CosPhi; ULN IL
Power factor; ULN IL
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Power factor; ULN IL
Power factor; ULN IL
THD, U LN
THD, U LN
THD, U LN
THD, U LL
THD, U LL

THD, U LL
Voltage, real part U LN
Voltage, real part U LN
Voltage, real part U LN
Voltage, imaginary part U LN
Voltage, imaginary part U LN
Voltage, imaginary part U LN
Harmonic A L1
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Current I L
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Current I L
Real power P LN
Real power P LN
Real power P LN
Real power P LN
Fund. reactive power Q LN
Fund. reactive power Q LN
Fund. reactive power Q LN
Fund. reactive power Q LN
Apparent power S LN
Apparent power S LN
Apparent power S LN
Apparent power S LN
Fund. real power P0 LN
Fund. real power P0 LN
Fund. real power P0 LN
Fund. real power P0 LN
Harmonic distortion power D LN
Harmonic distortion power D LN
Harmonic distortion power D LN
Harmonic distortion power D LN
THD I
THD I
THD I
TDD I
TDD I
TDD I
Current, zero sequence
Current, negative sequence

Current, positive sequence
Current, real part I L
Current, real part I L
Current, real part I L
Current, imaginary part I L
Current, imaginary part I L
Current, imaginary part I L
Rotation field; 1=right, 0=none, -1=left
Average, Measured frequency
Average, Voltage, zero sequence
Average, Voltage, negative sequence
Average, Voltage, positive sequence
Average, Voltage L-N
Average, Voltage L-N
Average, Voltage L-N
Average, Voltage L-L
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Average, Harmonic U L1
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Average, Fund. power factor, CosPhi; ULN IL
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Average, Power factor; ULN IL
Average, THD, U LN
Average, THD, U LN
Average, THD, U LN
Average, THD, U LL
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Average, THD, U LL
Average, Voltage, real part U LN
Average, Voltage, real part U LN
Average, Voltage, real part U LN
Average, Voltage, imaginary part U LN
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Average, Current I L
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Average, Real power P LN
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Average, Fund. reactive power Q LN
Average, Fund. reactive power Q LN
Average, Fund. reactive power Q LN
Average, Fund. reactive power Q LN
Average, Apparent power S LN
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Average, Apparent power S LN
Average, Apparent power S LN
Average, Fund. real power P0 LN
Average, Fund. real power P0 LN
Average, Fund. real power P0 LN
Average, Fund. real power P0 LN
Average, Harmonic distortion power D LN
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Average, Harmonic distortion power D LN
Average, THD I
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Average, THD I
Average, TDD I
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Average, Current, zero sequence
Average, Current, negative sequence
Average, Current, positive sequence
Average, Current, real part I L
Average, Current, real part I L
Average, Current, real part I L
Average, Current, imaginary part I L
Average, Current, imaginary part I L
Average, Current, imaginary part I L
Maximum, Measured frequency
Maximum, Voltage, zero sequence
Maximum, Voltage, negative sequence
Maximum, Voltage, positive sequence
Maximum, Voltage L-N
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Maximum, Harmonic U L1
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Maximum, Fund. power factor, CosPhi; ULN IL
Maximum, Fund. power factor, CosPhi; ULN IL

Maximum, Fund. power factor, CosPhi; ULN IL
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Maximum, THD, U LN
Maximum, THD, U LN
Maximum, THD, U LN
Maximum, THD, U LL
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Maximum, Voltage, real part U LN
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Maximum, Voltage, imaginary part U LN
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Maximum, Current I L
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Maximum, Real power P LN
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Maximum, Fund. reactive power Q LN
Maximum, Fund. reactive power Q LN
Maximum, Fund. reactive power Q LN
Maximum, Fund. reactive power Q LN
Maximum, Apparent power S LN
Maximum, Apparent power S LN
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Maximum, Apparent power S LN
Maximum, Fund. real power P0 LN
Maximum, Fund. real power P0 LN
Maximum, Fund. real power P0 LN
Maximum, Fund. real power P0 LN
Maximum, Harmonic distortion power D LN

Maximum, Harmonic distortion power D LN
Maximum, Harmonic distortion power D LN
Maximum, Harmonic distortion power D LN
Maximum, THD I
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Maximum, TDD I
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Maximum, Current, zero sequence
Maximum, Current, negative sequence
Maximum, Current, positive sequence
Maximum, Current, real part I L
Maximum, Current, real part I L
Maximum, Current, real part I L
Maximum, Current, imaginary part I L
Maximum, Current, imaginary part I L
Maximum, Current, imaginary part I L
Minimum, Measured frequency
Minimum, Voltage, zero sequence
Minimum, Voltage, negative sequence
Minimum, Voltage, positive sequence
Minimum, Voltage L-N
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Minimum, Fund. power factor, CosPhi; ULN IL
Minimum, Fund. power factor, CosPhi; ULN IL
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Minimum, THD, U LN
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Minimum, Voltage, real part U LN
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Maximum average, current I L
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Maximum average, current I L
Maximum average, Real Power P LN

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digital outputs state
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digital outputs state
Current I L4
THD I L4
TDD I L4
Power s0
Power s0
Power s0
Power s0, average
Power s0, average
Power s0, average
Power s0, maximum
Power s0, maximum
Power s0, maximum
digital inputs
digital inputs
digital inputs
overcurrent flag i4
digital inputs, bit coded
Current I L4, average
THD I L4, average
TDD I L4, average
Current I L4, maximum
THD I L4, maximum
TDD I L4, maximum
Current I L4, maximum average
Comp timers channel 1-5
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input pulse count
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Time of Maximum, Current I L4
Time of Maximum, THD I L4
Time of Maximum, TDD I L4
Time of Maximum average, current I L4
Time of Maximum power s0
Time of Maximum power s0
Time of Maximum power s0
Time of Maximum, Measured frequency
Time of Maximum, Voltage, zero sequence
Time of Maximum, Voltage, negative sequence
Time of Maximum, Voltage, positive sequence
Time of Maximum, Voltage L-N

Time of Maximum, Voltage L-N
Time of Maximum, Voltage L-N
Time of Maximum, Voltage L-L
Time of Maximum, Voltage L-L
Time of Maximum, Voltage L-L
Time of Maximum, Fund. power factor, CosPhi; ULN IL
Time of Maximum, Fund. power factor, CosPhi; ULN IL
Time of Maximum, Fund. power factor, CosPhi; ULN IL
Time of Maximum, Fund. power factor, CosPhi; ULN IL
Time of Maximum, Power factor; ULN IL
Time of Maximum, Power factor; ULN IL
Time of Maximum, Power factor; ULN IL
Time of Maximum, Power factor; ULN IL
Time of Maximum, THD, U LN
Time of Maximum, THD, U LN
Time of Maximum, THD, U LN
Time of Maximum, THD, U LL
Time of Maximum, THD, U LL
Time of Maximum, THD, U LL
Time of Maximum, Voltage, real part U LN
Time of Maximum, Voltage, real part U LN
Time of Maximum, Voltage, real part U LN
Time of Maximum, Voltage, imaginary part U LN
Time of Maximum, Voltage, imaginary part U LN
Time of Maximum, Voltage, imaginary part U LN
Time of Maximum, Current I L
Time of Maximum, Current I L
Time of Maximum, Current I L
Time of Maximum, Current I L
Time of Maximum, Real power P LN
Time of Maximum, Real power P LN
Time of Maximum, Real power P LN
Time of Maximum, Real power P LN
Time of Maximum, Fund. reactive power Q LN
Time of Maximum, Fund. reactive power Q LN
Time of Maximum, Fund. reactive power Q LN
Time of Maximum, Fund. reactive power Q LN
Time of Maximum, Apparent power S LN
Time of Maximum, Apparent power S LN
Time of Maximum, Apparent power S LN
Time of Maximum, Apparent power S LN
Time of Maximum, Fund. real power P0 LN
Time of Maximum, Fund. real power P0 LN
Time of Maximum, Fund. real power P0 LN
Time of Maximum, Fund. real power P0 LN
Time of Maximum, Harmonic distortion power D LN
Time of Maximum, Harmonic distortion power D LN
Time of Maximum, Harmonic distortion power D LN
Time of Maximum, Harmonic distortion power D LN
Time of Maximum, THD I
Time of Maximum, THD I
Time of Maximum, THD I
Time of Maximum, TDD I

Time of Maximum, TDD I
Time of Maximum, TDD I
Time of Maximum, Current, zero sequence
Time of Maximum, Current, negative sequence
Time of Maximum, Current, positive sequence
Time of Maximum, Current, real part I L
Time of Maximum, Current, real part I L
Time of Maximum, Current, real part I L
Time of Maximum, Current, imaginary part I L
Time of Maximum, Current, imaginary part I L
Time of Maximum, Current, imaginary part I L
Time of Maximum average, current I L
Time of Maximum average, current I L
Time of Maximum average, current I L
Time of Maximum average, current I L
Time of Maximum average, Real Power P LN
Time of Maximum average, Real Power P LN
Time of Maximum average, Real Power P LN
Time of Maximum average, Real Power P LN
Time of Minimum, Measured frequency
Time of Minimum, Voltage, zero sequence
Time of Minimum, Voltage, negative sequence
Time of Minimum, Voltage, positive sequence
Time of Minimum, Voltage L-N
Time of Minimum, Voltage L-N
Time of Minimum, Voltage L-N
Time of Minimum, Voltage L-L
Time of Minimum, Voltage L-L
Time of Minimum, Voltage L-L
Time of Minimum, Fund. power factor, CosPhi; ULN IL
Time of Minimum, Fund. power factor, CosPhi; ULN IL
Time of Minimum, Fund. power factor, CosPhi; ULN IL
Time of Minimum, Fund. power factor, CosPhi; ULN IL
Time of Minimum, Power factor; ULN IL
Time of Minimum, Power factor; ULN IL
Time of Minimum, Power factor; ULN IL
Time of Minimum, Power factor; ULN IL
Time of Minimum, THD, U LN
Time of Minimum, THD, U LN
Time of Minimum, THD, U LN
Time of Minimum, THD, U LL
Time of Minimum, THD, U LL
Time of Minimum, THD, U LL
Time of Minimum, Voltage, real part U LN
Time of Minimum, Voltage, real part U LN
Time of Minimum, Voltage, real part U LN
Time of Minimum, Voltage, imaginary part U LN
Time of Minimum, Voltage, imaginary part U LN
Time of Minimum, Voltage, imaginary part U LN
Maximum, Harmonic A L4
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Current I L4, integer
THD I L4, integer
TDD I L4, integer
Power s0, integer
Power s0, integer
Power s0, integer
Harmonic A L4, integer
Harmonic A L4, integer
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Harmonic A L4, integer
Harmonic A L4, integer
Harmonic A L4, integer
Current I L4, average, integer
THD I L4, average, integer
TDD I L4, average, integer
Power s0, average, integer
Power s0, average, integer
Power s0, average, integer
Average Harmonic A L4, integer
Average Harmonic A L4, integer
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Average Harmonic A L4, integer
Current I L4, maximum, integer
THD I L4, maximum, integer
TDD I L4, maximum, integer
Power s0, maximum, integer
Power s0, maximum, integer
Power s0, maximum, integer
Maximum Harmonic A L4, integer
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Maximum Harmonic A L4, integer
Maximum Harmonic A L4, integer
Maximum average, current I L, integer
Temperature input 1
Temperature input 2
Diff1 4-20mA
Diff2 4-20mA
Current Diff1
Current Diff2
THD I Diff1
THD I Diff2
Harmonic A Diff1
Harmonic A Diff1
Harmonic A Diff1
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Harmonic A Diff1
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Temperature input 1, average
Temperature input 2, average
Diff1 4-20mA, average
Diff2 4-20mA, average
Current Diff1, average
Current Diff2, average
THD I Diff1, average
THD I Diff2, average
Harmonic A Diff1, average
Harmonic A Diff1, average
Harmonic A Diff1, average
Harmonic A Diff1, average
Harmonic A Diff1, average
Harmonic A Diff1, average
Harmonic A Diff1, average

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Harmonic A Diff2, average
Harmonic A Diff2, average
Temperature input 1, maximum
Temperature input 2, maximum
Diff1 4-20mA, maximum
Diff2 4-20mA, maximum
Current Diff1, maximum
Current Diff2, maximum
THD I Diff1, maximum
THD I Diff2, maximum
Temperature input 1, maximum average
Temperature input 2, maximum average
Diff1 4-20mA, maximum average
Diff2 4-20mA, maximum average
Current Diff1, maximum average
Current Diff2, maximum average
Time of Maximum, Temperature input 1
Time of Maximum, Temperature input 2
Time of Maximum, Diff1 4-20mA
Time of Maximum, Diff2 4-20mA
Time of Maximum, Current Diff1
Time of Maximum, Current Diff2
Time of Maximum, THD I Diff1
Time of Maximum, THD I Diff2
Time of Maximum average, Temperature input 1
Time of Maximum average, Temperature input 2
Time of Maximum average, Diff1 4-20mA
Time of Maximum average, Diff2 4-20mA
Time of Maximum average,Current Diff1
Time of Maximum average,Current Diff2
Temperature input 1, integer
Temperature input 2, integer
Diff1 4-20mA, integer
Diff1 4-20mA, integer
Current Diff1, integer

Current Diff2, integer
THD I Diff1, integer
THD I Diff2, integer
Harmonic A Diff1, integer
Harmonic A Diff1, integer
Harmonic A Diff1, integer
Harmonic A Diff1, integer
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Harmonic A Diff2, integer
Temperature input 1, average, integer
Temperature input 2, average, integer
Diff1 4-20mA, average, integer
Diff2 4-20mA, average, integer
Current Diff1, average, integer
Current Diff2, average, integer
THD I Diff1, average, integer
THD I Diff2, average, integer
Harmonic A Diff1, average, integer
Harmonic A Diff1, average, integer
Harmonic A Diff1, average, integer
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Harmonic A Diff2, average, integer
Harmonic A Diff2, average, integer
Harmonic A Diff2, average, integer
Harmonic A Diff2, average, integer
Harmonic A Diff2, average, integer
Temperature input 1, maximum, integer
Temperature input 2, maximum, integer
Diff1 4-20mA, maximum, integer
Diff2 4-20mA, maximum, integer
Current Diff1, maximum, integer
Current Diff2, maximum, integer
THD I Diff1, maximum, integer
THD I Diff2, maximum, integer
Temperature input 1, maximum average, integer
Temperature input 2, maximum average, integer
Diff1 4-20mA, maximum average, integer
Diff2 4-20mA, maximum average, integer
Current Diff1, maximum average, integer
Current Diff2, maximum average, integer
Arithmetic Sum Current (I1+I2+I3)
Arithmetic Sum Current (I1+I2+I3), average
Arithmetic Sum Current (I1+I2+I3), maximum
Arithmetic Sum Current (I1+I2+I3), maximum average
Time of Arithmetic Sum Current (I1+I2+I3), maximum average
Time of Arithmetic Sum Current (I1+I2+I3), maximum average

Resistance temp input 1
Resistance temp input 2

diff current transformer connection broken (AC only)
diff current transformer connection broken (AC only)
diff current exceeds warnlevel
diff current exceeds warnlevel
diff current exceeds limit
diff current exceeds limit
internal (needed by parser)
Voltage L-N
Voltage L-N
Voltage L-N
Voltage L-L
Voltage L-L
Voltage L-L
Current I L
Current I L
Current I L
Current I L
Real power P LN
Real power P LN
Real power P LN
Real power P LN
Apparent power S LN
Apparent power S LN
Apparent power S LN
Apparent power S LN
Fund. reactive power Q LN
Fund. reactive power Q LN
Fund. reactive power Q LN
Fund. reactive power Q LN
Fund. power factor L-N
Fund. power factor L-N
Fund. power factor L-N
Measured frequency
Rotation field; 1=right, 0=none, -1=left
Active energy L1
Active energy L2
Active energy L3
Active energy sum
Active energy L1, obtained
Active energy L2, obtained
Active energy L3, obtained
Active energy sum, obtained Tarif 1
Active energy L1, supplied
Active energy L2, supplied
Active energy L3, supplied
Active energy sum, supplied
Apparent energy L1
Apparent energy L2
Apparent energy L3
Apparent energy sum
Reactive energy L1
Reactive energy L2
Reactive energy L3

Reactive energy sum
Reactive energy L1, inductive
Reactive energy L2, inductive
Reactive energy L3, inductive
Reactive energy sum, inductive
Reactive energy L1, capacitive
Reactive energy L2, capacitive
Reactive energy L3, capacitive
Reactive energy sum, capacitive
THD, U LN
THD, U LN
THD, U LN
THD I
THD I
THD I
calib key
Systemtime in sec (ro)
Systemtime in sec
tdd full load current L4
ct prim I4
ct sec I4
ct prim I5
ct sec I5
ct prim I6
ct sec I6
record 1 configuration
record 2 configuration
record 3 configuration
record 4 configuration
multiplication factor for s0 input frequency
multiplication factor for s0 input frequency
multiplication factor for s0 input frequency
Name of S0 input type IN0
Name of S0 input type IN1
Name of S0 input type IN2
Name of S0 input type IN0
Name of S0 input type IN1
Name of S0 input type IN2
Name of S0 input type IN0
Name of S0 input type IN1
Name of S0 input type IN2
pulse out reference address
pulse out reference address
pulse out reference address
pulse out reference address
pulse out reference address
pulse generation factor (freq=val/fac)
pulse generation factor (freq=val/fac)
pulse generation factor (freq=val/fac)
pulse generation factor (freq=val/fac)
pulse generation factor (freq=val/fac)
min exceed time comparator 0
min set time comparator 0

Concatenation comparators out 0 (0=OR,1=AND)
threshold value comparator 0
threshold value comparator 0
threshold value comparator 0
threshold value comparator 0
threshold value comparator 0
threshold value comparator 0
threshold value comparator 0
threshold value comparator 0
threshold value comparator 0
threshold value comparator 0
modbus addr comparator 0
modbus addr comparator 0
modbus addr comparator 0
modbus addr comparator 0
modbus addr comparator 0
modbus addr comparator 0
modbus addr comparator 0
modbus addr comparator 0
modbus addr comparator 0
modbus addr comparator 0
invert comparator 0
invert comparator 0
invert comparator 0
invert comparator 0
invert comparator 0
invert comparator 0
invert comparator 0
invert comparator 0
invert comparator 0
invert comparator 0
min exceed time comparator 1
min set time comparator 1
Concatenation comparators out 1 (0=OR,1=AND)
threshold value comparator 1
threshold value comparator 1
threshold value comparator 1
threshold value comparator 1
threshold value comparator 1
threshold value comparator 1
threshold value comparator 1
threshold value comparator 1
threshold value comparator 1
threshold value comparator 1
modbus addr comparator 1
modbus addr comparator 1
modbus addr comparator 1
modbus addr comparator 1
modbus addr comparator 1
modbus addr comparator 1
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modbus addr comparator 1

modbus addr comparator 1
invert comparator 1
invert comparator 1
invert comparator 1
invert comparator 1
invert comparator 1
invert comparator 1
invert comparator 1
invert comparator 1
invert comparator 1
invert comparator 1
min exceed time comparator 2
min set time comparator 2
Concatenation comparators out 2 (0=OR,1=AND)
threshold value comparator 2
threshold value comparator 2
threshold value comparator 2
threshold value comparator 2
threshold value comparator 2
threshold value comparator 2
threshold value comparator 2
threshold value comparator 2
threshold value comparator 2
threshold value comparator 2
modbus addr comparator 2
modbus addr comparator 2
modbus addr comparator 2
modbus addr comparator 2
modbus addr comparator 2
modbus addr comparator 2
modbus addr comparator 2
modbus addr comparator 2
modbus addr comparator 2
modbus addr comparator 2
modbus addr comparator 2
invert comparator 2
invert comparator 2
invert comparator 2
invert comparator 2
invert comparator 2
invert comparator 2
invert comparator 2
invert comparator 2
invert comparator 2
invert comparator 2
min exceed time comparator 3
min set time comparator 3
Concatenation comparators out 3 (0=OR,1=AND)
threshold value comparator 3
threshold value comparator 3
threshold value comparator 3
threshold value comparator 3
threshold value comparator 3
threshold value comparator 3

threshold value comparator 3
threshold value comparator 3
threshold value comparator 3
threshold value comparator 3
modbus addr comparator 3
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modbus addr comparator 3
modbus addr comparator 3
modbus addr comparator 3
modbus addr comparator 3
modbus addr comparator 3
invert comparator 3
invert comparator 3
invert comparator 3
invert comparator 3
invert comparator 3
invert comparator 3
invert comparator 3
invert comparator 3
invert comparator 3
invert comparator 3
min exceed time comparator 4
min set time comparator 4
Concatenation comparators out 4 (0=OR,1=AND)
threshold value comparator 4
threshold value comparator 4
threshold value comparator 4
threshold value comparator 4
threshold value comparator 4
threshold value comparator 4
threshold value comparator 4
threshold value comparator 4
threshold value comparator 4
threshold value comparator 4
modbus addr comparator 4
modbus addr comparator 4
modbus addr comparator 4
modbus addr comparator 4
modbus addr comparator 4
modbus addr comparator 4
modbus addr comparator 4
modbus addr comparator 4
modbus addr comparator 4
modbus addr comparator 4
invert comparator 4
invert comparator 4
invert comparator 4
invert comparator 4
invert comparator 4
invert comparator 4

invert comparator 4
invert comparator 4
invert comparator 4
invert comparator 4
comp out 0
comp out 0
comp out 0
comp out 0
comp out 0
comp out 0
comp out 0
comp out 0
comp out 0
comp out 0
comp out 1
comp out 1
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comp out 4
concatenated comparator out 0
concatenated comparator out 1
concatenated comparator out 2
concatenated comparator out 3
concatenated comparator out 4

overcurrent duration diff0
overcurrent duration diff1
Real Threshold Diff 0
Real Threshold Diff 1
minimal event time before signaling
upper limit event comparators (U1-U3,I1-I4,Diff1/2
upper limit event comparators (U1-U3,I1-I4,Diff1/2
upper limit event comparators (U1-U3,I1-I4,Diff1/2
upper limit event comparators (U1-U3,I1-I4,Diff1/2
upper limit event comparators (U1-U3,I1-I4,Diff1/2
upper limit event comparators (U1-U3,I1-I4,Diff1/2
upper limit event comparators (U1-U3,I1-I4,Diff1/2
upper limit event comparators (U1-U3,I1-I4,Diff1/2
upper limit event comparators (U1-U3,I1-I4,Diff1/2
lower limit event comparators (U1-U3)
lower limit event comparators (U1-U3)
lower limit event comparators (U1-U3)
bitwise event comparator output
dig out type
dig out type
dig out type
dig out type
dig out type
dig out invert
dig out invert
dig out invert
dig out invert
dig out invert
modbus dig out
modbus dig out
modbus dig out
modbus dig out
modbus dig out
tariff active energy source
tariff active energy source
tariff active energy source
tariff active energy source
tariff active energy source

tariff active energy source
tariff active energy source
tariff active energy, obtained source
tariff active energy, obtained source
tariff active energy, obtained source
tariff active energy, obtained source
tariff active energy, obtained source
tariff active energy, obtained source
tariff active energy, obtained source
tariff active energy, supplied source
tariff active energy, supplied source
tariff active energy, supplied source
tariff active energy, supplied source
tariff active energy, supplied source
tariff active energy, supplied source
tariff active energy, supplied source
tariff reactive energy source
tariff reactive energy source
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tariff reactive energy source
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tariff reactive energy source
tariff reactive energy source
tariff reactive energy inductive source
tariff reactive energy inductive source
tariff reactive energy inductive source
tariff reactive energy inductive source
tariff reactive energy inductive source
tariff reactive energy inductive source
tariff reactive energy inductive source
tariff reactive energy inductive source
tariff reactive energy capacitive source
tariff reactive energy capacitive source
tariff reactive energy capacitive source
tariff reactive energy capacitive source
tariff reactive energy capacitive source
tariff reactive energy capacitive source
tariff reactive energy capacitive source
tariff apparent energy source
tariff apparent energy source
tariff apparent energy source
tariff apparent energy source
tariff apparent energy source
tariff apparent energy source
tariff apparent energy source
1=delete all recordings
read recordings (func 23)
1=delete all event recordings
read events (func 23)

(for internal use only!) homepage: set/get ct_prim
(for internal use only!) homepage: set/get ct_prim
(for internal use only!) homepage: set/get ct_prim
(for internal use only!) homepage: set/get vt_prim
(for internal use only!) homepage: set/get vt_prim
(for internal use only!) homepage: set/get vt_prim
(for internal use only!) homepage: set/get ct_sec
(for internal use only!) homepage: set/get ct_sec
(for internal use only!) homepage: set/get ct_sec
(for internal use only!) homepage: set/get vt_sec
(for internal use only!) homepage: set/get vt_sec
(for internal use only!) homepage: set/get vt_sec
Select type of differential input measurement
Select type of differential input measurement
Thermoelement configuration
Thermoelement configuration

Realtime (2ns)

Offset for thermoelement measurements
Offset for thermoelement measurements

diff current transformer connected threshold
enable diff current transformer monitoring (AC only)
enable diff current transformer monitoring (AC only)
diff input alarm state
diff input alarm state

Only for internal use
Only for internal use
System Uptime. Only for internal use
SNMP User Variables
SNMP User Variables
SNMP User Variables
SNMP User Variables
SNMP User Variables
SNMP User Variables
SNMP User Variables
SNMP User Variables
SNMP User Variables
SNMP User Variables
SNMP User Variables
SNMP User Variables
SNMP User Variables
SNMP User Variables
SNMP User Variables
SNMP User Variables
UMG Hostname
Device ip address (change restarts system)
Device netmask (change restarts system)
Device gateway ip address (change restarts system)
Update address for boodloader
Update ethernet address
Set device to dhcp network config (change restarts system)
Device DNS server IP
Listen to NTP broadcast (1/0)
NTP Server 1
NTP Server 2
NTP Server 3
NTP Server 4
NTP Server 5
NTP Server 6
NTP Server 7
NTP Server 8
Modbusmaster Timeout
SNMP Trap server ip
BACNet instance
BACNet send i am time

SerialNr
SMTP Mailserver address
SMTP Mailserver port
SMTP Mailserver authorization mode: 0=none, 1=plain, 2=login, 3=cram-md5
SMTP Mailserver user
SMTP Mailserver pass
Mail_from Adress
Mail_to Adress(es)
Mail error string
Enable Mail for event (0-4=Comparator1-5, 5=Undervoltage, 6=Overvoltage, 7=Overcurrent)
Enable Mail for event (0-4=Comparator1-5, 5=Undervoltage, 6=Overvoltage, 7=Overcurrent)

Enable Mail for event (0-4=Comparator1-5, 5=Undervoltage, 6=Overvoltage, 7=Overcurrent)
Enable Mail for event (0-4=Comparator1-5, 5=Undervoltage, 6=Overvoltage, 7=Overcurrent)
Enable Mail for event (0-4=Comparator1-5, 5=Undervoltage, 6=Overvoltage, 7=Overcurrent)
Enable Mail for event (0-4=Comparator1-5, 5=Undervoltage, 6=Overvoltage, 7=Overcurrent)
Enable Mail for event (0-4=Comparator1-5, 5=Undervoltage, 6=Overvoltage, 7=Overcurrent)
Enable Mail for event (0-4=Comparator1-5, 5=Undervoltage, 6=Overvoltage, 7=Overcurrent)
Mail subject for Comparator 1 output
Mail subject for Comparator 2 output
Mail subject for Comparator 3 output
Mail subject for Comparator 4 output
Mail subject for Comparator 5 output
Mail subject for undervoltage event
Mail subject for overvoltage event
Mail subject for overcurrent event
Mail text for Comparator 1 output
Mail text for Comparator 2 output
Mail text for Comparator 3 output
Mail text for Comparator 4 output
Mail text for Comparator 5 output
Mail text for undervoltage event
Mail text for overvoltage event
Mail text for overcurrent event
IP of Bacnet Broadcast Management device (BBMD) for foreign device registration
Port of Bacnet Broadcast Management device (BBMD) for foreign device registration
Update ethernet address (ulong)
Password to unlock BACnet.
BACnet Location
BACnet configuration
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BACnet configuration

Tabelle1

BACnet configuration
SNMP OID
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
SNMP trap configuration
lead time before snmp trap is sent on violation
SNMP Trap hysteresis in 0.1%
Update for device module